



1. Description

1.1. Project

Project Name	STM32F030C8T6
Board Name	custom
Generated with:	STM32CubeMX 6.14.1
Date	05/10/2025

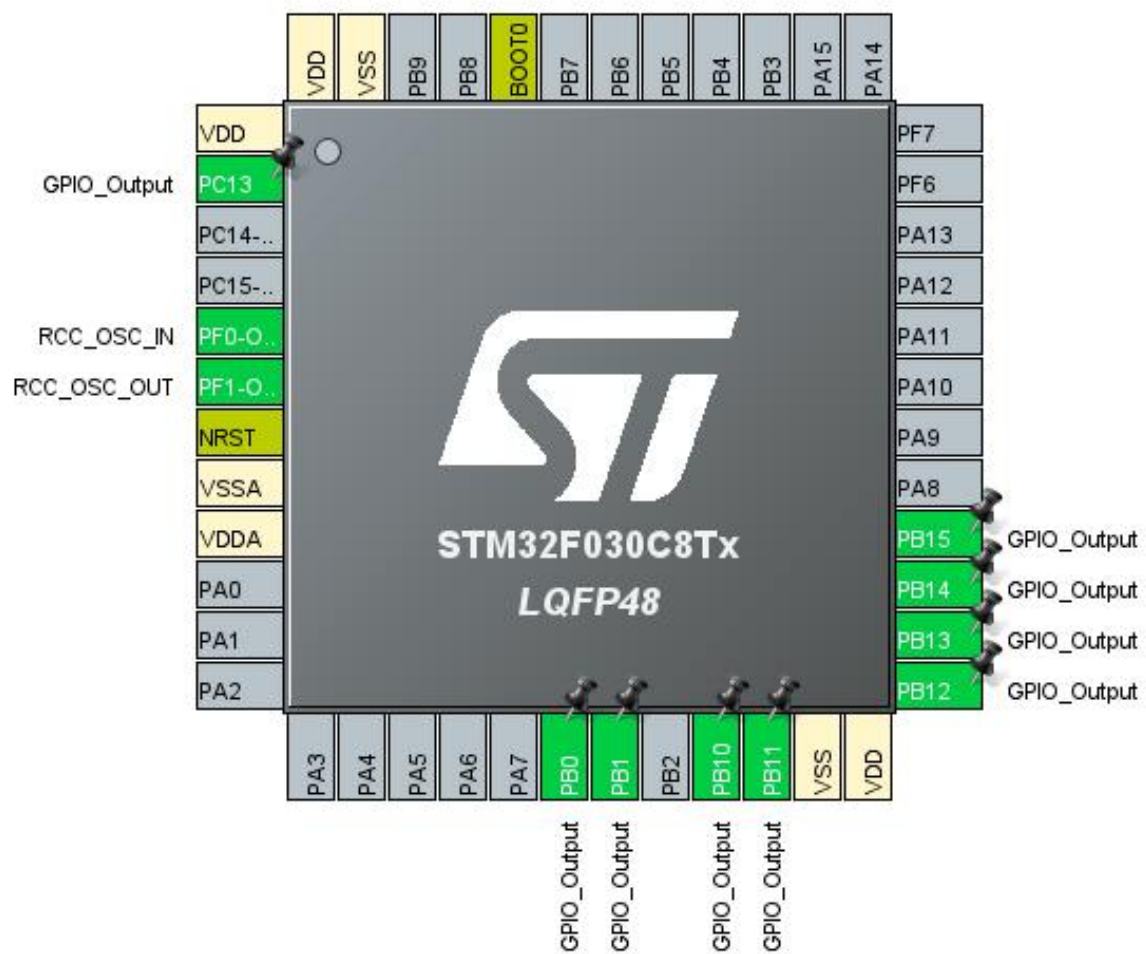
1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x0 Value Line
MCU name	STM32F030C8Tx
MCU Package	LQFP48
MCU Pin number	48

1.3. Core(s) information

Core(s)	Arm Cortex-M0
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2. Pinout Configuration



3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
2	PC13 *	I/O	GPIO_Output	
5	PF0-OSC_IN	I/O	RCC_OSC_IN	
6	PF1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
18	PB0 *	I/O	GPIO_Output	
19	PB1 *	I/O	GPIO_Output	
21	PB10 *	I/O	GPIO_Output	
22	PB11 *	I/O	GPIO_Output	
23	VSS	Power		
24	VDD	Power		
25	PB12 *	I/O	GPIO_Output	
26	PB13 *	I/O	GPIO_Output	
27	PB14 *	I/O	GPIO_Output	
28	PB15 *	I/O	GPIO_Output	
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function

1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x0 Value Line
MCU	STM32F030C8Tx
Datasheet	DS9773_Rev2

1.2. Parameter Selection

Temperature	25
Vdd	3.6

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

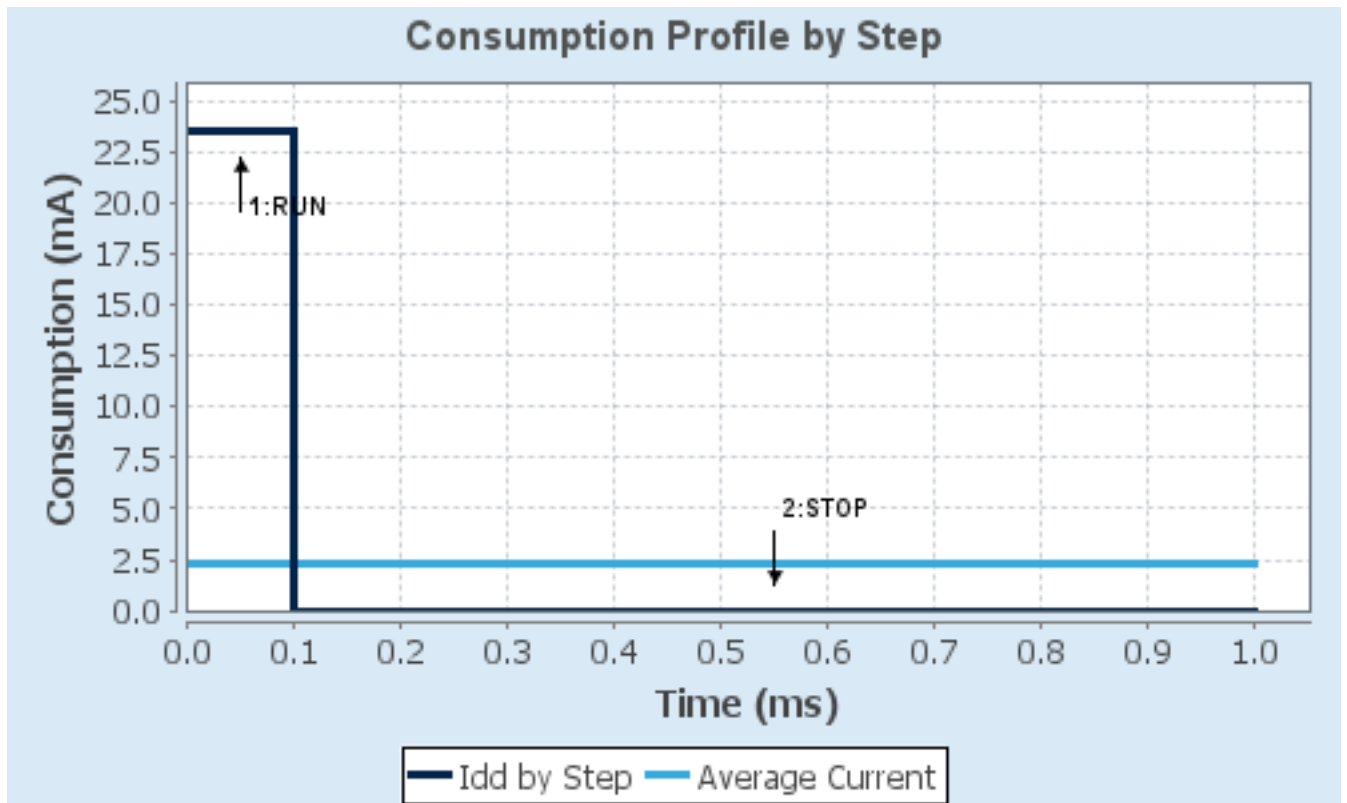
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.6	3.6
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	48 MHz	0 Hz
Clock Configuration	HSE PLL All IPs ON	Regulator LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	23.46 mA	7.9 μ A
Duration	0.1 ms	0.9 ms
DMIPS	0.0	0.0
Ta Max	100.36	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	2.35 mA
Battery Life	1 month, 29 days, 16 hours	Average DMIPS	0.0 DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	STM32F030C8T6
Project Folder	F:\EB_Projects\Project_STM32F030C8T6
Toolchain / IDE	CMake
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.5
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_TIM3_Init	TIM3
4	MX_TIM6_Init	TIM6
5	MX_TIM1_Init	TIM1

3. Peripherals and Middlewares Configuration

3.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

3.1.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

RCC Parameters:

HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

3.2. SYS

Timebase Source: SysTick

3.3. TIM1

Clock Source : Internal Clock

3.3.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	0
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	65535
Internal Clock Division (CKD)	No Division
Repetition Counter (RCR - 8 bits value)	0
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

3.4. TIM3

mode: Clock Source

3.4.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	48-1 *
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	1000-1 *
Internal Clock Division (CKD)	No Division
auto-reload preload	Enable *

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

3.5. TIM6

mode: Activated

3.5.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	48-1 *
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	100-1 *
auto-reload preload	Enable *

* User modified value

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PF0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PF1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
GPIO	PC13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	
	PB10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	
	PB11	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	
	PB12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	
	PB13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	
	PB14	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	
	PB15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Medium *	

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	3	0
TIM3 global interrupt	true	0	0
TIM6 global interrupt	true	0	0
Flash global interrupt	unused		
RCC global interrupt	unused		
TIM1 break, update, trigger and commutation interrupts	unused		
TIM1 capture compare interrupt	unused		

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
TIM3 global interrupt	false	true	true
TIM6 global interrupt	false	true	true

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

Middleware				
System Core	Analog	Timers	Connectivity	Computing
DMA		TIM1 ✓		
GPIO ✓		TIM3 ✓		
IVIC ✓		TIM6 ✓		
RCC ✓				
SYS ✓				

6. Docs & Resources

Type	Link
IBIS models	https://www.st.com/resource/en/ibis_model/stm32f0_ibis.zip
System View Description	https://www.st.com/resource/en/svd/stm32f0_svd.zip
Presentations	https://www.st.com/resource/en/product_presentation/gt_stm32f0-l0.pdf
Presentations	https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf
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Flyers	https://www.st.com/resource/en/flyer/fldpstpfc11120.pdf
Product Certifications	https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf
Application Notes	https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf
Application Notes	https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an3371-using-the-hardware-realtime-clock-rtc-in-stm32-f0-f2-f3-f4-and-l1-series-of-mcus-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an4099-implementation-of-transmitters-and-receivers-for-infrared-remote-control-protocols-with-mcus-of-the-stm32f0-and-stm32f3-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
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Errata Sheets https://www.st.com/resource/en/errata_sheet/es0219-stm32f030x4x6x8xc-device-errata-stmicroelectronics.pdf

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