COL215P: ASSIGNMENT 5

Gunjan Kumar - 2019CS10353

T Abishek - 2019CS10407

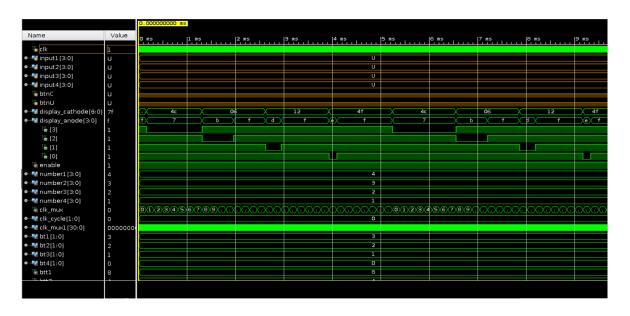
Objective:

- Using pre-designed circuit with PWM as a building block to build a design for scrolling effect.
- Controlling the brightness of the digits.

Implementation Overview:

- We use the four-digit seven-segment display implemented in Assignment 4.
- We've added two new buttons, btnC and btnU. The value on the four-digit seven-segment display is set only when btnC is pressed. The value of the brightness is set only when btnU is pressed.
- The right 16 switches are utilised to provide input for the 4 digits. Each digit corresponding to 4 switches. Similarly, the right 8 switches are utilised to provide input for the brightness level. The brightness level of each digit is represented by a two-bit value.
- To rotate the digits on the seven segment display, we use a new 28 bit counter. The order of digits to be displayed is determined by most significant two bits of this counter.

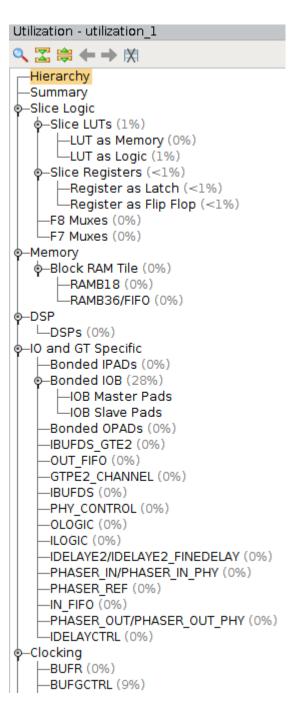
Simulation:



Resource utilisation:

| Hierarchy | | | | | | |
|-----------|-------------------------|-----------------------|----------------------------|---------------------|------------------|--|
| Q | Name 1 | Slice LUTs (20800) | Slice Registers (41600) | Bonded IOB (106) | BUFGCTRL (32) | |
| | P-N main | 111 | 61 | 30 | 3 | |
| | └2 create mux clock (ti | 89 | 29 | 0 | 0 | |

| Resource | Utilisation Count | Utilisation % | |
|-----------------|-------------------|---------------|--|
| Slice LUTs | 111 | < 1 | |
| Slice Registers | 61 | < 1 | |
| Bonded IOB | 30 | 28 | |
| BUFGCTRL | 3 | 9 | |



FPGA Observation:

To set the values, we set the digits as 1,2,3,4 from the switches and press btnC. The brightness of the digits are different and moves with them, as can be seen in the photographs.



