

KEVIN WANG

EDUCATION

BASc Computer Engineering **University of British Columbia**, Sept 2016 - Present

- › Pursuing software focused Computer Engineering degree - 3.70 GPA
- › Anticipated graduation date: **May 2022**

EXPERIENCE

SoC Emulation Engineer **Intel Vancouver**, May 2019 - Present

- › Developed C++ and SystemC software simulation models of SSD hardware designs for pre-silicon software development, HW/SW co-validation, and architectural exploration
- › Maintained Teamcity continuous-integration infrastructure for a team of 30+ developers, creating mandatory build chains and debugging critical failures on-the-spot
- › Managed a Chocolatey Windows package management system with Powershell scripts to automate package install, update, and setup for a combined 100+ developers
- › Wrote extensive documentation for numerous applications, including in-house Chocolatey patches and Teamcity agent bringup
- › Coded in a Git repository containing three submodules with a high-velocity, agile methodology

Manufacturing R&D Engineer **Tekmar Control Systems**, May 2018 - Aug 2018

- › Developed innovative mechanical, electrical, and software solutions to increase manufacturing efficiency and lower production costs
- › Brainstormed and manufactured a modular cartridge style clamping test stand which cut manufacturing time down two-thirds and test stand costs down to one-tenth of original cost
- › Designed and built a unique slide style test in stand to solve electrical contact alignment issues with OEM product line
- › Created action plan to fully automate temperature sensor product line in our manufacturing plant and negotiated equipment deals with Chinese equipment manufacturers
- › Redesigning LCD enclosures in SolidWorks to solve major alignment issue affecting user interface for boiler control product line
- › Ported older PCB products to accommodate new enclosures and FCC electromagnetic certification requirements

DESIGN TEAM

Electrical & Power Systems Lead **UBC Orbit**, Jan 2016 - Sept 2018

- › Handled power management and electronic systems design for UBC's micro-satellite design team, becoming lead in the end of 2017
- › Developed I2C communication interface in C to retrieve telemetry from and communicate with power system and battery modules
- › Simulated satellite flight paths in Systems Tool Kit, parsing communication access and telemetry data for a two year period using MATLAB
- › Modeled satellite power consumption with a state machine and created a power simulation model of mission lifetime using Matlab
- › Created block diagram electrical routing and power rationing for all satellite subsystems
- › Managed ongoing project deadlines and distributed tasks to team members

CONTACT

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SOFTWARE

- </> C, C++, & System C
- </> Assembly (8051)
- </> Java
- </> Gradle
- </> Python
- </> Powershell Scripting
- </> Unix Environments
- </> HTML & CSS
- </> JSON
- </> NodeJS
- 🔗 Git
- ⚡ Arduino & Raspberry Pi

MODELING TOOLS

- ⚡ Altium Designer
- ⚙ SolidWorks
- ⚡ NI Multisim
- ⚡ Quartus Prime

MANUFACTURING

- ⚡ PCB Assembly
- ⚙ 3D Printing
- ⚙ CNC Machining

HOBBIES

- 🎮 Competitive Gaming (Ask Me!)
- 📺 Farming
- 🎹 Piano
- 🏸 Badminton