



Project 5A: Logic Simulation

一、 apex4.blif

stimuli: apex4.sti

```
NTUST > digitalLogicDesign > Project05A >  apex4.sti
1 .i 9
2 111111001
3 111111100
4 111010000
5 110110000
6 111111001
7 000000011
8 101010011
9 000111010
10 100100000
11 011001110
12 110110100
13 101000000
14 001000101
15 100000000
16 110111111
17 110010110
18 001011110
19 111110010
20 011100010
21 000000010
22 010110010
23 100100110
24 010110100
25 011110111
26 110000011
27 101000111
28 111010011
29 010011001
30 010011011
31 .e
```

responses: apex4.res

```
NTUST > digitalLogicDesign > Project05A >  apex4.res
1 .i 9
2 .o 19
3 111111001 0010000000101100000
4 111111100 0010001100000000000
5 111010000 0100000000000110000
6 110110000 000111111101100000
7 111111001 0010000000101100000
8 000000011 0000000000011111111
9 101010011 0010000000100000000
10 000111010 0000000000011111111
11 100100000 0101110000011000000
12 011001110 0100000001000110000
13 110110100 0001000110011000000
14 101000000 0010001010101100000
15 001000101 0000000011011101110
16 100000000 0001111111110010000
17 110111111 0110000000000000000
18 110010110 0010001110110010000
19 001011110 0000001010100000000
20 111110010 0010011100000000000
21 011100010 0010100101010010000
22 000000010 0000000000011111111
23 010110010 0011011111000000000
24 100100110 0101000001010010000
25 010110100 0010111100100000000
26 011110111 0010101111001100000
27 110000011 0000001000000000000
28 101000111 0010001110100000000
29 111010011 0000010011000000000
30 010011001 0000000010010010000
31 010011011 0000000010000000000
32 .e
```

二、 ex1010.blif

stimuli: ex1010.sti

```
NTUST > digitalLogicDesign > Project05A > ex1010.sti
1 .i 10
2 1011110010
3 0110110111
4 0111010100
5 1110101001
6 1111010100
7 0011101011
8 0100011110
9 0001101001
10 0011101010
11 1101011011
12 1101100010
13 0001101010
14 0100101110
15 0011011111
16 1000101101
17 1000010100
18 0101010011
19 0010011001
20 0101111000
21 0100011110
22 0011000101
23 .e
```

responses: ex1010.res

```
NTUST > digitalLogicDesign > Project05A > ex1010.res
1 .i 10
2 .o 10
3 1011110010 1000100010
4 0110110111 0000000100
5 0111010100 0000000001
6 1110101001 0000000000
7 1111010100 0000001010
8 0011101011 1000000010
9 0100011110 0100000000
10 0001101001 0000001100
11 0011101010 0000000100
12 1101011011 0100011000
13 1101100010 0000000000
14 0001101010 0001000000
15 0100101110 0001001100
16 0011011111 0000000001
17 1000101101 0100101010
18 1000010100 0000110100
19 0101010011 0000100110
20 0010011001 1100100100
21 0101111000 0000001100
22 0100011110 0100000000
23 0011000101 0001000000
24 .e
```

三、 inc.blif

stimuli: inc.sti

```
NTUST > digitalLogicDesign > Project05A > inc.sti
1 .i 7
2 0000010
3 0010001
4 0110100
5 0001001
6 1111001
7 0101010
8 1101110
9 0001100
10 0111110
11 0100100
12 1000101
13 1011110
14 0110011
15 0100111
16 0011000
17 1010001
18 0001001
19 .e
```

responses: inc.res

```
NTUST > digitalLogicDesign > Project05A > inc.res
1 .i 7
2 .o 9
3 0000010 000001000
4 0010001 001110000
5 0110100 011010101
6 0001001 000110000
7 1111001 000000000
8 0101010 100100001
9 1101110 000000000
10 0001100 000110000
11 0111110 100000000
12 0100100 010010001
13 1000101 101000010
14 1011110 110000000
15 0110011 010110001
16 0100111 101000011
17 0011000 001110000
18 1010001 101001000
19 0001001 000110000
20 .e
```

四、 misex2.blif

stimuli: misex2.sti

```
NTUST > digitalLogicDesign > Project05A > misex2.sti
1 .i 25
2 1100010110100010110011100
3 0011110110010001111011111
4 0001111001001100010011011
5 0001100110100100010100110
6 0101100001101110010001011
7 1010010010101110100001011
8 0111010001001010001011111
9 0010101010101100100011101
10 1100011100001111100111010
11 0111010000111110011110000
12 1000110110101100011011000
13 0011111101101011111010011
14 0101100010100001000001001
15 1101101110001111100011011
16 1111000101010101000011111
17 1010001011110000110000000
18 1010111110000000010100100
19 1110100000110001011101111
20 0001000000000100001011101
21 1101100010010100000011110
22 0000101100010001100011100
23 1000101101110100111110111
24 1100100001100110111101111
25 0111011110100001111000000
26 0000011100000111110000000
27 1010100100010010101110011
28 0100111001111100100110110
29 1111011010111001001101001
30 1110111001100001001110010
31 111101111110101111011100
32 1101001111100010011101001
33 1011010000000011111001111
33 1011010000000011111001111
34 0011010101111101110001101
35 1111001000101110101000100
36 1100110110101000000110101
37 1000110111101011111011000
38 1001011011011101001010000
39 0100000111110010010010110
40 1011100010011000010110101
41 010110111101100110001011
42 1011011000110000101000110
43 0100011001000100110001110
44 1001110101011000100011110
45 0000111011010001101010000
46 .e
```

responses: misex2.res

```
NTUST > digitalLogicDesign > Project05A > misex2.res
1 .i 25
2 .o 18
3 1100010110100010110011100 0000000000000000100
4 0011110110010001111011111 0000000000000000001
5 0001111001001100010011011 0000000000000010000
6 0001100110100100010100110 0000000000000000100
7 0101100001101110010001011 0000000000000000010
8 1010010010101110100001011 0000000100000000000
9 0111010001001010001011111 0000000000000000010
10 0010101010101100100011101 0000000000000000001
11 1100011100001111100111010 0000000000000000100
12 0111010000111110011110000 0000000000000000010
13 1000110110101100011011000 0000000000000000100
14 0011111101101011111010011 0000000000000000001
15 0101100010100001000001001 0000000000000000010
16 1101101110001111100011011 0000000000000000100
17 1111000101010101000011111 0000000000000000100
18 1010001011110000110000000 0000000100000000000
19 10101111100000000010100100 0000000100000000000
20 1110100000110001011101111 0000000110000000000
21 0001000000000100001011101 0001000000000000000
22 1101100010010100000011110 0000000000000000100
23 0000101100010001100011100 00000000000000001000
24 1000101101110100111110111 00000000000000000100
25 1100100001100110111101111 00000000000000000100
26 0111011110100001111000000 0000000001000000010
27 0000011100000111110000000 000000000000010000
28 1010100100010010101110011 0000000010000000000
29 0100111001111100100110110 0000000000000000010
30 1111011010111001001101001 0001001000000000000
31 1110111001100001001110010 0000010000000000000
32 1111011111101011110101100 0000010000000000000
33 1101001111100010011101001 0000000000000000100
33 1101001111100010011101001 0000000000000000100
34 1011010000000011111001111 0000000100000000000
35 0011010101111101110001101 0000000000000000001
36 1111001000101110101000100 0000000000000000000
37 1100110110101000000110101 00000000000000000100
38 1000110111101011111011000 00000000000000000100
39 1001011011011101001010000 00000000000000000100
40 0100000111110010010010110 0000000000000000010
41 1011100010011000010110101 0000000010000000000
42 0101110111101100110001011 0000000000000000010
43 1011011000110000101000110 0000000100000000000
44 0100011001000100110001110 0000000000000000010
45 1001110101011000100011110 0000000000000000100
46 0000111011010001101010000 0001000000000000000
47 .e
```

