# Make Accelerator Pluggable for Container Engine

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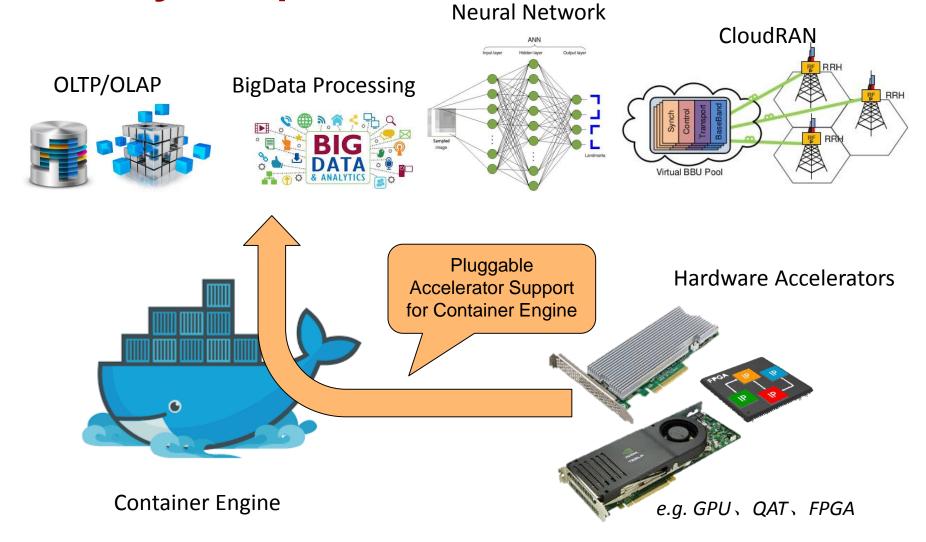
Author/ Email: Jiuyue Ma / majiuyue@huawei.com

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### **Today's Topic**



### **Agenda**

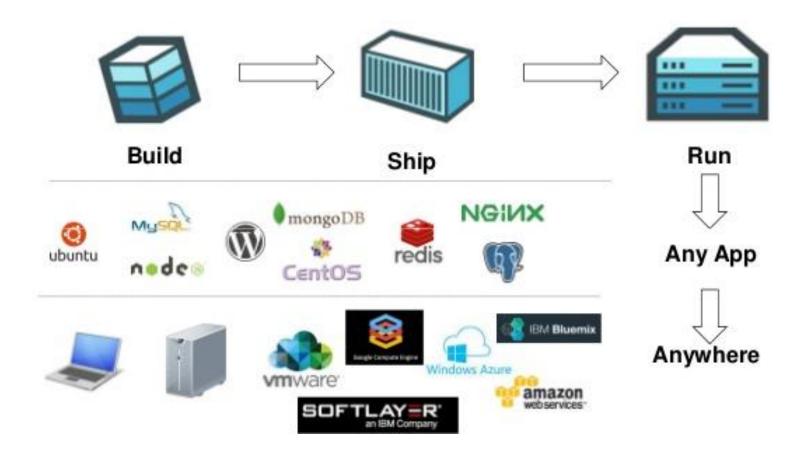
- Use accelerator in containers and How?
- Key problems to deal with
  - Identify accelerator requirements
  - Prepare accelerator runtimes
  - Manage accelerator resources
- Further development



## Why Container?



## Why Container? Why NOT Container?



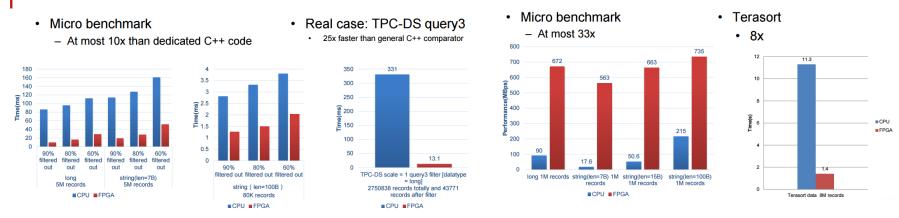
<sup>\*</sup> Raphael Da Silva. OPEN SOURCE TECHNOLOGY: Docker Containers on IBM Bluemix. 2015.



## Why Accelerator?

CPU is not fast/effective/... enough

Evaluation - filter [baidu, HotChips 2016]

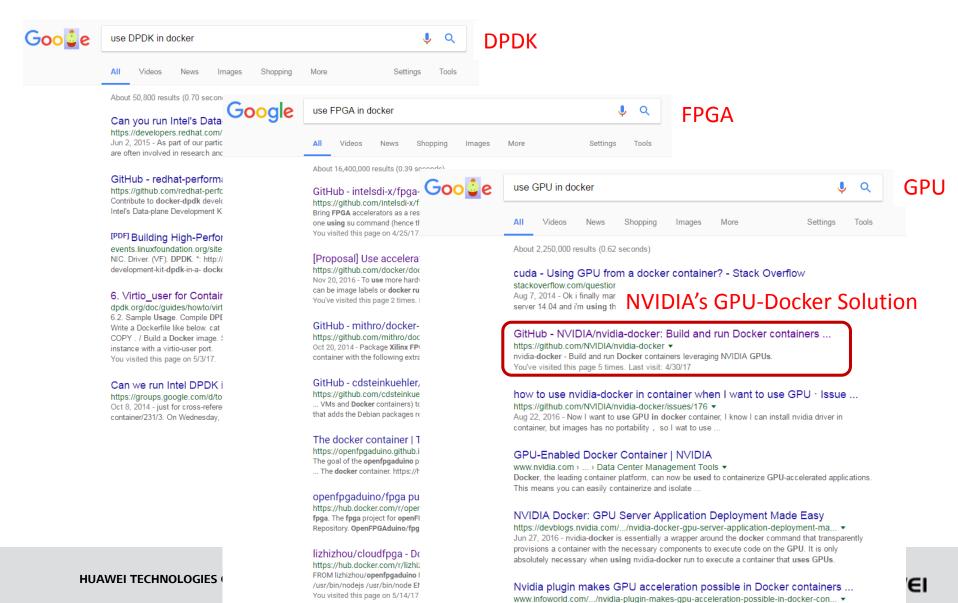


- Heterogeneous is the future
  - Nvidia GPU-compute, Intel Skylake Xeon+FPGA, AWS P2/F1 Instance
  - use the right processor, in the right place, at the right time

Container a.k.a. Docker

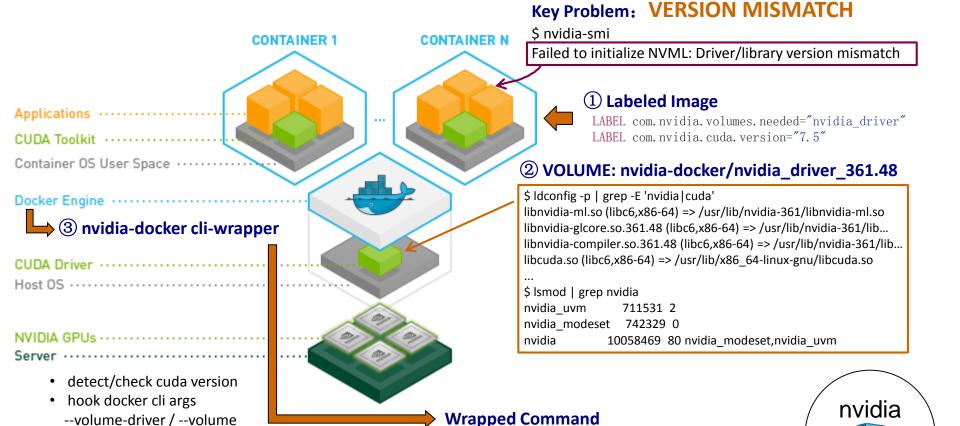


### Google it! "Use XXX in docker"



Jun 29, 2016 - The Docker images that use the GPU have to be built against Nvidia's CUDA toolkit.

#### Use GPU in Docker (from nvidia-docker)



docker run

--volume-driver=nvidia-docker



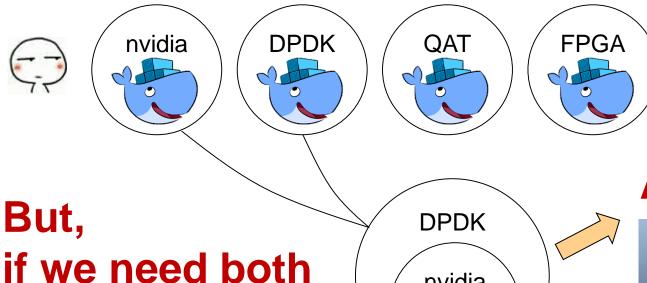
--volume=nvidia driver 361.48:/usr/local/nvidia:ro

#### Do we really need "nvidia-docker"?

If so, we also need:

dpdk-docker, qat-docker, fpga-docker, ...

nvidia



**Pluggable Accelerator** 

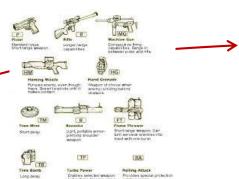


**GPU and DPDK?** 

### Pluggable Accelerator, How?

• What's the difference? **FPGA Accel DPDK FPGA** nvidia /dev/nvidia0 **Device Files** /dev/uio0 /dev/fpga0 /dev/nvidia-smi (-d device) /dev/nvidia-uvm /usr/lib/nvidia-361/libnvidia-ml.so File Bindings /usr/lib/nvidia-361/libnvidia-glcore.so.361.48 /usr/lib/libfpga.so /usr/lib/nvidia-361/libnvidia-/sys/bus/pci/devices/xxxx:xx:xx.x /sys/bus/pci/devices/xxxx:xx:xx.x (-v src:dest) compiler.so.361.48 /dev/hugepages /dev/hugepages /usr/lib/x86\_64-linux-gnu/libcuda.so **Environments** PATH=\$PATH:/usr/lib/nvidia-361/bin LD\_LIBRARY\_PATH=... (-e env)

Accelerator Plugin Interface







in-house

### Implementation (1/3): IDENTIFY

#### Accelerator Requirements

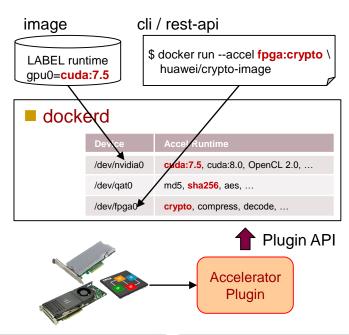
- We are using the function provided by device, not the device itself
- Identified by runtime instead of device
  - e.g. "A SHA256 accelerator" instead of "A FPGA with XXXX-bitstream"

#### Identify user requirements

- Integrate into image
- Specify when start container

#### Identify device capabilities

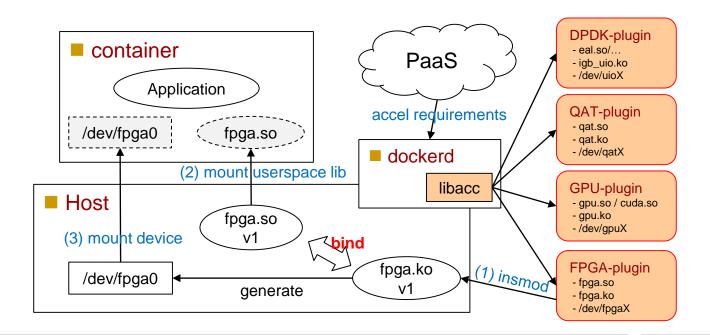
- Identified by "accelerator plugin"
- Report to engine through plugin api





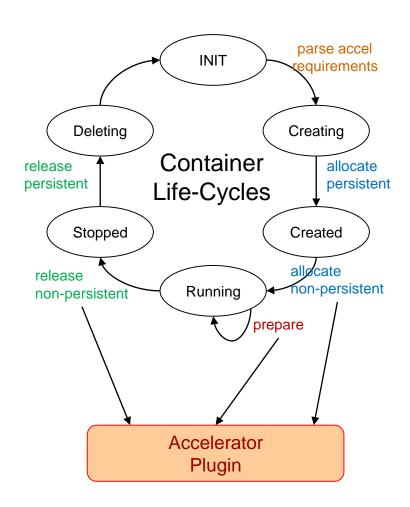
### Implementation (2/3): PREPARE

- Select plugin from accelerator requirements
- Device initialization by plugin
- Dynamic inject required libraries & devices



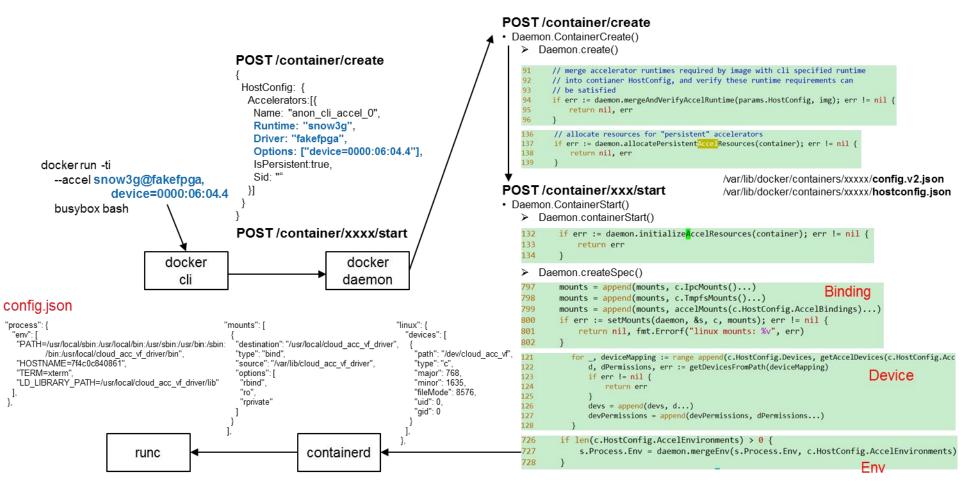
### Implementation (3/3): MANAGEMENT

- Accelerator Life-Cycle
  - □ Allocate → Prepare → Release
  - Plugin hooks for each stage
- Integrate with Container Life-Cycle
  - □ Persistent: Create → Delete
  - Non-persistent: Start→Stop





#### **Example Flow**



#### **Further Developments**

Standardize "Accelerator Runtime"



- OCI Runtime/Image Spec should be good place
- Integrate with Docker Swarm / K8S / etc
  - Cluster level accelerator schedule&management





- More Features
  - Accelerator Sharing
  - Accelerator Exception Interface
  - Accelerator Hot-Plug
  - ...





#### Thank you

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