**Ve270 Introduction to Logic Design Homework 1**

**Assigned: September 13, 2018**

**Due: September 20, 2018, 4:00pm.**

**The homework should be submitted in hard copies.**

1. Fill out the blank spaces, assuming unsigned numbers. Show steps to earn partial credits. (8 points)

1101101.101 2 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_10 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_16

78.39 10 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_2 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_8 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_3

1. Fill out the blank spaces, assuming 2’s complement numbers. (16 points)

-3310 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_2 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_16

3310 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_2 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_16

101101001012 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_10

F35816= \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_2 = \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_10

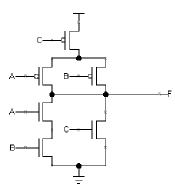
1. Perform the following arithmetic operations step by step, assuming signed numbers: (12 points)

(6FE58C + A3DD) 16 =

(11100 – 10001111) 2 =

(532 – 265) 8 =

1. Problem 2.14 (Boolean equation = logic equation) (4 points)
2. Problem 2.15 (10 points)
3. Problem 2.18 (c) (10 points)
4. Problem 2.22 (10 points)
5. Problem 2.35 (c) (d) (10 points)
6. Build a truth table for the following circuit. (10 points)



Vdd

1. Given a logic equation F = a’c + b’c’ + ab, draw an output waveform for F based on the given input waveforms. (10 points)



a

F

c

b