

**भारतीय सूचना प्रौद्योगिकी संस्थान कोटा**  
**INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTA**

**B.Tech. (CSE& ECE)**  
**End Term Examination, Odd Semester 2023-24**

**Computer Architecture and Organization (CST201)**

**Marks: 40**

**Time: 120 minutes**

**Date: December 15, 2023**

**All Questions are Compulsory.**

- ✗ Q1. Design a Datapath for 5 stage pipelines (IF, ID, IE, DM and WB) for given instructions I1 and I2.  
I1: ADD R1, R2, R3, I2: SW R1, 8(R4).  
Find out the number of stall cycles with and without operator forwarding/bypassing. Calculate the effective CPI.
- ✗ Q2. Calculate the no of stall cycles as well as effective CPI for the following set of Instructions with and without bypassing in pipeline architecture.  
A. I1: ADD \$1, \$2, \$3, I2: LW \$4, 8(\$1)  
B. I1: LW \$1, 8(\$2), I2: LW \$4, 8(\$1)
- ✗ Q3. Why branch delay slot is created and give a suitable example to reduce the delay slot cycle.
- ✗ Q4. A super scalar processor has different pipeline stages for the different functional unit (ALU). Create a scenario for the out-of-order execution of instructions. List the possible solution of OOO executions.
- ✗ Q5. Why cache is faster? explain the 3C's principal of the cache memory miss. How cache miss can be reduced.
- ✗ Q6. A system has main memory size 32 GB, cache size 512 KB and block size =1KB. Find out the tag bit and Tag directory size for the following mapping algorithms.  
1. Direct map  
2. Set associative when cache is 2-way set associative.
- ✗ Q7. Define the following Terms:  
A. CISC Vs RISC  
B. Hit vs Miss Rate  
C. CPU performance vs Power Budget
- ✓ Q8. Explain the performance evaluation of multi-core architecture.

# INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTA

B.Tech. (CSE), Semester – III

End Term Examination, Odd Semester 2023-24

DBMS (CST203)

Marks: 50

Time: 2 Hrs

Date: Dec. 13 2023

Note 1: Take suitable assumptions, if needed anywhere. Clearly write them.

2. All questions are compulsory.
3. Missing parameters or values may be assumed.

Q 1	<p>(a) Consider the following COMPANY database</p> <p>EMP(Name,SSN,Salary,SuperSSN,Gender,Dno)  DEPT(DNum,Dname,MgrSSN,Dno)  DEPT_LOC(Dnum,Dlocation)  DEPENDENT(ESSN,Dep_name,Sex)  WORKS_ON(ESSN,Pno,Hours)  PROJECT(Pname,Pnumber,Plocation,Dnum)</p> <p>Write the relational algebra queries for the following</p> <p>(i) Retrieve the name, address, salary of employees who work for the Research department.</p> <p>(ii) find the names of employees who work on all projects controlled by department number 4.</p> <p>(iii) Retrieve the SSN of all employees who either in department no :4 or directly supervise an employee who work in department number :4</p> <p>(iv) Retrieve the names of employees who have no dependents</p> <p>(v) Retrieve each department number, the number of employees in the department and their average salary.</p>	5 M
(b)	<p>Consider the following relation schema</p> <p>Works(Pname,Cname,salary)  Lives:(Pname,Street,City)  located_in (Cname, city)  Manager(Pname,Mgrname) Write</p> <p>the SQL queries for the following</p> <p>i) Find the names of all persons who live in the city Bangalore.</p> <p>ii) Retrieve the names of all person of "Infosys" whose salary is between Rs .50000 and 60000</p> <p>iii) Find the names of all persons who lives and work in the same city</p> <p>iii) List the names of the people who work for "Tech M" along with the cities they live in.</p> <p>(v) Find the average salary of "infosys" persons .</p>	6 M



<p>Q 2</p> <p>Only one</p>	<p>(a) Consider the following COMPANY database</p> <p>EMP(Name,SSN,Salary,SuperSSN,Dno)</p> <p>DEPT(DNum,Dname,MgrSSN,Dno)</p> <p>DEPT_LOC(Dnum,Dlocation)</p> <p>DEPENDENT(ESSN,Dep_name,Sex)</p> <p>WORKS_ON(ESSN,Pno,Hours)</p> <p>PROJECT(Pname,Pnumber,Plocation,Dnum)</p> <p>Write the SQL queries for the following</p> <p>i) Retrieve the name of the employee who works with same department as ravi</p> <p>ii) Retrieve the number of dependents for an employee "Ravi"</p> <p>iii) Retrieve the name of the managers working in location "DELHI" who has no female dependents</p> <p>iv) List female employees from Dno=20 earning more than 50000</p> <p>v) List "CSE" department details</p>	<p>5M</p>
	<p>(b) Consider the relation schema R(A,B,C,D,E,F) and the functional dependencies <math>A \rightarrow B, C \rightarrow DE, AC \rightarrow E, D \rightarrow</math> What is the primary key of this relation R? What is its highest normal form? Preserving the dependency, decompose R into third normal form.</p> <p><math>D \rightarrow F</math> <math>(A,B,C,D,E) \rightarrow (D,F)</math></p>	<p>5 M</p>

<p>Q 3</p> <p>only one</p>	<p>(a) Write the algorithm to find the minimal cover for a sets of FD's</p> <p>Consider <math>R = \{A,B,C,D,E,H\}</math> FD's <math>\{A \rightarrow C, AC \rightarrow D, E \rightarrow AD, E \rightarrow H\}</math></p> <p>irreducible cover for this set of FD's (minimal cover) <math>A \rightarrow CD, E \rightarrow AH</math></p>	<p>5 M</p>
<p>Q 4</p>	<p>(b) Given below are two sets of FD's for a relation R(A,B,C,D,E,H) Are they equivalent?</p> <p><math>F = \{A \rightarrow C, AC \rightarrow D, E \rightarrow AD, E \rightarrow H\}</math> and <math>G = \{A \rightarrow CD, E \rightarrow AH\}</math></p>	<p>5M</p>
<p>Q 5</p>	<p>(a) List and explain ACID Properties</p>	<p>2 M</p>
<p>Q 5</p>	<p>(b) Check whether given schedule is serializable or not using precedence graph. Explain with algorithm.</p> <p><math>T1 \rightarrow T2 \quad T3 \rightarrow T2 \quad T2 \rightarrow T1</math></p> <p><math>S1: R1(X) \quad R2(Z) \quad R1(Z) \quad R3(X) \quad R3(Y) \quad W1(X) \quad W3(Y) \quad R2(Y)</math></p> <p><math>W2(Z) \quad W2(Y)</math> <i>Conflict Serializable</i></p>	<p>5 M</p>
<p>Q 5</p>	<p>(a) Explain why concurrency control is required?</p>	<p>4 M</p>
<p>Q 5</p>	<p>(b) Write short notes on the following</p> <p>I. transaction rollback and cascading rollback.</p> <p>II. shadow paging</p> <p>III. NO-UNDO/REDO Recovery Based on Deferred Update</p> <p>IV. Recovery Techniques Based on Immediate Update</p>	<p>8 M</p>

# भारतीय सूचना प्रौद्योगिकी संस्थान कोटा

INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTA

B.Tech. (CSE), Semester – III

End Term Examination, Odd Semester 2023-24

Subject: Discrete Mathematical Structures (MAT201)

Marks: 40

Time: 02 Hours

Date: 14 Dec. 2023

Attempt all the questions.

1	a) How many nonnegative integer solutions to the equation $x_1 + x_2 + x_3 + x_4 + x_5 + x_6 = 10$ .	2
	b) Determine the coefficient of $x^{15}$ in $f(x) = (x^3 + x^4 + x^5 + \dots)^3$ .	3
2	a) Let $G$ be a group and $H_1, H_2$ are two subgroups of $G$ . Then, prove or disprove that $H_1 \cup H_2$ is always a subgroup of $G$ .	3
	b) Show that a finite cyclic group of order $n$ is isomorphic to $\mathbb{Z}_n$ , the group of integers modulo $n$ .	4
	c) Verify that $U_{14}$ is a cyclic group or not, under the multiplication modulo 14, where $U_{14}$ is the set of integers less than 14 and relatively prime to 14.	3
3	a) Consider a function $f: \mathbb{R} \rightarrow \mathbb{R}$ such that $f(x) = e^{2\sin x + 3\cos x + 5}$ . Is it one-one and onto function?	2
	b) A deposit of \$100000 is made to an investment fund at the beginning of a year. On the last day of each year two dividends are awarded. The first dividend is 20% of the amount in the account during that year. The second dividend is 45% of the amount in the account in the previous year. (i) Find the recurrence relation for $\{P_n\}$ , where $P_n$ is the amount in the account at the end of $n$ years if no money is ever withdrawn? (ii) How much is in the account after $n$ years if no money has been withdrawn?	1+3



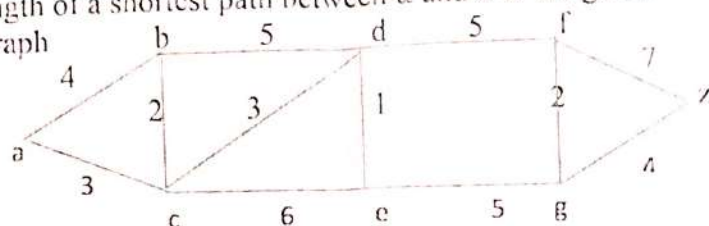
a) Can a simple graph exist with 25 vertices each of degree five?

b) Draw a graph with the adjacency matrix

$$\begin{bmatrix} 0 & 3 & 0 & 2 \\ 3 & 0 & 1 & 1 \\ 0 & 1 & 1 & 2 \\ 2 & 1 & 2 & 0 \end{bmatrix}$$

c) What is the sum of the entries in a column of the incidence matrix for an undirected graph?

d) Find the length of a shortest path between  $a$  and  $z$  in the given weighted graph



5. a) Write a logical argument for the given statement using predicates, quantifiers and logical connectives. "All clear explanations are satisfactory. Some excuses are unsatisfactory. Therefore, some excuses are not clear explanations" (assuming that the domain of discourse consists of all English text).

b) Prove that, for all positive integer  $n$ ,

$$\frac{1}{\sqrt{1}} + \frac{1}{\sqrt{2}} + \frac{1}{\sqrt{3}} + \dots + \frac{1}{\sqrt{n}} > 2(\sqrt{n+1} - 1).$$

6. a) Find the number of positive integers not exceeding 1000 that are either the square or the cube of an integer.

b) Suppose that  $f: A \rightarrow A$  be a one-one function. Then show that  $f^n: A \rightarrow A$  is a one-one function for all integers  $n \geq 1$ .

c) Let  $A$  and  $B$  be two sets. Prove that

$$A \Delta B = (A - B) \cup (B - A) = (A \cup B) - (A \cap B).$$



# Indian Institute of Information Technology Kota

## Department of CSE

### ECT211- Microprocessors and Microcontrollers

### II Year III Semester End Semester Examination-2023



Time: 120 Minutes

Date: 12/12/2023

Max. Marks: 40

Note: Attempt all questions.

Q. 1 Differentiate between Interrupts and Exceptions? Classify the various types exceptions and how are they handled in ARM processors? Write Software interrupt handler execution steps. [5 Marks]

Q.2 Define CPSR and SPSR. What is the primary purpose of these registers in the ARM architecture? Explain the key flags present in the CPSR. How are these flags used to represent the current state of the processor during program execution? [5 Marks]

Q.3 Discuss the concept of byte, half word, and word operations in ARM load and store instructions. How does ARM handle different data sizes during memory access? Define big endian and little endian. How do they differ in terms of storing multi-byte values in memory? [5 Marks]

Q.4 Explain the key components of the AMBA architecture. What are the major buses defined by AMBA, and what roles do they play in a typical SoC design? [5 Marks]

Q.5 Define the Thumb instruction set. How does it differ from the regular ARM instruction set in terms of instruction length and encoding? [5 Marks]

Q.6 Describe the basic formats of ARM instructions. How are ARM instructions encoded, and what are the key fields in an instruction format?

OR

✓ Discuss the data processing instructions in ARM. What are the fundamental operations that can be performed on data, and how are these instructions structured? [5 Marks]

Q.7 Describe the IEEE 754 standard for floating-point representation. What are the formats and characteristics of half precision, single precision and double precision representations? Represent the following values in half-precision, single-precision, and double-precision.

- a. 1.5
- b. 3.0
- c. -4.5
- d. -0.46875

[5 Marks]

Q.8 (a) Compare the pipeline depth of ARM processors with a 3-stage architecture to those with a 5-stage architecture.

Q.8 (b) Describe the basic operating modes in ARM architecture. What is the purpose of each operating mode, and how do they differ? [5 Marks]



INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTA  
Mid Term Examination, Autumn Semester 2023-24

B. Tech. Computer Science and Engineering (III Semester)

Marks: 40

CST 205 – Software Engineering

Time: 120 mins

Compulsory Question

1. Draw the Class and Object Diagrams for the Student Grading System for a Semester. 10M

Answer any 3 Questions

2. A. Compare characteristics of Agile and Spiral Process models. Discuss the pros and cons for each. 5M  
B. Explain User Stories and Test-first development 5M
3. A. Discuss the importance of Reuse in Implementation of a software 5M  
B. Discuss the Architectural Design decisions 5M
4. A. Discuss the three methodologies that led to the development of UML. 4M  
B. Discuss the things that form the building blocks of UML. 6M
5. A. Discuss the Use Case, Sequence and Activity Diagrams with examples for few sub modules of a Student Grading System for a Semester. 6M  
B. Discuss about Collaboration Diagrams and compare them with Sequence Diagrams 4M
6. A. Explain Release Testing and User Testing and Compare them. 4M  
B. Draw a neat diagram for Software Testing Process. Explain three testing stages in the Software Testing in detail. 6M