LOGIC GATE LABS

Instructions on wiring logic gates

OVERVIEW

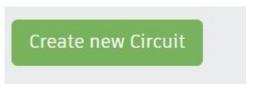
- 1. <u>Tinkercad Site Login Instructions</u>
- 2. AND GATE Pin Layout
- 3. <u>Wiring an AND Gate</u>
- 4. <u>Other Gates</u>

LOGIN INSTRUCTIONS

- Go to https://www.tinkercad.com/login
- Use the account you used just before the March
 Break when you registered and created your profile.
- Make sure that circuits is selected

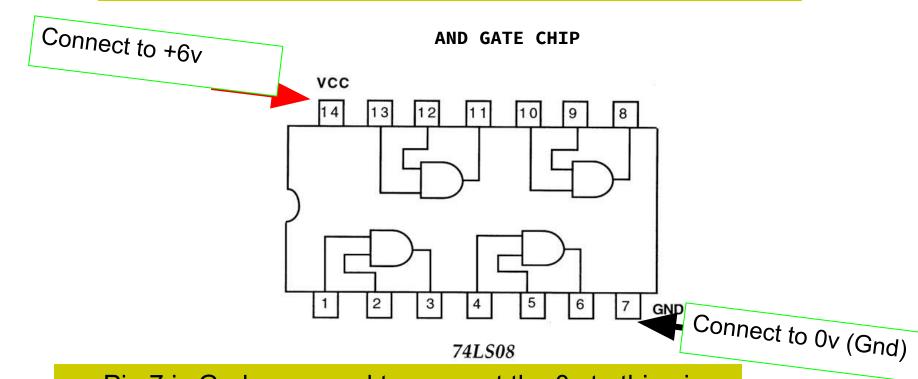


To begin the lab assignments click on



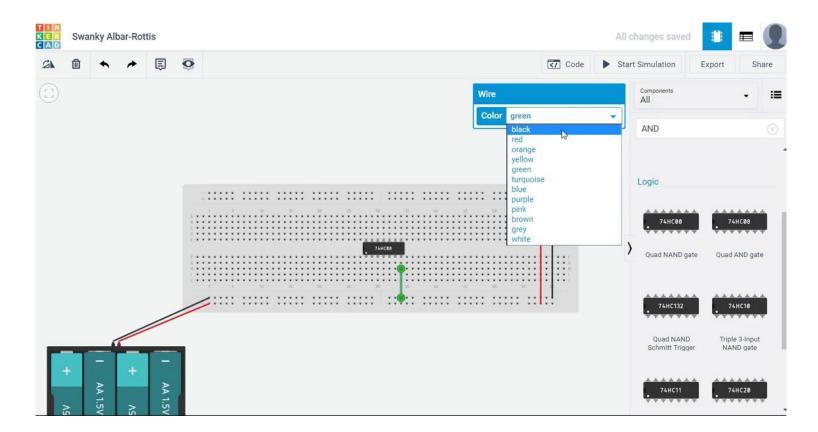
POWER

- All chips need power
- Pin 14 is VCC you need to connect 5v to this pin



Pin 7 is Gnd, you need to connect the 0v to this pin

INITIAL SETUP



WIRING THE AND GATE

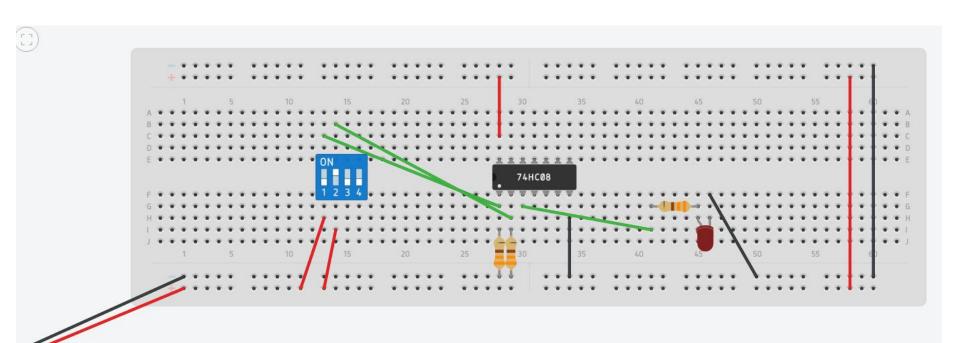


For more information on DIP switches, go here: https://www.computerhope.com/jargon/d/dipswitc.htm

WIRING THE LED AND TESTING THE CIRCUIT



FINAL CIRCUIT





OTHER GATES

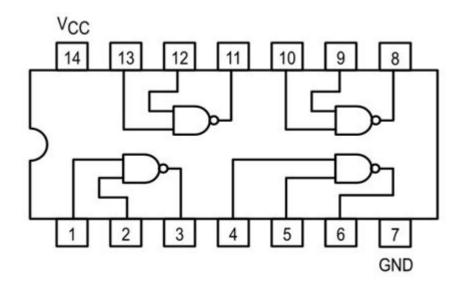
NAND gate (S00)

OR Gate (S32)

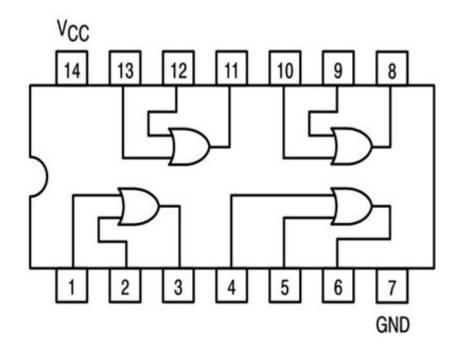
NOR Gate (S02)

XOR gate (S86)

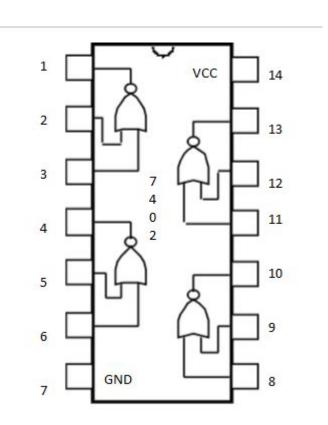
NAND GATE



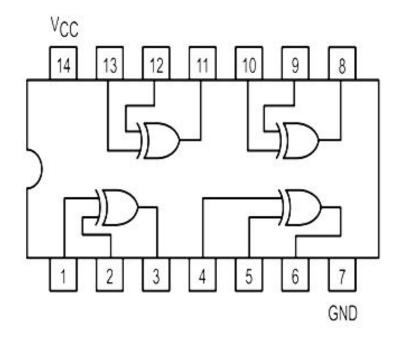
OR GATE



NOR GATE



XOR GATE



THIS WEEK...

- Work on new lab
- Other logic gate labs will be posted this week but for now, I will only post 1
- Finish up labs from last week if you are not already finished.
- We will be doing a summative based on this week's work so make sure you keep up and ask questions if you having difficulty.