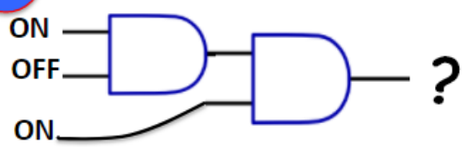
Logic Gate Worksheet - TEJ4M Name:

1.



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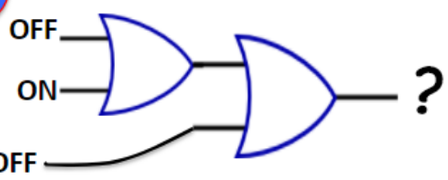

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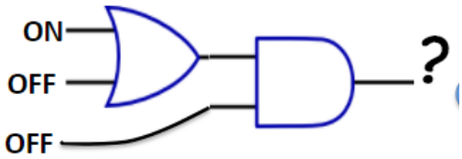

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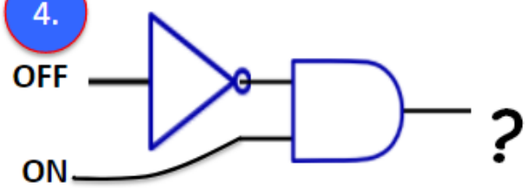

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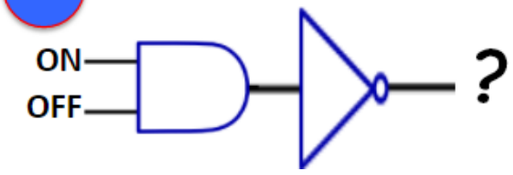
6.



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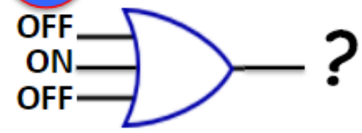

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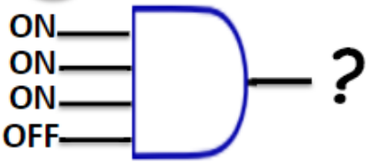
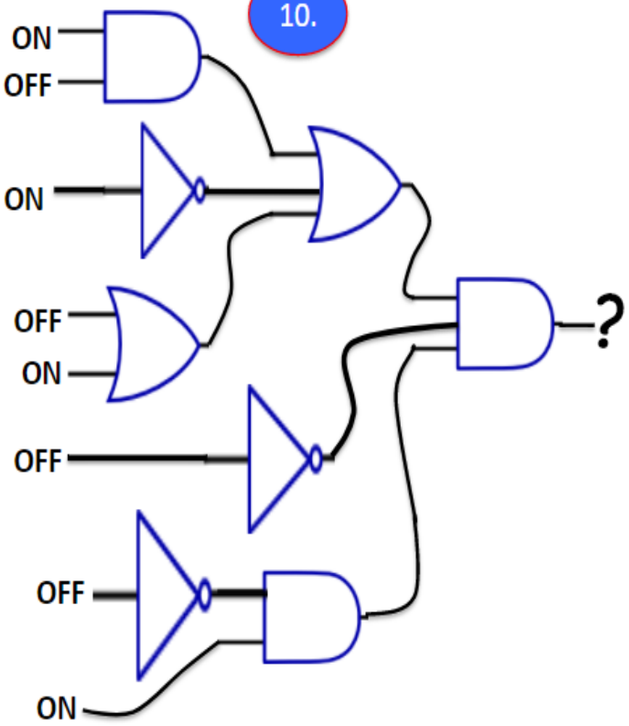
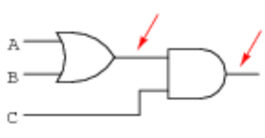


9.

10.



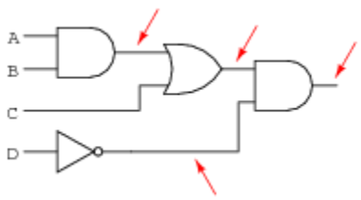
11. Convert the following logic gate circuit into a Boolean equation, writing Boolean sub-expressions next to each gate output in the diagram:



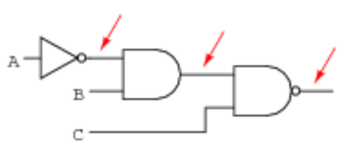
Logic Gate Worksheet - TEJ4M

Name:

12. Convert the following logic gate circuit into a Boolean equation, writing Boolean sub-expressions next to each gate output in the diagram:



13. Convert the following logic gate circuit into a Boolean equation, writing Boolean sub-expressions next to each gate output in the diagram:



14. Draw the following logic gate circuit based on the expressions below:

(AB + C)D

(A + B)(B + C)

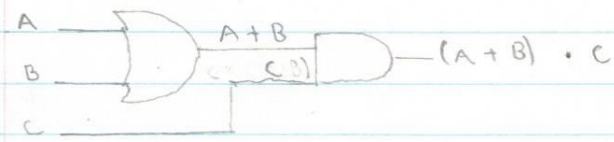
A'B + (B+C)' *Note: A' means NOT A)

*(B+C)' means the NOT is applied to the result of B+C

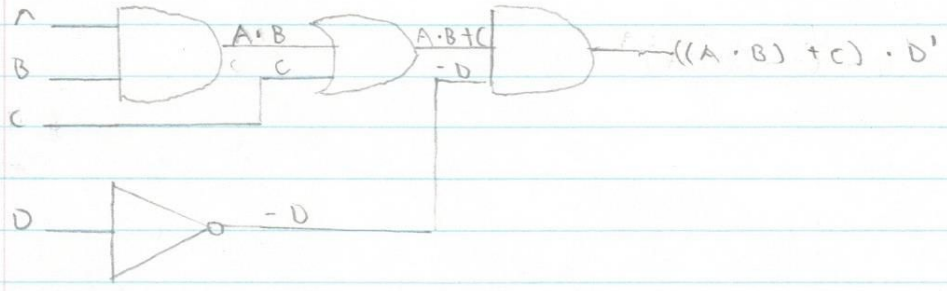
Logic Gate Worksheet

1) OFF
2) ON
3) OFF
4) ON
5) OFF
6) ON
7) ON
8) OFF
9) OFF
10) ON

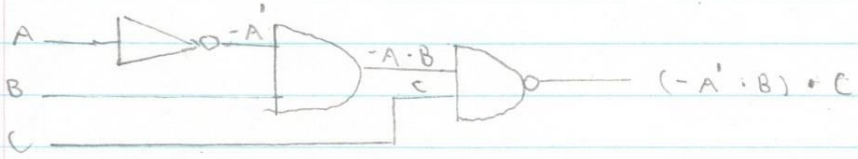
11)



12)



13)



14)