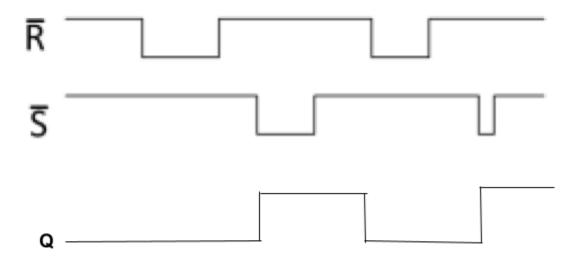
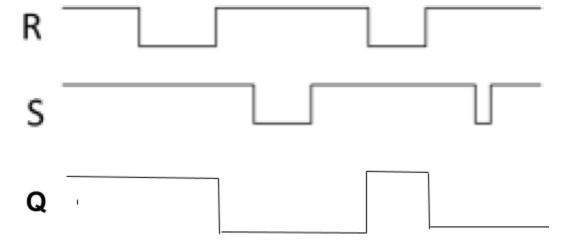
Note: Assume that Q = 0 for all questions before the first clock cycle starts. You will need to double-click on each image in order to show your work.

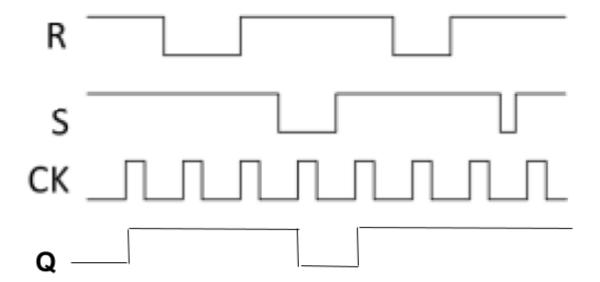
1. Complete the following timing diagram for an active **low** R-S flip flop (no clock).



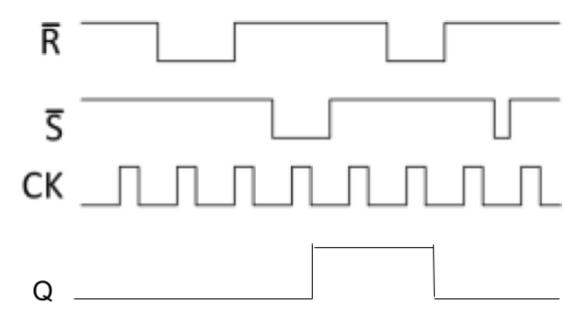
2. Complete the same timing diagram for an active <u>high</u> R-S flip flop (no clock).



3. Complete the timing diagram for an active **high, PGT** R-S flip flop (with clock).



4. Complete the timing diagram for an active **low, NGT** R-S flip flop (with clock).



5. Complete the timing diagram for an active **high, NGI** R-S flip flop (with clock).

