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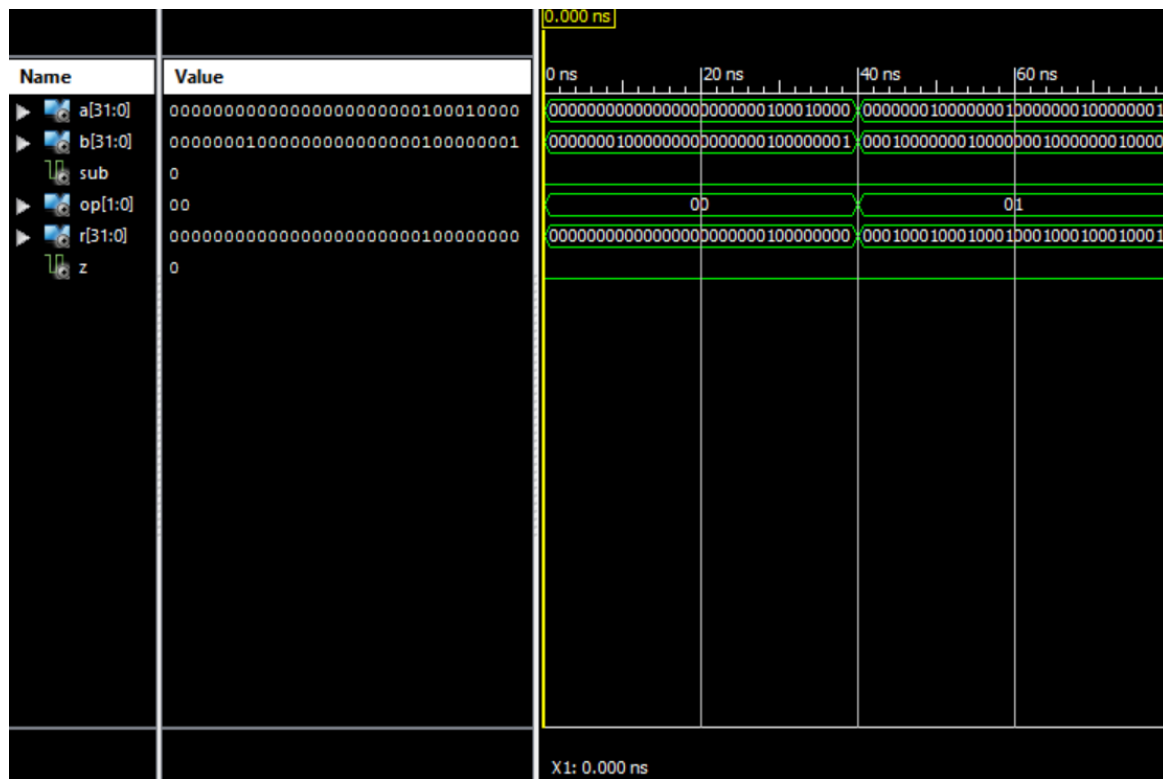
# Lab 3b

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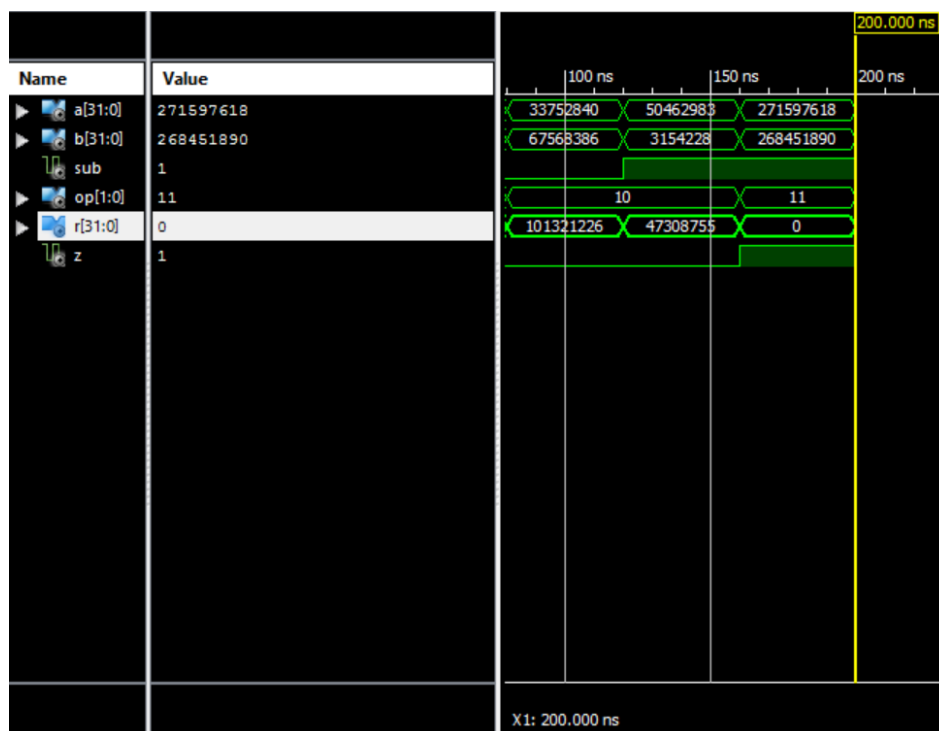
## Part 1

We split the simulation into two screenshots since we could not fit it all in one.

The simulation for AND and OR.

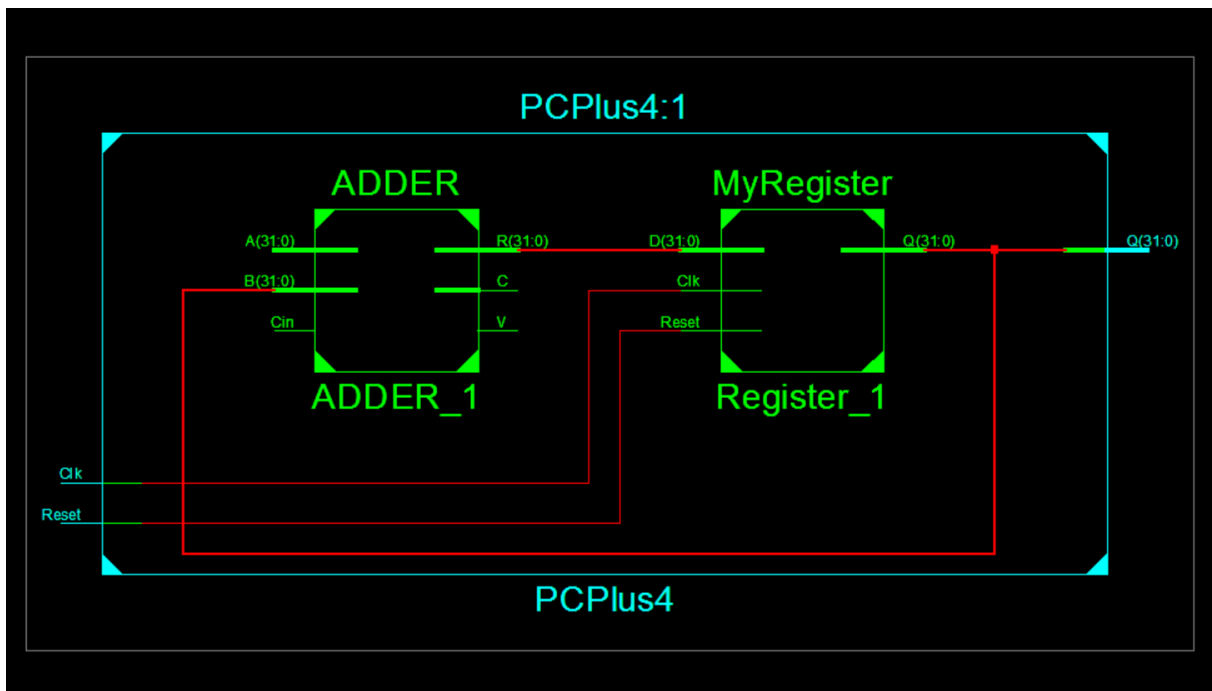


The simulation without AND and OR.



## Part 2

The RTL schematic for PCPlus4



The simulation of PCPlus4

