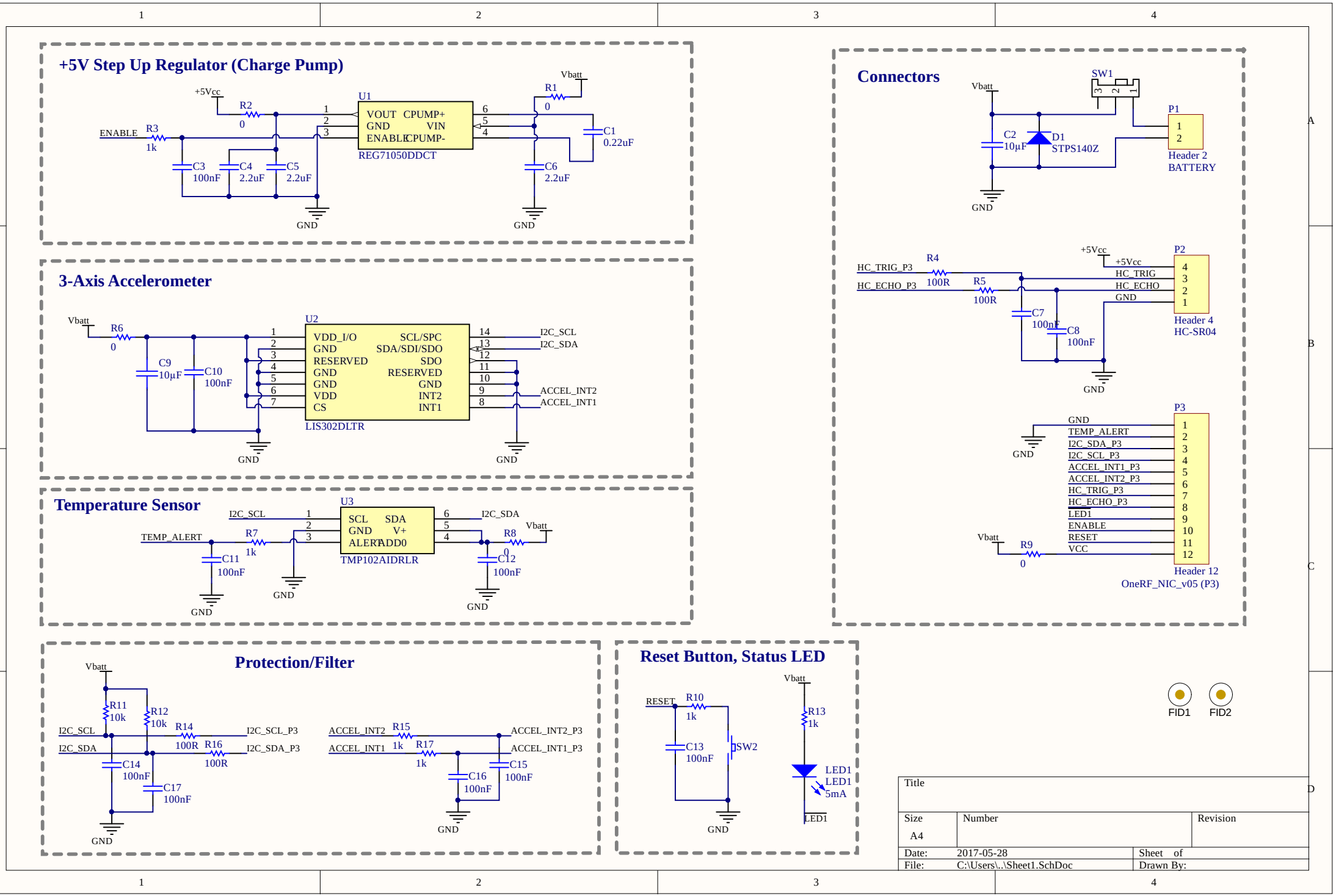
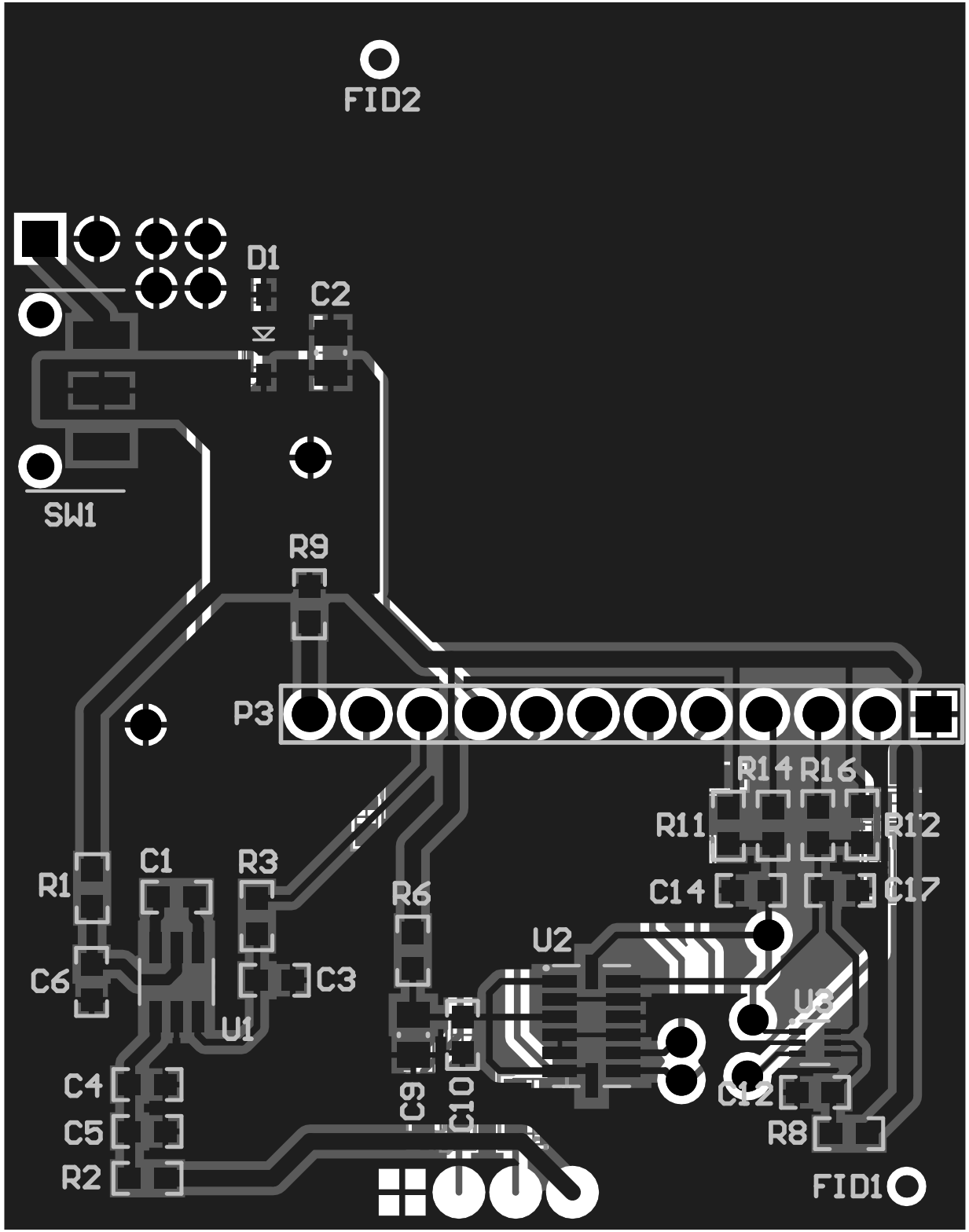

+5V Step Up Regulator (Charge Pump)
+5Vcc
R2
0
ENABLE
R3
1k
C3
100nF
C4
2.2uF
C5
2.2uF
U1
VOUT CPUMP+
GND VIN
ENABLE CPUMP-
REG71050DDCT
R1
0
Vbatt
C1
0.22uF
C6
2.2uF
GND
3-Axis Accelerometer
Vbatt
R6
0
C9
10µF
C10
100nF
U2
VDD_I/O SCL/SPC
GND SDA/SDI/SDO
RESERVED SDO
GND RESERVED
GND GND
VDD INT2
CS INT1
LIS302DLTR
I2C_SCL
I2C_SDA
ACCEL_INT2
ACCEL_INT1
Temperature Sensor
I2C_SCL
TEMP_ALERT
R7
1k
C11
100nF
U3
SCL SDA
GND V+
ALERT ADD0
TMP102AIDRLR
I2C_SDA
R8
0
Vbatt
C12
100nF
Protection/Filter
Vbatt
R11
10k
R12
10k
I2C_SCL
I2C_SDA
R14
100R
R16
100R
I2C_SCL_P3
I2C_SDA_P3
C14
100nF
C17
100nF
ACCEL_INT2
ACCEL_INT1
R15
1k
R17
1k
ACCEL_INT2_P3
ACCEL_INT1_P3
C16
100nF
C15
100nF
Reset Button, Status LED
RESET
R10
1k
C13
100nF
SW2
Vbatt
R13
1k
LED1
LED1
5mA
Connectors
Vbatt
SW1
C2
10µF
D1
STPS140Z
P1
Header 2
BATTERY
HC_TRIG_P3
HC_ECHO_P3
R4
100R
R5
100R
+5Vcc
C7
100nF
C8
100nF
P2
HC_TRIG
HC_ECHO
Header 4
HC-SR04
P3
GND
TEMP_ALERT
I2C_SDA_P3
I2C_SCL_P3
ACCEL_INT1_P3
ACCEL_INT2_P3
HC_TRIG_P3
HC_ECHO_P3
LED1
ENABLE
RESET
VCC
R9
0
Header 12
OneRF_NIC_v05 (P3)
FID1
FID2
Title
Size
A4
Number
Revision
Date: 2017-05-28
Sheet of
File: C:\Users\..\Sheet1.SchDoc
Drawn By:


FID2
D1
C2
SW1
R9
P3
R14
R16
R11
R12
C14
C17
R1
C1
R3
R6
U2
C6
C3
U1
U3
C4
C5
R2
C9
C10
C12
R8
FID1