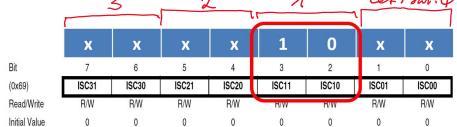
6.3. Externer Interrupt ATMEGA2560

Montag, 18. September 2017 11:10





Einstellung pros. od. nop. Flanke

15010

15011 lefeener Interrugt 1

0×3

2.B.: Eleaner Interryol 2 and stergendo Flourse

Bits 1SC20 and 1SC21 setsen

Maske: 0011 0000

EICRA 1 = 0x30;

Register EIMSK (External Interrupt Mask Register)

	X	х	х	х	х	х	/1	х	
Bit	7	6	5	4	3	2	1	0	
0x1D (0x3D)	INT7	INT6	INT5	INT4	INT3	INT2	INT1	INT0	

 Read/Write
 R/W
 R/W

Interrupt abdioreren

Z.B.: Externer Interryt 1 aktivieren

Ril INT1 seken

ÉIMSK 1 = 0x02;

rade: EIMSK 1 = (1 << INT1);

Interryts peneral aktivieres:

include (avr/intearupt. h >

sei (); // set enable is

dr ();

11 set enable interrupt - unterrupts
11 clear - unterrupt deakdiosionen

Interryd Service Routine:

Z.B.: Externer Interrugh 3

15R (INT3_rock)

2

7