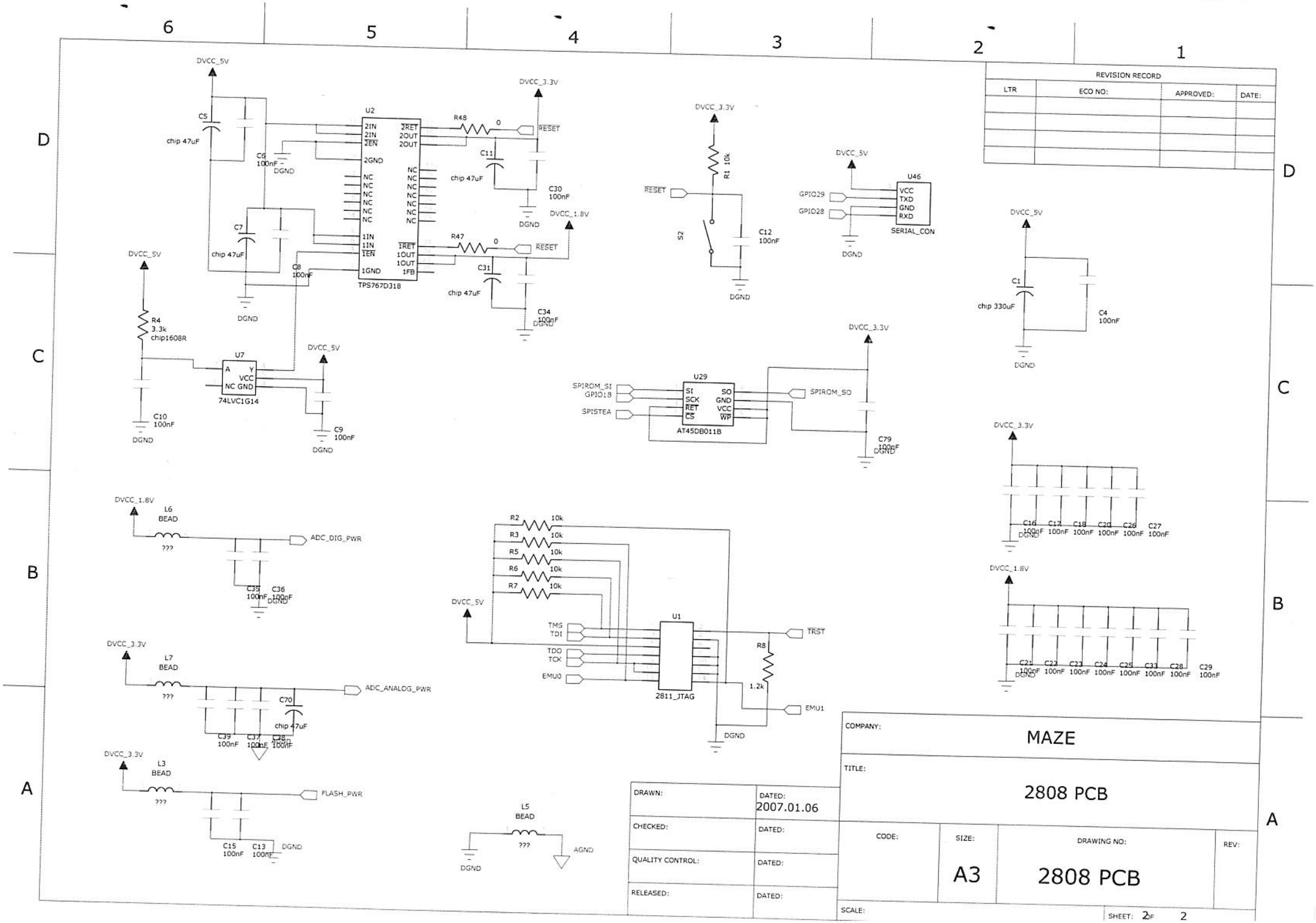
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
DVCC_5V
C5
chip 47uF
C6
100nF
DGND
C7
chip 47uF
C8
100nF
U2
TPS767D318
2IN
2IN
2EN
2GND
NC
1IN
1IN
1EN
1GND
2RET
2OUT
2OUT
1RET
1OUT
1OUT
1FB
R48
0
RESET
C11
chip 47uF
C30
100nF
DVCC_3.3V
DVCC_1.8V
R47
0
C31
chip 47uF
C34
100nF
R4
3.3k
chip1608R
U7
74LVC1G14
A
Y
VCC
NC GND
C10
100nF
C9
100nF
R1 10k
S2
C12
100nF
U46
SERIAL_CON
GPIO29
GPIO28
VCC
TXD
GND
RXD
C1
chip 330uF
C4
100nF
U29
AT45DB011B
SPIROM_SI
GPIO18
SPISTEA
SI
SCK
RET
CS
SO
GND
VCC
WP
SPIROM_SO
C79
100nF
C16
100nF
C17
100nF
C18
100nF
C20
100nF
C26
100nF
C27
100nF
C21
100nF
C22
100nF
C23
100nF
C24
100nF
C25
100nF
C33
100nF
C28
100nF
C29
100nF
DVCC_1.8V
L6
BEAD
???
ADC_DIG_PWR
C35
100nF
C36
100nF
DVCC_3.3V
L7
BEAD
ADC_ANALOG_PWR
C70
chip 47uF
C39
100nF
C37
100nF
C38
100nF
L3
BEAD
FLASH_PWR
C15
100nF
C13
100nF
L5
BEAD
AGND
R2
R3
R5
R6
R7
10k
U1
2811_JTAG
TMS
TDI
TDO
TCK
EMU0
TRST
R8
1.2k
EMU1
DRAWN:
DATED:
2007.01.06
CHECKED:
QUALITY CONTROL:
RELEASED:
COMPANY:
MAZE
TITLE:
2808 PCB
CODE:
SIZE:
A3
DRAWING NO:
2808 PCB
REV:
SCALE:
SHEET: 2 OF 2