

### **Mini Project 1 Submission**

System Verilog Files: <https://github.com/H-TejadaDeras/ENGR3410-01.25FA/tree/main/assignments/mini%20project%201>

Demo Video: [https://olincollege-my.sharepoint.com/:v:/g/personal/htejada\\_olin\\_edu/EdmfltayH5hKifjYGvctPlsBRsAMHB\\_3orDgrgk9Y4WPzg?e=6btMmp](https://olincollege-my.sharepoint.com/:v:/g/personal/htejada_olin_edu/EdmfltayH5hKifjYGvctPlsBRsAMHB_3orDgrgk9Y4WPzg?e=6btMmp)