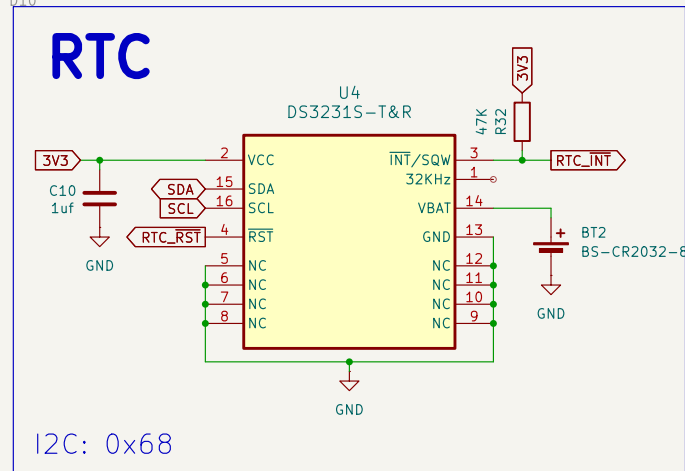


RGB

- terminal for vbat disconnection switch
- turn Analog input 3 & 4 to 4-20mA receivers using 150ohm shunt resistor to GND
- remove redundant i2c jst
- add 4 wall mount right angle aviation ports
- add one wall mount right angle DC jack
- move all RS485 pins to 38-41

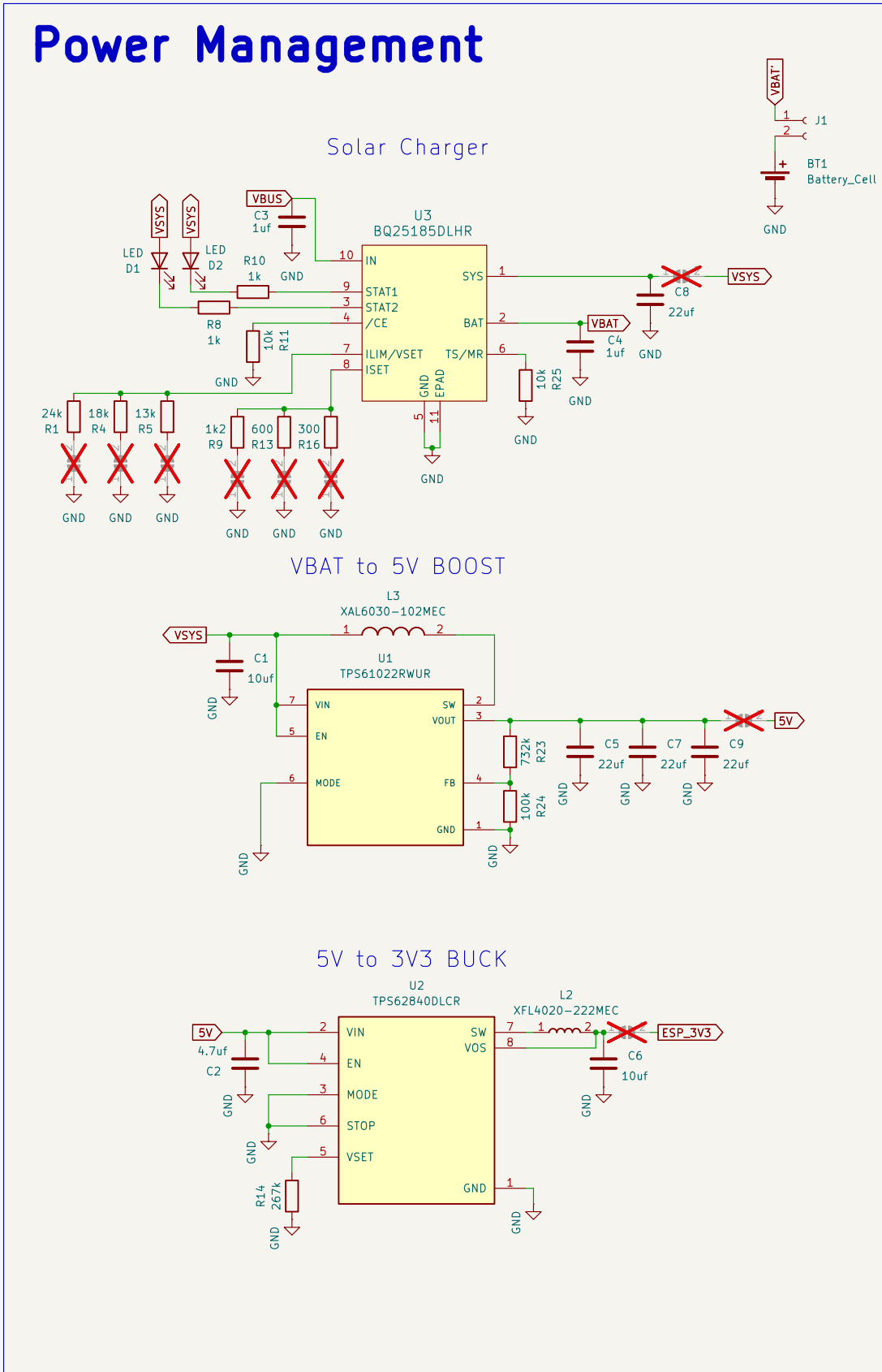
[illegible]

Current/Power Monitor

The diagram illustrates the connection of an INA219AxCN module for current and power monitoring. The module is represented by a yellow box with pins 1 through 8. The connections are as follows:

- Pin 1 (IN+):** Connected to the positive terminal of a 10mΩ shunt resistor (R_SHUNT1) in series with the VBAT+ supply.
- Pin 2 (IN-):** Connected to the negative terminal of the 10mΩ shunt resistor, which is also connected to the VBAT- supply.
- Pin 3 (GND):** Connected to the common ground of the system.
- Pin 4 (V5):** Connected to a 3V3 supply through a 100nF capacitor (C14).
- Pin 5 (SDA):** Connected to the SDA pin of an I2C device.
- Pin 6 (SCL):** Connected to the SCL pin of an I2C device.
- Pin 7 (A1):** Connected to ground.
- Pin 8 (A0):** Connected to ground.

The I2C address is specified as 0x40.

[illegible]

DC Jack

DC=012408M=2.0

J3

CR2 SK34A-LTP

C21 1000uf

C22 100nf

C24 100nf

C25 100nf

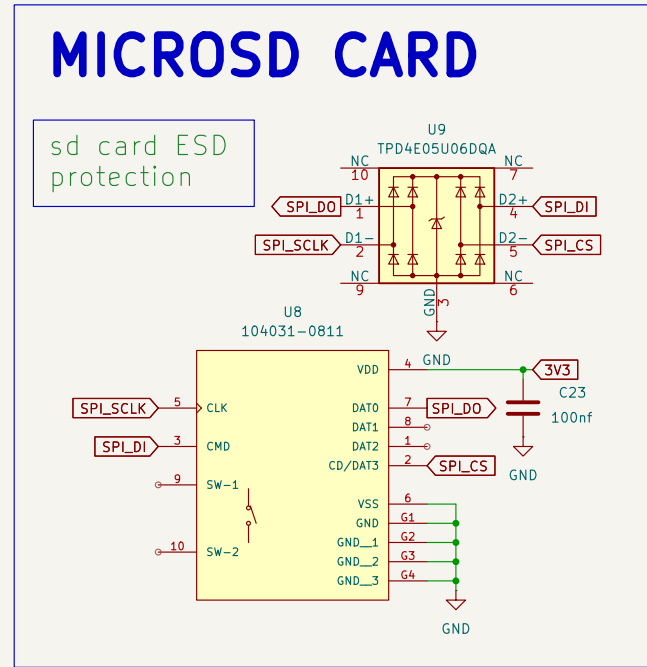
GND

GND

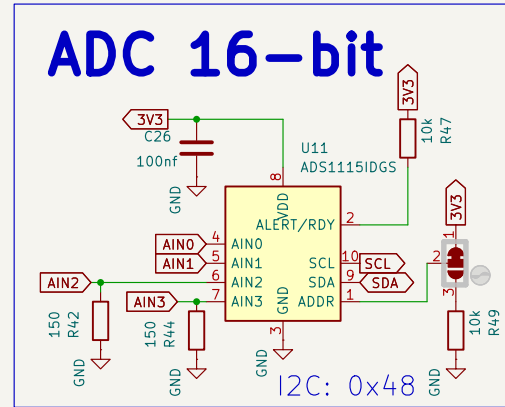
GND

GND

VBUS

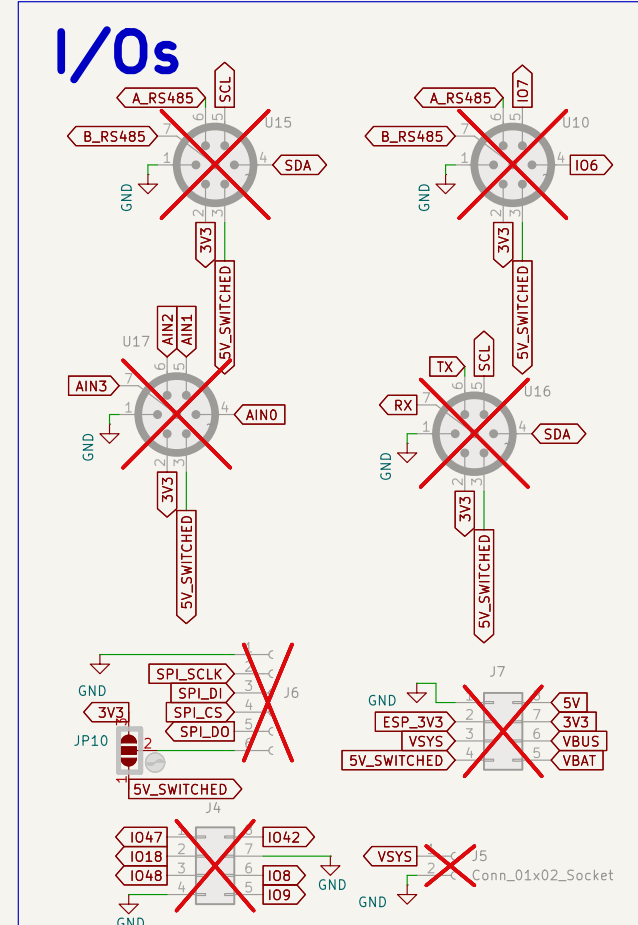


RS485 to TTL



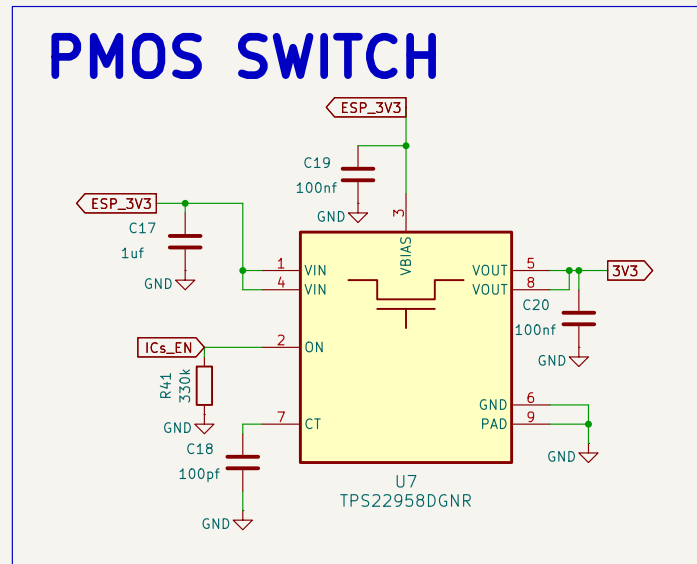
I2C PULLUP

The diagram illustrates an I2C bus pullup network. At the top, a green horizontal line represents the I2C bus. A green dot on this line is connected to a green vertical line that leads to a 3V3 voltage source, represented by a battery symbol. From the green dot, two green lines branch out downwards to two red rectangular components representing I2C devices. The left device is labeled 'R52 4K7' and the right device is labeled 'R53 4K7'. Below each device is a red rectangular component representing an I2C address decoder. The left decoder is labeled '15A' and the right decoder is labeled '15S'. The entire circuit is enclosed in a blue rectangular frame.



Holes

A diagram showing six holes, labeled H1 through H6, arranged in a horizontal row. Each hole is represented by a small circle with a cross inside. A large red 'X' is drawn over each hole, indicating that they are closed or blocked.

[illegible]

5V SWITCH

The diagram shows the AP2171WG-7 IC with the following connections:

- IN (Pin 4):** Connected to a 5V-SWITCHED signal via a 10k resistor (R48).
- OUT (Pin 1):** Connected to a 5V supply via capacitors C34 (100nF) and C32 (10uF).
- GND (Pin 2):** Connected to ground.
- FLG (Pin 3):** Connected to ground.