CC3501 weekly report example

**Group number:** 2 **Team members:** Hunter Kruger-Ilingworth, Thomas Mehes, Quentin Bouet   
**Week number:** 9

**Progress this week:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Task** | **Who did it?** | **What were the outcomes?** | **Who did the peer review?** | **What did you learn?** |
| Tested connections and power supplies of board | Quentin | The board works and all tested connections are functioning as expected. The 12V voltage regulator outputs below 12V (10.6V) as expected, because the board is powered by 12V. | Hunter | Confirmed and supervised testing. Checked that 10V is enough for all sensors. Noted that the load cell will need calibration because of this. |
|  |  |  |  |  |
|  |  |  |  |  |

**Overall project tracking:**

|  |  |
| --- | --- |
| **Week number** | **Milestones** |
| 4 | Confirm project topic |
| 5 | Begin Overview and planning |
| 6 | Hardware design: Microcontroller, DAC, SD card, flash and usb interface |
| 7 | Hardware design: Voltage regulators, loadcell circuit layout and testing, SDI-12 testing and interfacing and Informal check with Laurance |
| 8 | Hardware design: write working SDI-12 code, start PCB layout  Finalise draft schematic for Laurance to review. |
| 9 | Finish PCB layout and review to make sure all design rules pass. Implement fixes to the PCB. Final PCB design submitted on Friday to Terence |
| LR | Software: Begin development that doesn’t require hardware testing  Report: begin report writing |
| 10 | Hardware: Solder components to PCB and begin interfacing  Software: Coding to receive data from I^2C DAC and optimise more SDI-12 sensor code |
| 11 | Software: data logging applications including averaging, variable sampling periods and clean exported data. |
| 12 | Verify all hardware functionality, perform testing of existing software on the physical board. Polish the software. |
| 13 | Implement final bug fixes.  Write the report.  Demo day during Friday lab. |