CC3501 weekly report example

**Group number:** 2 **Team members:** Hunter Kruger-Ilingworth, Thomas Mehes, Quentin Bouet   
**Week number:** 12

**Progress this week:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Task** | **Who did it?** | **What were the outcomes?** | **Who did the peer review?** | **What did you learn?** |
| Implement UI and data sampling program | Hunter | Terminal program that implements the following commands,  help,  set\_voltage number //set dac voltage,  get\_data,  shutdown  All whilst in the background sample and save data to the SD card via SPI |  |  |
| DAC driver | Quentin |  | Hunter Quentin | The driver works and the output voltage, when measured using a multi meter is correct to the nearest ~0.05V. This has the accuracy needed for the client’s application |
| Solder on correct SM resistors and wiring traces | Thomas Hunter  Laurance | Pain | Quentin | Accomplish the impossible |
| Report | Thomas |  |  |  |

**Overall project tracking:**

|  |  |
| --- | --- |
| **Week number** | **Milestones** |
| 4 | Confirm project topic |
| 5 | Begin Overview and planning |
| 6 | Hardware design: Microcontroller, DAC, SD card, flash and usb interface |
| 7 | Hardware design: Voltage regulators, loadcell circuit layout and testing, SDI-12 testing and interfacing and Informal check with Laurance |
| 8 | Hardware design: write working SDI-12 code, start PCB layout  Finalise draft schematic for Laurance to review. |
| 9 | Finish PCB layout and review to make sure all design rules pass. Implement fixes to the PCB. Final PCB design submitted on Friday to Terence |
| LR | Software: Begin development that doesn’t require hardware testing  Report: begin report writing |
| 10 | Hardware: Solder components to PCB and begin interfacing  Software: Coding to receive data from I^2C DAC and optimise more SDI-12 sensor code |
| 11 | All major functionality of the project working but unrefined. Fixed hardware issues and updated software. |
| 12 | Software: data logging applications including averaging, variable sampling periods and clean exported data. Verify all hardware functionality, perform testing of existing software on the physical board. Polish the software. |
| 13 | Implement final bug fixes.  Write the report.  Demo day during Friday lab. |