Chapter 11 Boolean Algebra

What is Boolean Algebra?

- A minor generalization of propositional logic.
 - In general, an *algebra* is any mathematical structure satisfying certain standard algebraic axioms.
 - Such as associative/commutative/transitive laws, etc.
 - General theorems that are proved about an algebra then apply to any structure satisfying these axioms.
- Boolean algebra just generalizes the rules of propositional logic to sets other than {T,F}. Claude Shannon' Master's thesis!
 - E.g., to the set $\{0, 1\}$ of base-2 digits, or the set $\{V_{\rm L}, V_{\rm H}\}$ of low and high voltage levels in a circuit.
- We will see that this algebraic perspective lends itself to the design of *digital logic circuits*.

Boolean Algebra

- §1 Boolean Functions
- §2 Representing Boolean Functions
- §3 Logic Gates
- §4 Minimization of Circuits

§11.1 – Boolean Functions

- Boolean complement, sum, product.
- Boolean expressions and functions.
- Boolean algebra identities.
- Duality.
- Abstract definition of a Boolean algebra.

Complement, Sum, Product

- Correspond to logical NOT, OR, and AND.
- We will denote the two logic values as
 0:≡F and 1:≡T, instead of False and
 True.
 - Using numbers encourages algebraic thinking.
- New, more algebraic-looking notation for the most common Boolean operators:

$$\overline{x} := \neg x$$

$$x \cdot y \equiv x \wedge y$$

$$x + y \equiv x \vee y$$

Boolean Sum, Boolean Product, Complement

Boolean Sum (or)

$$1+1=1$$
, $1+0=1$, $0+1=1$, $0+0=0$

Boolean Product (and)

$$1 \cdot 1 = 1$$
, $1 \cdot 0 = 0$, $0 \cdot 1 = 0$, $0 \cdot 0 = 0$

Complement

$$\bar{1} = 0, \quad \bar{0} = 1$$

Example 1 Find the value of

$$1 \cdot 0 + (0+1)$$

$$1 \cdot 0 + (0+1) = 0 + 1 = 0 + 0 = 0.$$

Boolean Expressions and Boolean Functions

- Let $B = \{0, 1\}$, the set of Boolean values.
- For all $n \in \mathbb{Z}^+$, any function $f: B^n \to B$ is called a Boolean function of degree n.
- There are 2^{2^n} (wow!) distinct Boolean functions of degree n.
 - $-\exists 2^n$ rows in truth table, with 0 or 1 in each.

Degree	How many	<u>Degree</u>	How many
0	2	4	65,536
1	4	5	4,294,967,296
2	16	6	18,446,744,073,709,551,616.
3	256		

Example 2 Find the values of Boolean function (of degree 2) $F(x, y) = x\overline{y}$.

Boolean expression

TA	BLE	1
x	y	F(x, y)
1	1	0
1	0	1
0	1	0
0	0	0

Example 3 Find the values of Boolean function (of degree 3) F(x, y, z) = xy + z.

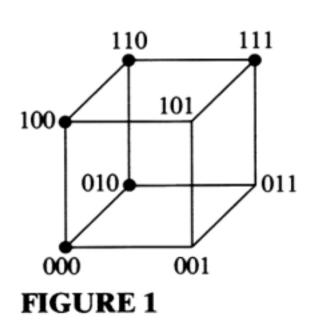
TAE	BLE 2				
x	y	z	хy	\overline{z}	$F(x, y, z) = xy + \overline{z}$
1	1	1	1	0	1
1	1	0	1	1	1
1	0	1	0	0	0
1	0	0	0	1	1
0	1	1	0	0	0
0	1	0	0	1	1
0	0	1	0	0	0
0	0	0	0	1	1

A Boolean function of degree *n* can be represented by an *n*-cube (hypercube) with the corresponding function value at each vertex.

Example 4 Express the Boolean function

$$F(x, y, z) = xy + z$$
. by a hypercube.

TABLE 2									
x	у	z	ху	Ī	$F(x, y, z) = xy + \overline{z}$				
1	1	1	1	0	1				
1	1	0	1	1	1				
1	0	1	0	0	0				
1	0	0	0	1	1				
0	1	1	0	0	0				
0	1	0	0	1	1				
0	0	1	0	0	0				
0	0	0	0	1	1				



Boolean Expressions

- Let $x_1, ..., x_n$ be *n* different Boolean variables.
 - -n may be as large as desired.
- A *Boolean expression* (recursive definition) is a string of one of the following forms:
 - Base cases: $0, 1, x_1, ..., \text{ or } x_n$.
 - Recursive cases: E_1 , (E_1E_2) , or (E_1+E_2) , where E_1 and E_2 are Boolean expressions.
- A Boolean expression represents a Boolean function.
 - Furthermore, every Boolean function (of a given degree) can be represented by a Boolean expression.

Boolean equivalents, operations on Boolean expressions

- Two Boolean expressions e_1 and e_2 that represent the exact *same* function f are called *equivalent*. We write $e_1 \Leftrightarrow e_2$, or just $e_1 = e_2$.
 - Implicitly, the two expressions have the same value for *all* values of the free variables appearing in e_1 and e_2 .
- The operators , +, and · can be extended from operating on expressions to operating on the functions that they represent, in the obvious way.

$$\overline{F}(x_1, x_2, ..., x_n) = \overline{F(x_1, x_2, ..., x_n)} \quad i.e. \ F(x, y, z) = xy + \overline{z}, \ \overline{F}(x, y, z) = \overline{xy + \overline{z}} = \overline{xy} + z$$

$$(F + G)(x_1, x_2, ..., x_n) = F(x_1, x_2, ..., x_n) + G(x_1, x_2, ..., x_n)$$

$$(FG)(x_1, x_2, ..., x_n) = F(x_1, x_2, ..., x_n)G(x_1, x_2, ..., x_n)$$

How many different Boolean functions of degree 3?

TABLE 3 The Boolean Functions of Degree Two.																	
x	у	F_1	F ₂	F ₃	F4	F ₅	F ₆	F ₇	F ₈	F9	F ₁₀	F ₁₁	F ₁₂	F ₁₃	F ₁₄	F ₁₅	F ₁₆
1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
1	0	1	1	1	1	0	0	0	0	1	1	1	1	0	0	0	0
0	1	1	1	0	0	1	1	0	0	1	1	0	0	1	1	0	0
0	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0

Example 5 (How many different Boolean functions of degree 3?

TABLE 4 The Number of Boolean Functions of Degree n.				
Degree	Number			
1	4			
2	16			
3	256			
4	65,536			
5	4,294,967,296			
6	18,446,744,073,709,551,616			

Identities of Boolean Algebra

Identity	Name				
$\overline{\overline{x}} = x$	Law of the double complement				
$ \begin{aligned} x + x &= x \\ x \cdot x &= x \end{aligned} $	Idempotent laws				
$ \begin{aligned} x + 0 &= x \\ x \cdot 1 &= x \end{aligned} $	Identity laws				
$ \begin{aligned} x + 1 &= 1 \\ x \cdot 0 &= 0 \end{aligned} $	Domination laws				
$ \begin{aligned} x + y &= y + x \\ xy &= yx \end{aligned} $	Commutative laws				
x + (y + z) = (x + y) + z $x(yz) = (xy)x$	Associative laws				
x + yz = (x + y)(x + z) x(y + z) = xy + xz	Distributive laws				
$\frac{\overline{(xy)} = \overline{x} + \overline{y}}{(x+y)} = \overline{x} \overline{y}$	De Morgan's laws				
$ \begin{aligned} x + xy &= x \\ x(x + y) &= x \end{aligned} $	Absorption laws				
$x + \overline{x} = 1$	Unit property				
$x\overline{x} = 0$	Zero property				

Not true in ordinary algebras.

Example 6 Prove that the Boolean equation x(y+z) = xy + xz is true.

x	y	z	y + z	xy	xz	x(y+z)	xy + xz
1	1	1	1	1	1	1	1
1	1	0	1	1	0	1	1
1	0	1	1	0	1	1	1
1	0	0	0	0	0	0	0
0	1	1	1	0	0	0	0
0	1	0	1	0	0	0	0
0	0	1	1	0	0	0	0
0	0	0	0	0	0	0	0

Example 7 Prove the absorption law

$$x(x+y)=x.$$

Solution:

$$x(x + y) = (x + 0)(x + y)$$
 Identity law for the Boolean sum
$$= x + 0 \cdot y$$
 Distributive law of the Boolean sum over the Boolean product
$$= x + y \cdot 0$$
 Commutative law for the Boolean product
$$= x + 0$$
 Domination law for the Boolean product
$$= x.$$
 Identity law for the Boolean sum

Duality

Definition: The *dual* e^d of a Boolean expression e representing function f is obtained by exchanging + with \cdot , and 0 with 1 in e.

The function represented by e^{d} is denoted f^{d} .

Duality principle: If $e_1 \Leftrightarrow e_2$ then $e_1^d \Leftrightarrow e_2^d$.

Example: The equivalence x(x+y) = x implies (and is implied by) x + xy = x.

Example 8: Find the *dual* of the Boolean expressions

$$f(x,y) = x(y+0), \quad g(x,y,z) = x \cdot 1 + (y+z).$$

Solution:

$$f^{d}(x,y) = x + (y \cdot 1),$$

 $g^{d}(x,y,z) = (x + 0)(yz).$

Example 9: Take the duals of both sides of the Boolean expression.

$$x(x+y)=x.$$

Solution:

$$x + (xy) = x$$
.

The Abstract Definition of Boolean Algebra

Definition 1 A *Boolean algebra* is a set B with operators \vee and \wedge , element 0 and 1, and a uniary operator $\overline{}$ such that the following properties holds for x, y, z in B.

$$\left. \begin{array}{l}
 x \lor 0 = x \\
 x \land 1 = x
 \end{array} \right\}$$

$$\begin{array}{l}
x \lor \overline{x} = 1 \\
x \land \overline{x} = 0
\end{array}$$

$$(x \lor y) \lor z = x \lor (y \lor z) (x \land y) \land z = x \land (y \land z)$$

$$\left. \begin{array}{l}
x \lor y = y \lor x \\
x \land y = y \land x
\end{array} \right\}$$

$$x \lor (y \land z) = (x \lor y) \land (x \lor z) x \land (y \lor z) = (x \land y) \lor (x \land z)$$

Identity laws

Complement laws

Associative laws

Commutative laws

Distributive laws

Note that *B* may generally have other elements besides **0**, **1**, and we have not fully defined any of the operators!

More about Boolean algebras

- Any Boolean algebra can be proven to satisfy all the theorems of "ordinary" Boolean algebra!
- An example of another Boolean algebra:
 - For any set *U*, let $B = 2^U$, $0 = \emptyset$, 1 = U, $∨ = \bigcirc$, and = (set complement).
 - Then, $(B,0,1,\land,\lor,\lnot)$ is a Boolean algebra!
- Boolean algebras can also be defined in terms of *lattices* (in chapter 7, though we skipped it).
 - A poset where every pair x,y has a lub and a glb.
 - A complemented, distributed lattice is a Boolean alg.

§11.2 Representing Boolean Functions

- Sum-of-products Expansions
 - A.k.a. Disjunctive Normal Form (DNF)
- Product-of-sums Expansions
 - A.k.a. Conjunctive Normal Form (CNF)
- Functional Completeness
 - Minimal functionally complete sets of operators.

Sum-of-Products Expansions

Example 1: Find the Boolean expressions that represent the function F(x, y, z) and G(x, y, z).

TA	BL	E 1		
x	y	z	F	G
1	1	1	0	0
1	1	0	0	1
1	0	1	1	0
1	0	0	0	0
0	1	1	0	0
0	1	0	0	1
0	0	1	0	0
0	0	0	0	0

Solution:

For
$$F$$
, $F(x, y, z) = 1$ iff $x = y = z = 1$.

$$\Rightarrow F(x, y, z) = xyz.$$

For
$$G$$
, $G(x, y, z) = 1$ iff $x = y = z = 1$ or

$$\bar{x} = y = \bar{z} = 1.$$

$$\Rightarrow F(x, y, z) = xyz + xyz.$$

Definition 1: A *literal* is a Boolean variable or its complement. A *minterm* of Boolean variables x_1, x_2, \dots, x_n is Boolean product $y_1y_2 \dots y_n$ where $y_i = x_i$ or $y_i = \overline{x_i}$.

Example 2 (P.710): Find the minterm F such that F = 1 if $x_1 = x_3 = 0$ and F = 0 if $x_2 = x_4 = x_5 = 1$. Solution: The minterm is $\frac{1}{x_1} \frac{1}{x_2} \frac{1}{x_3} \frac{1}{x_4} \frac{1}{x_5} \frac{1}{x_5}$.

The sum of minterms that represents the functions is called the *sum-of-products expansion* or the **disjunctive normal form** of the Boolean function.

$$F(x, y, z) = xyz + xyz + xyz$$

Example 3 (P.710): Find the sum-of-product expansion for the function F(x, y, z) = (x + y)z.

Solution:

$$F(x, y, z) = (x + y)\overline{z}$$

$$= x\overline{z} + y\overline{z}$$
Distributive law
$$= x1\overline{z} + 1y\overline{z}$$
Identity law
$$= x(y + \overline{y})\overline{z} + (x + \overline{x})y\overline{z}$$
Unit property
$$= xy\overline{z} + x\overline{y}\overline{z} + xy\overline{z} + \overline{x}y\overline{z}$$
Distributive law
$$= xy\overline{z} + x\overline{y}\overline{z} + xy\overline{z} + \overline{x}y\overline{z}$$
Idempotent law

$$F(x, y, z) = xyz + xyz + xyz$$

(ii)

TAE	TABLE 2								
x	у	z	x + y	\overline{z}	$(x+y)\overline{z}$				
1	1	1	1	0	0				
1	1	0	1	1	1				
1	0	1	1	0	0				
1	0	0	1	1	1				
0	1	1	1	0	0				
0	1	0	1	1	1				
0	0	1	0	0	0				
0	0	0	0	1	0				

$$F(x, y, z) = 1 \text{ iff } x = y = \overline{z} = 1, \ x = \overline{y} = \overline{z} = 1, \text{ or } \overline{x} = y = \overline{z} = 1.$$

$$\Rightarrow$$
 $F(x, y, z) = xyz + xyz + xyz$.

Sum-of-Products Expansions

Theorem: Any Boolean function can be represented as a sum of products of variables and their complements.

Proof: By construction from the function's truth table. For each row that is 1, include a term in the sum that is a product representing the condition that the variables have the values given for that row.

Show an example on the board.

Literals, Minterms, DNF

- A *literal* is a Boolean variable or its complement.
- A minterm of Boolean variables $x_1, ..., x_n$ is a Boolean product of n literals $y_1, ..., y_n$, where y_i is either the literal x_i or its complement $\overline{x_i}$.
 - Note that at most one minterm can have the value 1.
- The disjunctive normal form (DNF) of a degree-n Boolean function f is the unique sum of minterms of the variables x_1, \ldots, x_n that represents f.
 - A.k.a. the sum-of-products expansion of f.

Conjunctive Normal Form

- A maxterm is a sum of literals.
- CNF is a *product-of-maxterms* representation.
- To find the CNF representation for f,
- take the DNF representation for complement $\neg f$,

$$\neg f = \sum_{i} \prod_{j} y_{i,j}$$

• and then complement both sides & apply DeMorgan's laws to get:

$$f = \prod_{i} \sum_{j} \neg y_{i,j}$$

Can also get CNF more directly, using the 0 rows of the truth table.

Functional Completeness

- Since every Boolean function can be expressed in terms of ·,+,¯, we say that the set of operators {·,+,¯} is *functionally complete*.
- There are smaller sets of operators that are also functionally complete.
 - We can eliminate either · or + using DeMorgan's law.
- NAND | and NOR ↓ are also functionally complete, each by itself (as a singleton set).
 - E.g., -x = x|x, and xy = (x|y)|(x|y).

Reversible Boolean Logic

- A reversible Boolean function of degree n is a bijective function $f:B^n \leftrightarrow B^n$.
 - Also corresponds to a permutation of B^n .
- Reversible unary and binary Boolean operators are bijective operators on B and B^2 , respectively.
 - Unary $f:B \leftrightarrow B$, binary $f:B^2 \leftrightarrow B^2$.
 - It turns out that *no* set of reversible unary and binary
 Boolean operators is functionally complete!
 - However, there are many ternary reversible operators that are functionally complete, even as singletons.

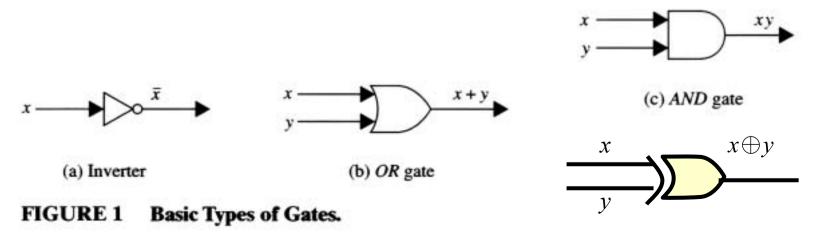
A little Quantum Logic

- A quantum Boolean function is a bijective and linear function $f: \mathbb{C}^{2^n} \leftrightarrow \mathbb{C}^{2^n}$.
 - That is, it maps vectors of 2^n complex numbers (one for each n-bit string of Boolean values) reversibly and linearly.
 - Any reversible Boolean function corresponds to a quantum Boolean function where a string in B^n is represented by c = 1 for that string, c = 0 for all others.
- Any quantum Boolean function can be built out of *quantum operators* operating on just C and C².
 - Quantization removes the need for ternary gates!

§11.3 Logic Gates

Boolean Algebra can be used to model the circuits of electronic devices. A computer or other electronic device, is made up of a number of circuits. Each circuit can be designed using the rules of Boolean algebra. The basic elements of circuits are called **gates**.

The circuit that we will study give output that depend only on the input and not on the current state of the circuit. Such circuits have no memory capacities. They are called **combinational circuits** or **gating networks**.



We an extend these gates to arbitrarily many inputs.

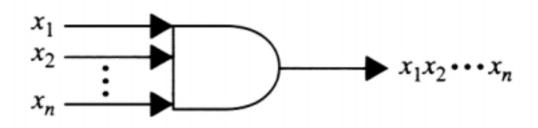
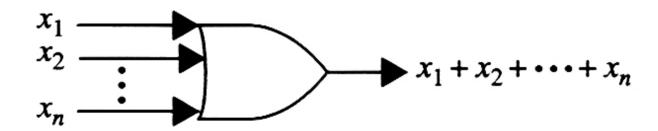
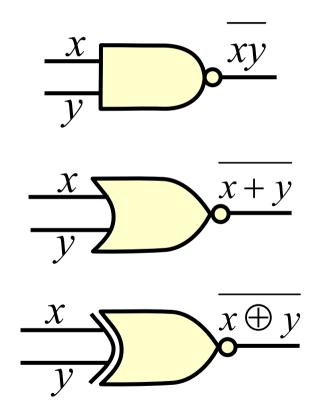


FIGURE 2 Gates with *n* Inputs.



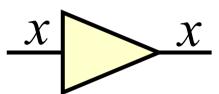
NAND, NOR, XNOR

- Just like the earlier icons, but with a small circle on the gate's output.
 - Denotes that output is complemented.
- The circles can also be placed on inputs.
 - Means, input is complemented before being used.



Buffer

• What about an inverter symbol *without* a circle?



- This is called a *buffer*. It is the identity function.
- It serves no logical purpose, but...
- It represents an explicit delay in the circuit.
 - This is sometimes useful for timing purposes.
- All gates, when physically implemented, incur a non-zero delay between when their inputs are seen and when their outputs are ready.

Combinational Logic Circuits

- Note: The correct word to use here is "combinational," NOT "combinatorial!"
 - Many sloppy authors get this wrong.
- These are circuits composed of Boolean gates whose outputs depend only on their most recent inputs, not on earlier inputs.
 - Thus these circuits have no useful memory.
 - Their state persists while the inputs are constant, but is irreversibly lost when the input signals change.

Combinational Circuit Examples

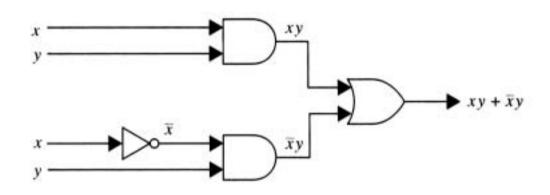
- Draw a few examples on the board:
 - Majority voting circuit.
 - XOR using OR / AND / NOT.
 - 3-input XOR using OR / AND / NOT.
- Also, show some binary adders:
 - Half adder using OR/AND/NOT.
 - Full adder from half-adders.
 - Ripple-carry adders.

Combinational Gates

Example: Construct circuits that produce the output

xy + xy.

Solution:



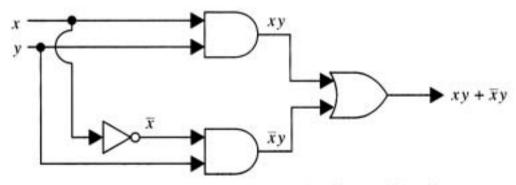
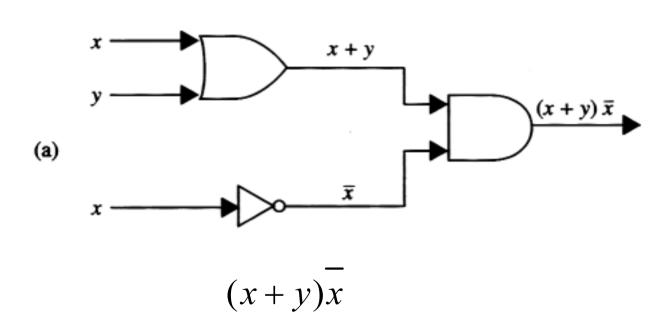
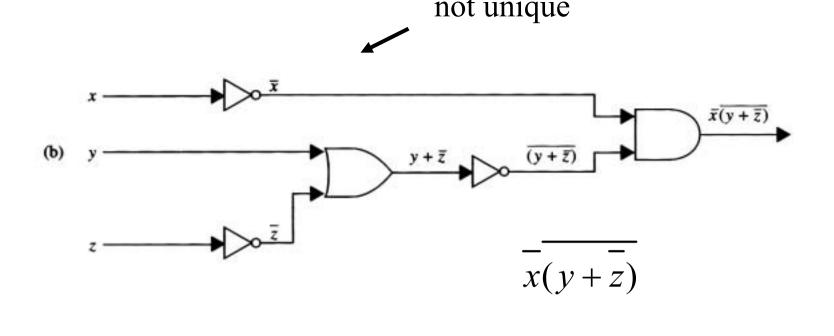


FIGURE 3 Two Ways to Draw the Same Circuit.

Example 1 (P. 714): Construct circuits that produce the output (a) (x+y)x, (b) x(y+z), (c) (x+y+z)(xyz). Solution:





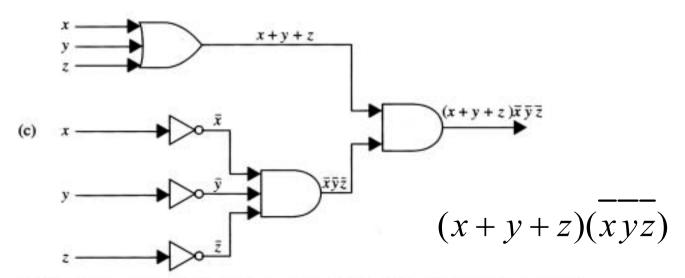


FIGURE 4 Circuits that Produce the Outputs Specified in Example 1.

Example:

Temperature > 80°?	Humidity > 50%	Air Conditioner on?
no	no	no
no	yes	yes
yes	no	yes
yes	yes	yes

Design a circuit that will turn on or turn off the air conditioner according to the above conditions.

Solution:

Let
$$x = \begin{cases} 1, & \text{if } temperature > 80^{o}, \\ 0 & \text{otherwise.} \end{cases}$$
 $y = \begin{cases} 1, & \text{if } humidity > 50\%, \\ 0 & \text{otherwise.} \end{cases}$ $F(x,y) = \begin{cases} 1, & \text{the air conditioner is on,} \\ 0, & \text{otherwise.} \end{cases}$

$\boldsymbol{\mathcal{X}}$	\mathcal{Y}	F
0	0	0
0	1	1
1	0	1
1	1	1

X	y	F
0	0	0
0	1	1
1	0	1
1	1	1

By sum- of- product expansions,

$$F(x,y) = \overline{x} y + x y + x y = y + x y$$

Example 2 (P. 714):

$$x = \begin{cases} 1 & \text{if the 1st individual votes yes,} \\ 0 & \text{otherwise} \end{cases}$$

$$z = \begin{cases} 1 & \text{if the 3rd individual votes yes,} \\ 0 & \text{otherwise} \end{cases}$$

Design a circuit that determine a proposal passes.

Solution:

$$F(x,y,z) = \begin{cases} 1, & \text{if } x + y + z \ge 2 \text{ (ordinary addition)}, \\ 0, & \text{otherwise.} \end{cases}$$

 $y = \begin{cases} 1 & \text{if the 2 nd individual votes yes,} \\ 0 & \text{otherwise} \end{cases}$

or
$$F(x, y, z) = xy + xz + yz$$
 (Boolean operations)

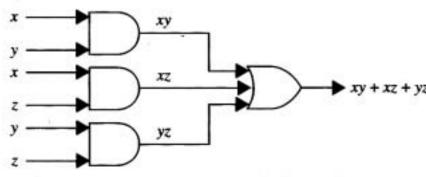


FIGURE 5 A Circuit for Majority Voting.

Example 3(a) (P. 714): To design a **two** switches circuit so that flipping any one of the switches for the fixture turns the light on when it is off and turns the light off when it is on.

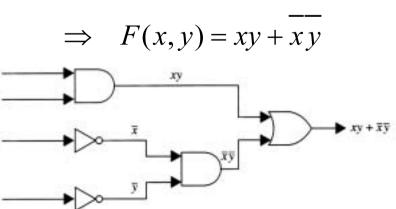
Solution:

Let $x = \begin{cases} 1 & \text{if the 1st switch is closed,} \\ 0 & \text{otherwise.} \end{cases}$ $y = \begin{cases} 1 & \text{if the 2nd switch is closed,} \\ 0 & \text{otherwise.} \end{cases}$

$$F(x,y) = \begin{cases} 1 & \text{if the light is on,} \\ 0 & \text{otherwise.} \end{cases} \Rightarrow F(1,1) = 1, F(1,0) = F(0.1) = 0, F(0,0) = 0$$

$$F(x,y) = \begin{cases} 1 & \text{if the light is on,} \\ 0 & \text{otherwise.} \end{cases} \Rightarrow F(x,y) = xy + \overline{xy}$$

TABLE 1						
x	у	F(x, y)				
1	1	1				
1	0	0				
0	1	0				
0	0	1				



Example 3(b) (P. 714): To design a three switches circuit so that flipping any one of the switches for the fixture turns the light on when it is off and turns the light off when it is on.

Solution:

Let
$$x = \begin{cases} 1 & \text{if the 1st switch is closed,} \\ 0 & \text{otherwise.} \end{cases}$$
 $y = \begin{cases} 1 & \text{if the 2nd switch is closed,} \\ 0 & \text{otherwise.} \end{cases}$ $z = \begin{cases} 1 & \text{if the 3rd switch is closed,} \\ 0 & \text{otherwise.} \end{cases}$

$$F(x,y,z) = \begin{cases} 1 & \text{if the light is on,} \\ 0 & \text{otherwise.} \end{cases}$$

$$\Rightarrow F(1,1,1) = 1, F(1,1,0) = F(1,0,1) = F(0,1.1) = 0, F(1,0,0) = F(0,1,0) = F(0,0,1) = 1$$
$$F(0,0,0) = 0.$$

 $\Rightarrow F(x, y, z) = xyz + xyz + xyz + xyz$ (Using sum- of- products expansion.)

T	TABLE 2							
x	у	z	F(x, y, z)					
1	1	1	1					
1	1	0	0					
1	0	1	0					
1	0	0	1					
0	1	1	0					
0	1	0	1					
0	0	1	1					
0	0	0	0					

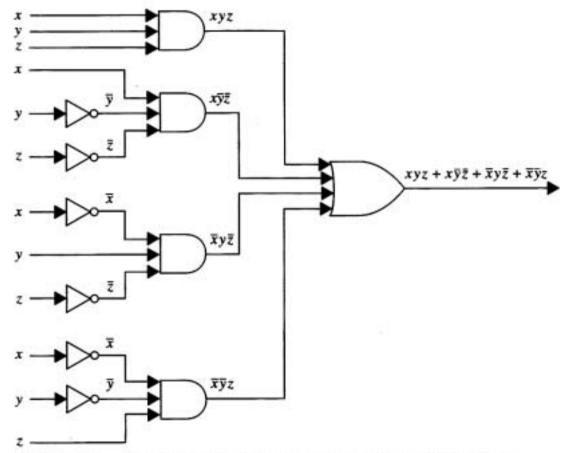


FIGURE 7 A Circuit for a Fixture Controlled by Three Switches.

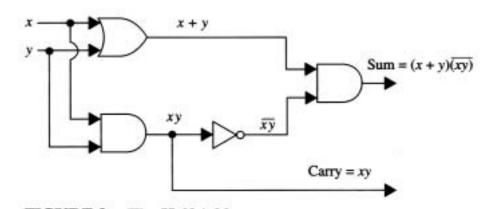
Adders

To design a circuit that carries out addition of two positive integers from their binary expansions.

The output will consist of two bits, namely s and c, where s is the sum bit and c is the carry bit. This circuit is called a **multiple output circuit**.

TA	BLI	E 3	
0.60 1.60	out a		
	tput		
the	Hal	f	
Ad	der.		
Inj	put	Ou	ıtput
r	v	e	-

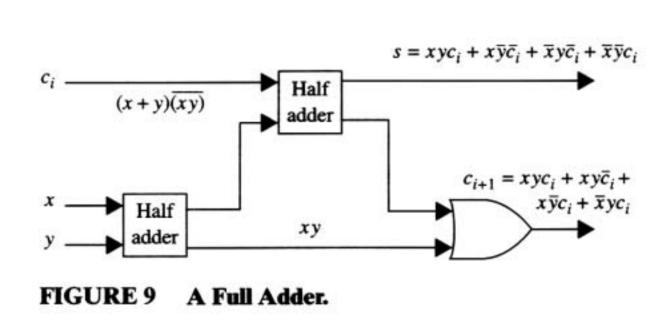
Half adder: It adds two bits, without considering a carry from the previous addition.



Full adder: It compute the sum of bit and the carry bit when two bits and carry are added.

The input are the bits x and y and the carry c_i . The outputs are the sum bit s and the new carry c_{i+1} .

0	utp	and ut fo	r	er.
1	npu	ıt	o	utput
x	y	ci	s	c_{i+1}
1	1	1	1	1
1	1	0	0	1
1	0	1	0	1
1	0	0	1	0
0	1	1	0	1
0	1	0	1	0
0	0	1	1	0
0	0	0	0	0

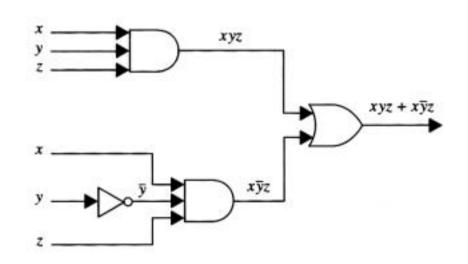


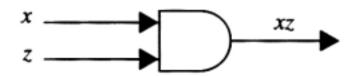
§11.4 — Minimization of Circuits

$$xyz + x\overline{y}z = (y + \overline{y})(xz)$$
$$= 1 \cdot (xz)$$
$$= xz.$$

$$F(x, y, z) = xyz + xyz, \ G(x, z) = xz$$

$$\Rightarrow F(x,y,z) \equiv G(x,z).$$





Karnaugh Maps

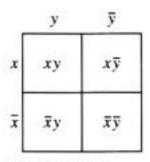


FIGURE 2 K-maps in Two Variables.



Maurice Karnaugh 1924 -

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Example 1 (P. 721): Find the K-maps for (a) xy + xy, (b) xy + xy, and (c) xy + xy + xy.

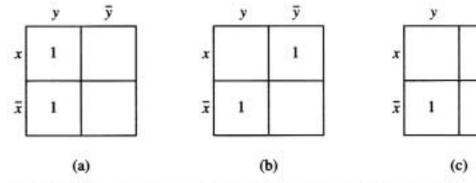


FIGURE 3 K-maps for the Sum-of-Products Expansions in Example 1.

Karnaugh Maps

(1) If there are 1s in two adjacent cells in the K-map, the minterm represented by these cells can be combined into a product involving just one of the variables.

e.g.
$$x\overline{y} + x\overline{y} = (x + x)\overline{y}$$
.

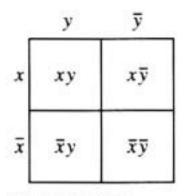


FIGURE 2 K-maps in Two Variables.

- (2) If 1s are in all four cells, the four minterms can be combined into one term, i.e. 1 involving none of the variables.
- (3) To circle the blocks of cells that can be combined. The goal is to identify the largest possible blocks, and to cover all the 1's with the fewest blocks using the largest blocks first and always using the largest possible blocks.

Goals of Circuit Minimization

- (1) Minimize the number of primitive Boolean logic gates needed to implement the circuit.
 - Ultimately, this also roughly minimizes the number of transistors, the chip area, and the cost.
 - Also roughly minimizes the energy expenditure
 - among traditional irreversible circuits.
 - This will be our focus.
- (2) It is also often useful to minimize the number of combinational *stages* or logical *depth* of the circuit.
 - This roughly minimizes the *delay* or *latency* through the circuit, the time between input and output.

Minimizing DNF Expressions

- Using DNF (or CNF) guarantees there is always some circuit that implements any desired Boolean function.
 - However, it may be far larger than needed!
- We would like to find the *smallest* sum-ofproducts expression that yields a given function.
 - This will yield a fairly small circuit.
 - However, circuits of other forms (not CNF or DNF)
 might be even smaller for complex functions.

Example 2 (P. 721): Simplify the sum-of products expansions

(a)
$$xy + xy$$
, (b) $xy + xy$, and (c) $xy + xy + xy$.

Solution:

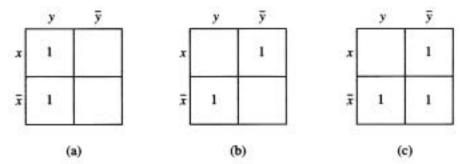


FIGURE 3 K-maps for the Sum-of-Products Expansions in Example 1.

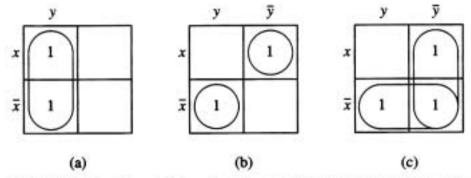
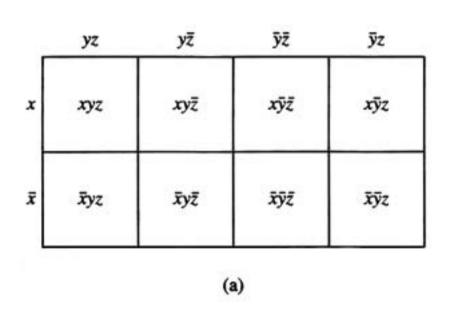
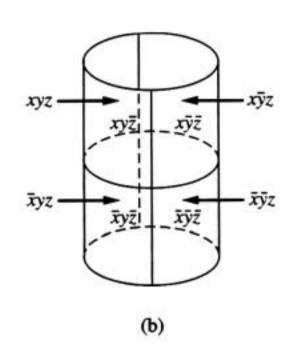


FIGURE 4 Simplifying the Sum-of-Products Expansion from Example 1.

$$\Rightarrow$$
 (a) y , (b) $xy + xy$, and (c) $x + y$.

To form a K-map in three variables. Such a map can be thought of as lying on a cylinder. On the cylinder two cells have a common border if and only if they are adjacent.





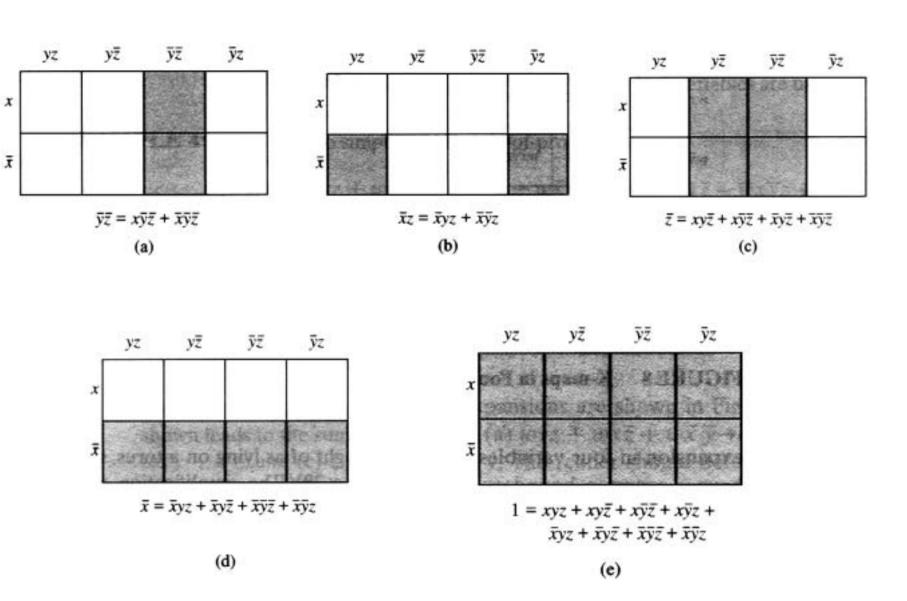


FIGURE 6 Blocks in K-maps in Three Variables.

Example 3 (P. 722): Simplify the sum-of products expansions

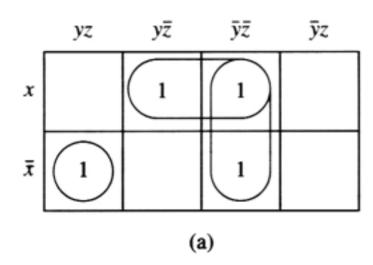
$$(a) xyz + xyz + xyz + xyz$$

$$(b) xyz + xyz + xyz + xyz + xyz$$

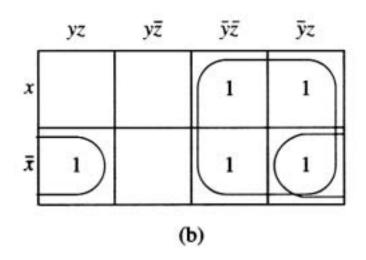
$$(c) x y z + x y z + x y z + x y z + x y z + x y z + x y z + x y z$$

$$(d) x y z + x y z + x y z + x y z$$

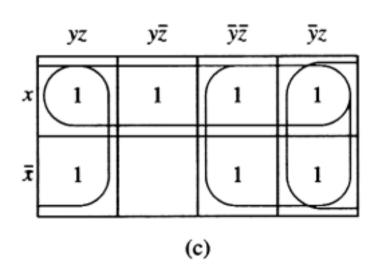
Solution:



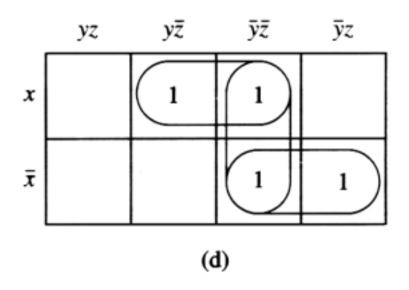
$$\Rightarrow xyz + xyz + xyz + xyz = xz + yz + xyz$$



$$\Rightarrow xyz + xyz + xyz + xyz + xyz = y + xz$$



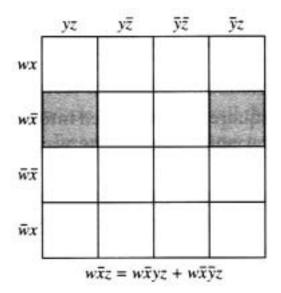
$$\Rightarrow xyz + xyz + xyz + xyz + xyz + xyz + xyz = x+y+z$$

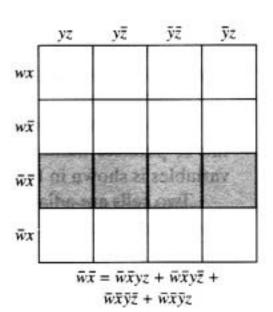


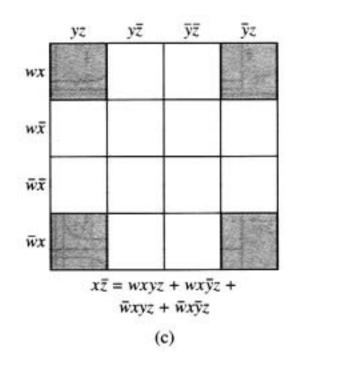
$$\Rightarrow xyz + xyz + xyz + xyz = xz + xy$$

ľ	yz	уī	ÿξ	ÿz
wx	wxyz	wxyż	wxÿź	wxÿz
wx	wxyz	wxyz	wxÿż	wxÿz
ŵπ	w̄xyz	₩য়yয়	wxyz	wxÿz
ŵχ	ŵxyz	w̄xyz̄	wxyż	wxÿz

FIGURE 8 K-maps in Four Variables.







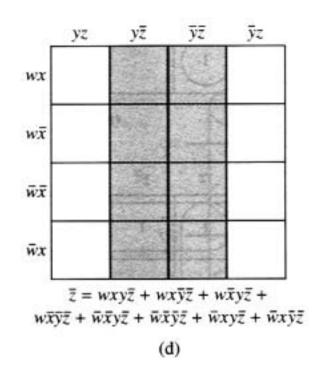


FIGURE 9 Blocks in K-maps in Four Variables.

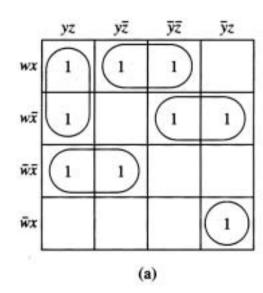
Example 4 (P. 725): Simplify the sum-of products expansions

$$(a) wxyz + wxy$$

$$(b) wxyz + wxyz + wxyz + wxyz + wxyz + wxyz + wxyz$$

$$(c) wxyz + wxy$$

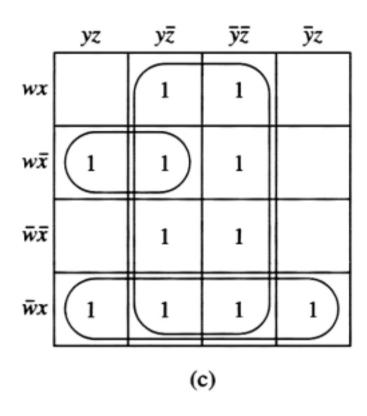
Solution:



 $\Rightarrow wxyz + wxyz$

(b) wxyz + wxyz + wxyz + wxyz + wxyz + wxyz + wxyz

1
1
1
1



$$\Rightarrow wxyz + wxyz$$

$$= z + wx + wxy$$

Don't Care Conditions

In some circuits, some combinations of input values are not possible or never occur.

Example 8 (P. 727):

$$f(x) = \begin{cases} 1, & \text{if } 5 \le x \le 10 \\ 0, & \text{if } 0 \le x \le 4. \end{cases}$$

$$F(w, x, y, z) = \begin{cases} 1, & \text{if } 5 \le (wxyz)_2 \le 10, \\ 1, & \text{if } 0 \le (wxyz)_2 \le 4. \end{cases}$$

Construct a circuit using *OR* gates, *AND* gates, and inverters only.

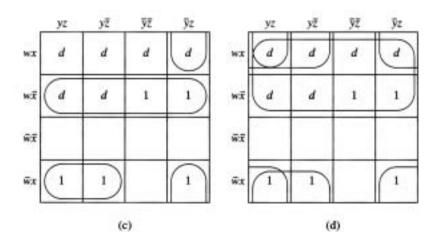
TABLE 1								
Digit	w	x	у	z	F			
0	0	0	0	0	0			
1	0	0	0	1	0			
2	0	0	1	0	0			
3	0	0	1	1	0			
4	0	1	0	0	0			
5	0	1	0	1	1			
6	0	1	1	0	1			
7	0	1	1	1	1			
8	1	0	0	0	1			
9	1	0	0	1	1			

By sum- of- products expansion,

$$F(w,x,y,z) = wxyz + wxyz + wxyz + wxyz + wxyz.$$

TABLE 1							
Digit	w	x	у	z	F		
0	0	0	0	0	0		
1	0	0	0	1	0		
2	0	0	1	0	0		
3	0	0	1	1	0		
4	0	1	0	0	0		
5	0	1	0	1	1		
6	0	1	1	0	1		
7	0	1	1	1	1		
8	1	0	0	0	1		
9	1	0	0	1	1		

ř	yz	уž	ÿ₹	ÿτ		yz	уž	ÿž	ÿz
wx	d	d	d	d	wx	d	d	d	d
wā	d	d	1	1	wī	d	d	(1	-
WX					ŵī			8	
йx	1	1	*	1	Øχ	1	1)		Ī
Ť	E = 18	6	1)	3	•		(1	b)	



$$\Rightarrow F(w, x, y, z) = w + x y + x z.$$

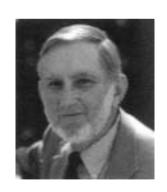
The Quine-McCluskey Method

K-maps are awkward to use when there are more than four variables. Furthermore, the use of K-maps relies on visual inspection to identify terms to group.

The **Quine-McCluskey method** can be used for Boolean functions in any number of variables.

Quine-McCluskey method

- (1) The first part finds those terms that are candidate for inclusion in minimal expansion as a Boolean sum of Boolean products.
- (2) The second part determine which of these terms to actually use.



Edward J. McCluskey 1929 -



Willard Van Orman Quine 1908 - 2000

Example 9 (P.728): Use Quine-McCluskey method to find a minimal expansion which is equivalent to

Solution: x y z + x y z + x y z + x y z + x y z.

TABLE 2						
Minterm	Bit String	Number of 1s				
xyz	111	3				
xyz	101	2				
$\overline{x}yz$	011	2				
$\overline{x} \overline{y}z$	001	1				
$\overline{x}\overline{y}\overline{z}$	000	0				

				Step 1			Step 2	
	Term	Bit String		Term	String		Term	String
1	xyz	111	(1,2)	xz	1-1	(1,2,3,4)	z	1
2	$x\overline{y}z$	101	(1,3)	yz	-11	33		
3	$\overline{x}yz$	011	(2,4)	$\overline{y}z$	-01			
4	$\overline{x} \overline{y}z$	001	(3,4)	$\overline{x}z$	0-1			
5	$\overline{x}\overline{y}\overline{z}$	000	(4,5)	$\overline{x}\overline{y}$	00-			

x y z + x y z + x y z + x y z + x y z.

TABLE 2						
Minterm	Bit String	Number of 1s				
xyz	111	3				
xyz	101	2				
$\overline{x}yz$	011	2				
$\overline{x} \overline{y}z$	001	1				
$\overline{x}\overline{y}\overline{z}$	000	0				

TABLE 4								
	xyz	xyz	x yz	$\overline{x} \overline{y}z$	$\overline{x} \overline{y} \overline{z}$			
z	X	X	х	х				
$\overline{x}\overline{y}$				х	х			

				Step 1			Step 2	
	Term	Bit String		Term	String		Term	String
1	xyz	111	(1,2)	xz	1-1	(1,2,3,4)	z	1
2	$x\overline{y}z$	101	(1,3)	yz	-11	34 62		
3	$\bar{x}yz$	011	(2,4)	$\overline{y}z$	-01			
4	$\overline{x} \overline{y}z$	001	(3,4)	$\bar{x}z$	0–1			
5	$\overline{x}\overline{y}\overline{z}$	000	(4,5)	$\overline{x}\overline{y}$	00-			

$$\Rightarrow xyz + xyz + xyz + xyz + xyz = z + xy.$$

Example 10 (P.731): Use Quine-McCluskey method to find a minimal expansion which is equivalent to

wxyz + wxyz + wxyz + wxyz + wxyz + wxyz. Solution:

Term	Bit String	Number of 1s
wxyz	1110	3
$w\overline{x}yz$	1011	3
$\overline{w}xyz$	0111	3
$w\overline{x}y\overline{z}$	1010	2
$\overline{w}x\overline{y}z$	0101	2
$\overline{w}\overline{x}yz$	0011	2

wxyz + wxyz + wxyz + wxyz + wxyz + wxyz.

			Step 1			Step 2		
	Term	Bit String		Term	String		Term	String
1	wxy₹	1110	(1,4)	wy₹	1-10	(3,5,6,7)	$\overline{w}z$	01
2	$w\overline{x}yz$	1011	(2,4)	$w\overline{x}y$	101-			
3	$\overline{w}xyz$	0111	(2,6)	$\overline{x}yz$	-011			
4	$w\overline{x}y\overline{z}$	1010	(3,5)	$\overline{w}xz$	01-1			
5	$\overline{w}x\overline{y}z$	0101	(3,6)	$\overline{w}yz$	0-11			
6	$\overline{w} \overline{x} yz$	0011	(5,7)	$\overline{w} \overline{y} z$	0-01			
7	$\overline{w} \overline{x} \overline{y} z$	0001	(6,7)	$\overline{w} \overline{x} z$	00-1			

TABLI	TABLE 7								
	wxy z	wxyz	wxyz	w≅y₹	$\overline{w}x\overline{y}z$	w x yz	$\overline{w} \overline{x} \overline{y} z$		
$\overline{w}z$			х		х	х	x		
wyz	х			х					
w⊼y		х		х					
₹yz		х				х			

wxyz + wxyz + wxyz + wxyz + wxyz + wxyz

$$= wz + wvz + wxv$$
 or $wz + wvz + xvz$.