

SARDAR VALLABHBHAI PATEL INSTITUTE OF TECHNOLOGY, VASAD
COMPUTER ENGINEERING DEPARTMENT
SYLLABUS FOR MID SEM 2 EXAM

Subject Name: Computer Organization and Architecture (3140707)

Unit 3

Assembly Language Programming:

Arithmetic and logic operations, looping constructs, Subroutines, I-O Programming.

Unit 5

Central Processing Unit

Introduction, General Register Organization, Stack Organization, Instruction format, Addressing Modes, Data transfer and manipulation, Program control, Reduced Instruction Set Computer (RISC) & Complex Instruction Set Computer (CISC)

Unit 7

Computer Arithmetic

Introduction, Addition and subtraction (Hardware implementation and flowchart), Multiplication Algorithms - Booth Multiplication Algorithm (Hardware implementation and flowchart), Division Algorithms, Floating Point Arithmetic operations, Decimal Arithmetic Unit.