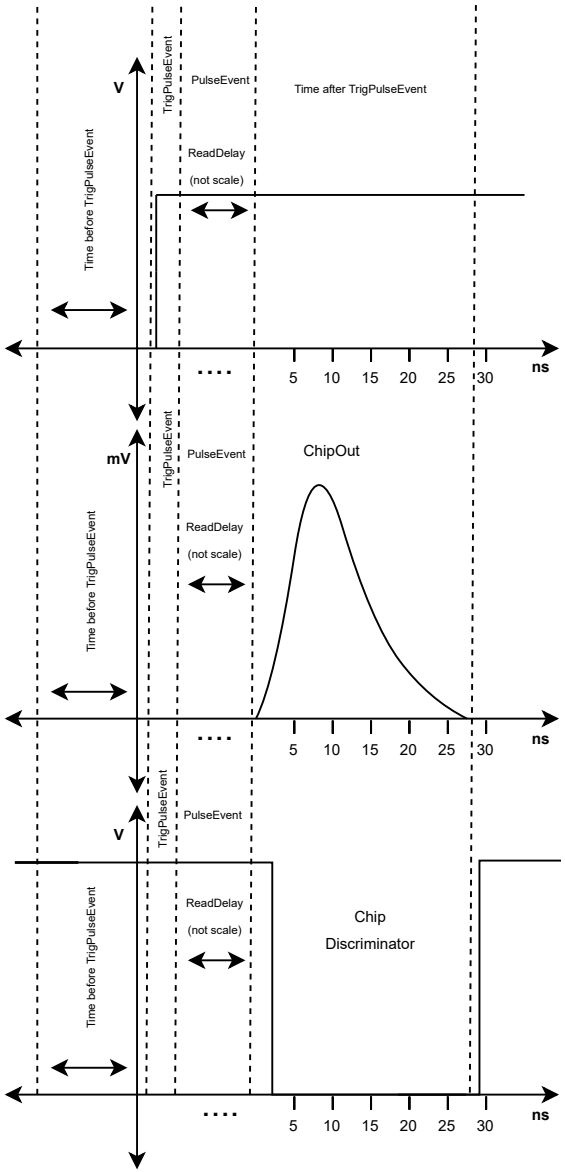
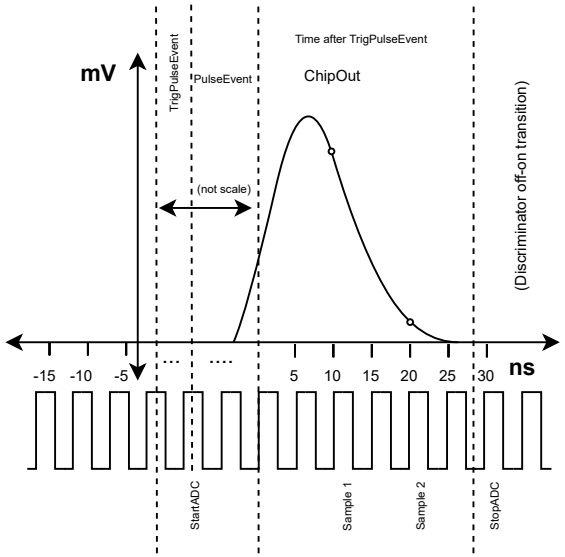

V
TrigPulseEvent
PulseEvent
Time after TrigPulseEvent
ReadDelay
(not scale)
Time before TrigPulseEvent
5
10
15
20
25
30
ns
mV
ChipOut
V
Chip
Discriminator

FpgaClk = 200Mhz

Ts = 100Msps

tStartSampling = tTriggerPulse
(StartADC)

tStopSampling = tDiscOffOn
(StopADC) = (time off-on transition)

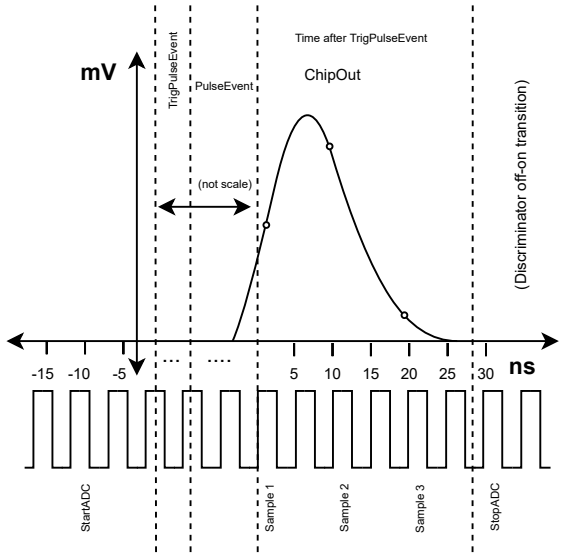


FpgaClk = 200Mhz

Ts = 100Msps

tStartSampling = 2 cycles before tTriggerPulse
(StartADC)

tStopSampling = tDiscOffOn
(StopADC) = (time off-on transition)

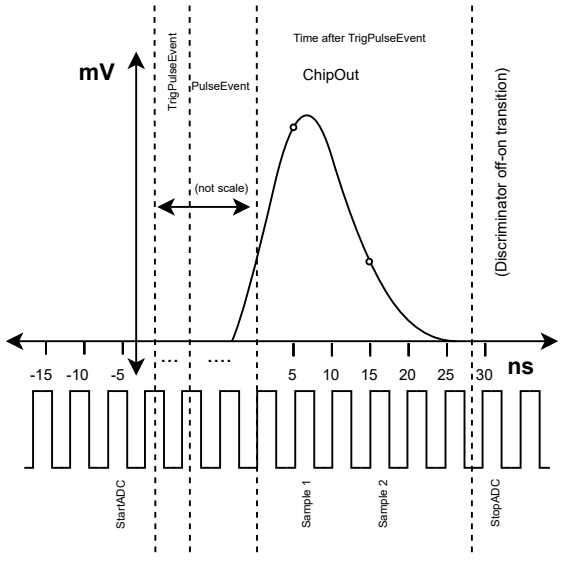


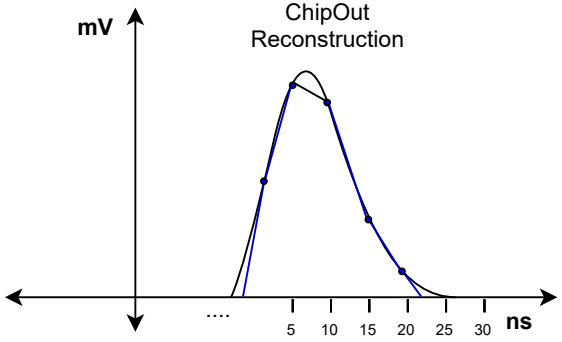
FpgaClk = 200Mhz

Ts = 100Msps

tStartSampling = 1 cycle before tTriggerPulse
(StartADC)

tStopSampling = tDiscOffOn
(StopADC) = (time off-on transition)



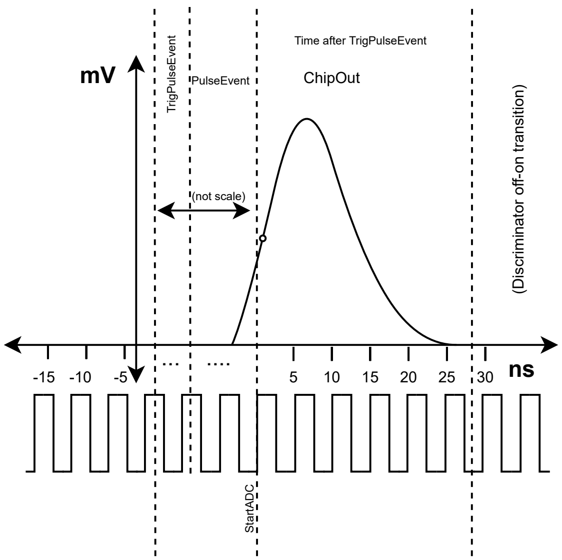


Low Ts, fast Tad ADC

FpgaClk = 200Mhz tStopSampling = one cicle after to start

tStartSampling = Up to you (StopADC) = (time off-on transition)

(StartADC)

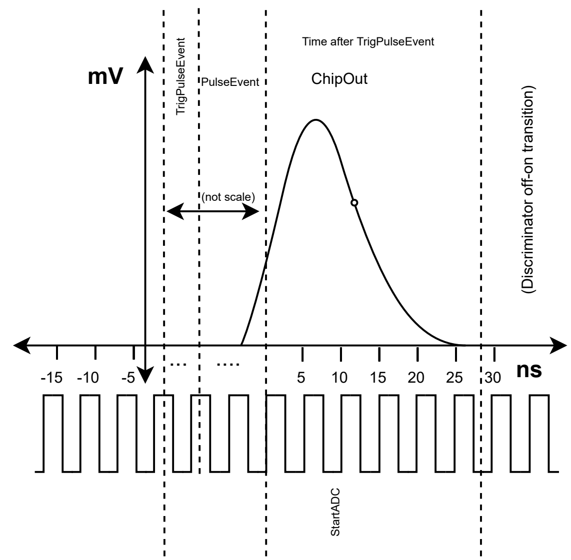


Low Ts, fast Tad ADC

FpgaClk = 200Mhz tStopSampling = one cicle after to start

tStartSampling = Up to you (StopADC) = (time off-on transition)

(StartADC)

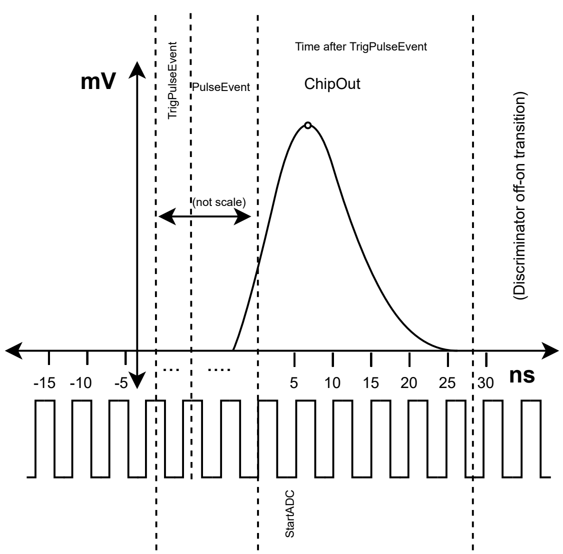


Low Ts, fast Tad ADC

FpgaClk = 200Mhz tStopSampling = one cicle after to start

tStartSampling = Up to you (StopADC) = (time off-on transition)

(StartADC)

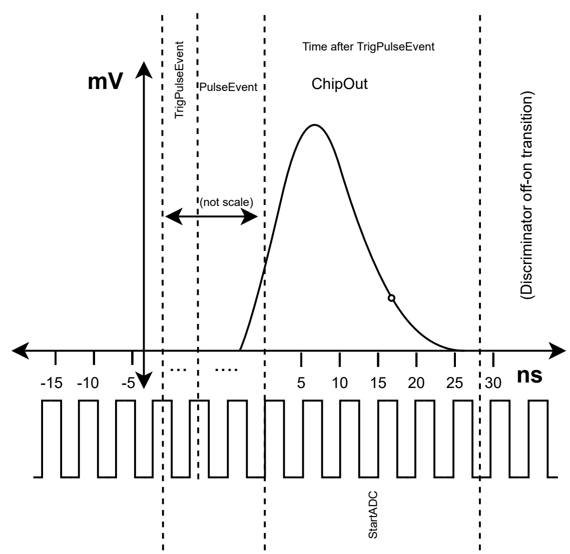


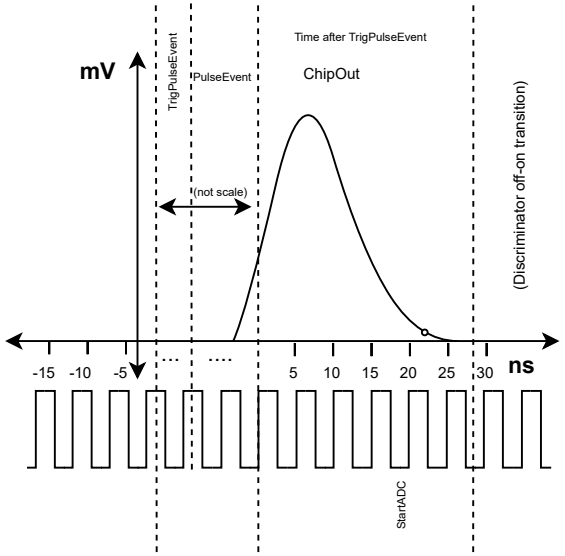
Low Ts, fast Tad ADC

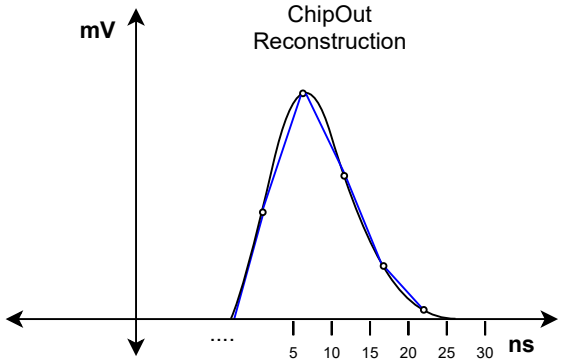
FpgaClk = 200Mhz tStopSampling = one cicle after to start

tStartSampling = Up to you (StopADC) = (time off-on transition)

(StartADC)







1 adc por cada salida de los canales?

1 adc por canal y multiplexar?

1 adc para toda la pcb multiplexado?