Additional and the control of the co	clk=1 adc_clk=0 we=1 rst=0 internal_count[10:0]=000	Timo		5940 ns	s	595	90 ns	5	960 ns	59	V rs	5980	ns en	5990	l is	61	IS	6	11 0 ns	602	20 ns	6030	TS ST	6040	IS	6050	IS	6060 1	IS	6070 ns		6080 lns
adc_clk=0 we=1 rst=0 internal_count[10:0]=000	adc_clk=0 we=1 rst=0 internal_count[10:0]=000 006 007 008 009 000 001 002 003 004 005 006 007 008 009 000 001 002 003 004 005 trig_count[15:0]=2 fsm_counter_repeat[15:0]=2 trigger_vdd[3:0]=0 trigger_vdd[3]=0 trigger_vdd[3]=0 trigger_vdd[1]=0 trigger_vdd[0]=1 trigger_gdd[3]=0 tr	11111C c1k=1						11111	11111		1 1 1 1 1 1					Т		111111			11111			1111		Ш	11111					\blacksquare
we-1 rst=0 internal_count[10:0]=000 006 007 008 009 000 001 002 003 004 005 007 008 009 000 001 002 003 004 005 006 007 008 009 000 001 002 003 004 005 006 007 008 009 000 001 002 003 004 005 006 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 005 007 008 009 000 001 002 003 004 005 005 005 005 007 008 009 000 001 002 003 004 005 005 005 005 007 008 009 000 001 002 003 004 005 005 005 007 008 009 000 001 002 003 004 005 005 005 005 007 008 009 000 001 002 003 004 005 005 005 005 005 005 005 005 005	we-1 rst=0 internal_count[10:0]=000 \(\)006 \(\)007 \(\)008 \(\)009 \(\)000 \(\)001 \(\)002 \(\)003 \(\)006 \(\)007 \(\)008 \(\)009 \(\)000 \(\)001 \(\)002 \(\)003 \(\)004 \(\)005 \(\)006 \(\)007 \(\)008 \(\)009 \(\)000 \(\)001 \(\)002 \(\)003 \(\)004 \(\)005 \(\)006 \(\)007 \(\)008 \(\)009 \(\)000 \(\)001 \(\)002 \(\)003 \(\)004 \(\)005 \(\)006 \(\)007 \(\)008 \(\)009 \(\)000 \(\)001 \(\)002 \(\)003 \(\)004 \(\)005 \(\)006 \(\)007 \(\)008 \(\)009 \(\)000 \(\)001 \(\)002 \(\)003 \(\)004 \(\)005 \(\)006 \(\)007 \(\)008 \(\)009 \(\)000 \(\)001 \(\)00			'					_	<u> </u>							=															
rst=0 internal_count[10:0]=000	rst=0 internal_count[10:0]=000	we=1					_		_								=															=
<pre>internal_count[10:0]=000</pre>	<pre>internal_count[10:0]=000</pre>																															-+
trig_count[15:0]=12 10 M1 M2 M3 fsm_counter_repeat[15:0]=2 0 1 M2 M3 trigger_sel[3:0]=0 0 1 M2 M3 trigger_vdd[3:0]=1 0 M1 M2 M3 trigger_vdd[3:0]=1 0 M1 M2 M3 trigger_vdd[3]=0 0 M3 M3 trigger_gnd[3:0]=0 1 M3 M3 trigger_gnd[3]=0 0 M3 M3 trigger_gnd[1]=0 0 M3 M3 trigger_gnd[0]=0 0 M3 M3	trig_count[15:0]=12		Vone	V 0.07	Vnno	Vnno	Ynnn	Vnn-	1 V nn	o Vono	Vnna	Vnns	Vnns	Vnna	Vnno	Vnna	Inc	nn Y nn	Vnna	Vnna	Vnna	Vnns	Vnns	Vnna	Vnno	Vnna	Vonn	Y 001	Vnno	Vnna	Vnna	Vnns
fsm_counter_repeat[15:0]=2 I 2 3 trigger_sel[3:0]=0 I I 0 trigger_vdd[3:0]=1 I I I trigger_vdd[3]=0 I I I trigger_vdd[1]=0 I I I trigger_gnd[3:0]=0 I I I trigger_gnd[2]=0 I I I trigger_gnd[1]=0 I I I trigger_gnd[0]=0 I I I	fsm_counter_repeat[15:0]=2 I 2 3 trigger_sel[3:0]=0 I I I trigger_vdd[3:0]=1 I I I trigger_vdd[3]=0 I I I trigger_vdd[1]=0 I I I trigger_gnd[3:0]=0 I I I trigger_gnd[3]=0 I I I trigger_gnd[1]=0 I I I trigger_gnd[0]=0 I I I			N 007	Vono	VOOS				<u> </u>	V004	Voos	Vono	N 007	Vono	N 003			, , 002		Vona	Vona	Vono	N 007	Vono	N 003		VOOT	<u></u>	Voos	Vona	V002
trigger_sel[3:0]=0 0 [1 [0 trigger_vdd[3]=0 [1 [0 trigger_vdd[2]=0 0 0 trigger_vdd[1]=0 0 0 trigger_vdd[0]=1 0 0 trigger_gnd[3:0]=0 0 0 0 trigger_gnd[2]=0 0 0 0 trigger_gnd[1]=0 0 0 0 trigger_gnd[0]=0 0 0 0	trigger_sel[3:0]=0						VTT									V 1	-										V _T 2			_		=
trigger_vdd[3:0]=1 0 [1 [0 trigger_vdd[3]=0 0 0 trigger_vdd[2]=0 0 0 trigger_vdd[1]=0 0 0 trigger_gnd[3:0]=1 0 0 trigger_gnd[3]=0 0 0 trigger_gnd[2]=0 0 0 trigger_gnd[1]=0 0 0 trigger_gnd[0]=0 0 0	trigger_vdd[3:0]=1 0	twisses sel[2:0]=2	0													<u>λ</u> -																=
trigger_vdd[3]=0 1 trigger_vdd[1]=0 2 trigger_vdd[0]=1 3 trigger_gnd[3:0]=0 1 trigger_gnd[3]=0 1 trigger_gnd[1]=0 1 trigger_gnd[0]=0 1	trigger_vdd[3]=0 trigger_vdd[2]=0 trigger_vdd[1]=0 trigger_vdd[0]=1 trigger_gnd[3:0]=0 trigger_gnd[3]=0 trigger_gnd[2]=0 trigger_gnd[2]=0 trigger_gnd[0]=0 trigger_gnd[0]=0		0													V 1			Vn													=
trigger_vdd[2]=0 trigger_vdd[1]=0 trigger_vdd[0]=1 trigger_gnd[3:0]=0 trigger_gnd[3]=0 [0] trigger_gnd[2]=0 trigger_gnd[1]=0 trigger_gnd[0]=0 [0]	trigger_vdd[2]=0 trigger_vdd[1]=0 trigger_vdd[0]=1 trigger_gnd[3:0]=0 trigger_gnd[3]=0 trigger_gnd[2]=0 trigger_gnd[1]=0 trigger_gnd[0]=0 trigger_gnd[0]=0	trigger_vad[3.0]=1	U													<u>\</u>																\rightarrow
trigger_vdd[1]=0	trigger_vdd[1]=0		_	-					_								-															=
trigger_vdd[0]=1 0 1 trigger_gnd[3:0]=0 1 0 1 trigger_gnd[3]=0 0 1 0 trigger_gnd[2]=0 0 0 0 trigger_gnd[1]=0 0 0 0 trigger_gnd[0]=0 0 0 0	trigger_vdd[0]=1 0 1 trigger_gnd[3:0]=0 1 0 1 trigger_gnd[3]=0 0 1 0 trigger_gnd[2]=0 0 0 0 trigger_gnd[1]=0 0 0 0 0 trigger_gnd[0]=0 0<	trigger_vdd[2]=0	-														-															=
trigger_gnd[3:0]=0]0]1 trigger_gnd[3]=0 trigger_gnd[2]=0 trigger_gnd[1]=0 trigger_gnd[0]=0	trigger_gnd[3:0]=0		-														+		_													\dashv
trigger_gnd[3]=0 trigger_gnd[2]=0 trigger_gnd[1]=0 trigger_gnd[0]=0	trigger_gnd[3]=0 trigger_gnd[2]=0 trigger_gnd[1]=0 trigger_gnd[0]=0		-											Vo			\perp			V-												=
trigger_gnd[2]=0 trigger_gnd[1]=0 trigger_gnd[0]=0 trigger_gnd[0]=0	trigger_gnd[2]=0 trigger_gnd[1]=0 trigger_gnd[0]=0	trigger_gnd[3:0]=0	1	-										NU.			+			<u>l</u> l												+
trigger_gnd[1]=0 trigger_gnd[0]=0	trigger_gnd[1]=0 trigger_gnd[0]=0		-														+											-				\dashv
trigger_gnd[0]=0	trigger_gnd[0]=0	trigger_gnd[2]=0	_	-													+											-				\dashv
Exiger_gmax uj=u	Exager_gnd(u)=U 92_1(13:0)=0004 000 000 000 000 000 000 0		_											_			+															=
SZ_11.5:0 J=W049 (000)	82_11.15*0J=0009 0000 0000 0000 00000 00000 00000 00000 0000	trigger_gnd[0]=0	2002																													
		s2_1[13:0]=0004	0000														00	004														