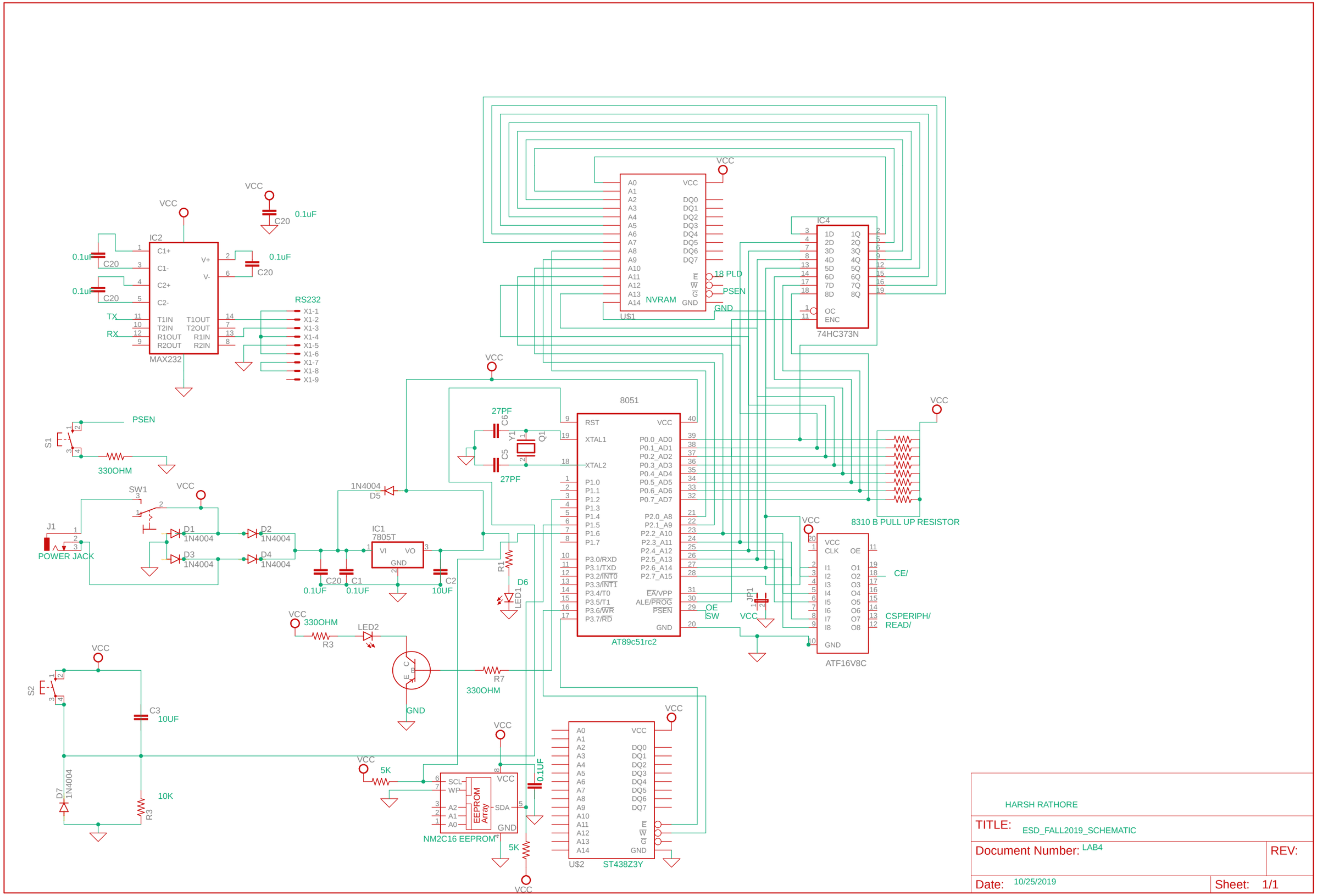
IC2
MAX232
C1+
C1-
C2+
C2-
V+
V-
T1IN
T2IN
R1OUT
R2OUT
T1OUT
T2OUT
R1IN
R2IN
TX
RX
RS232
X1-1
X1-2
X1-3
X1-4
X1-5
X1-6
X1-7
X1-8
X1-9
VCC
C20
0.1uF
PSEN
S1
330OHM
SW1
J1
POWER JACK
D1
D2
D3
D4
1N4004
IC1
7805T
VI
VO
GND
C1
C2
10UF
0.1UF
D5
R1
D6
LED1
LED2
R3
R7
S2
C3
D7
10K
5K
U$1
NVRAM
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
DQ0
DQ1
DQ2
DQ3
DQ4
DQ5
DQ6
DQ7
18 PLD
IC4
74HC373N
1D
2D
3D
4D
5D
6D
7D
8D
1Q
2Q
3Q
4Q
5Q
6Q
7Q
8Q
OC
ENC
8051
RST
XTAL1
XTAL2
P1.0
P1.1
P1.2
P1.3
P1.4
P1.5
P1.6
P1.7
P3.0/RXD
P3.1/TXD
P3.2/INT0
P3.3/INT1
P3.4/T0
P3.5/T1
P3.6/WR
P3.7/RD
P0.0_AD0
P0.1_AD1
P0.2_AD2
P0.3_AD3
P0.4_AD4
P0.5_AD5
P0.6_AD6
P0.7_AD7
P2.0_A8
P2.1_A9
P2.2_A10
P2.3_A11
P2.4_A12
P2.5_A13
P2.6_A14
P2.7_A15
EA/VPP
ALE/PROG
PSEN
AT89c51rc2
27PF
C6
C5
Y1
Q1
8310 B PULL UP RESISTOR
ATF16V8C
CE/
CSPERIPH/
READ/
OE
SW
JP1
NM2C16 EEPROM
SCL
WP
SDA
EEPROM Array
ST438Z3Y
U$2
HARSH RATHORE
TITLE: ESD_FALL2019_SCHEMATIC
Document Number: LAB4
REV:
Date: 10/25/2019
Sheet: 1/1