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1 # User Constraint File fuer Modulares System
2 # Autor: Friedrich, Daniel und Beyerstedt, Jannik
3
4 # Datum der Erstellung: Mittwoch, der 14. Januar 2015
5 # Xilinx Coolrunner
6 # Baustein XC2C256-PQG208-7C
7
8 # Komponenten, die an das Mainboard angeschlossen sind:
9 # Connector3: IOM-Board
10 # Connector4: IOM-Board
11
12 # NET SYCLK LOC = P55 | IOSTANDARD=LVCMOS33;
13 # Diesen Pin vorzugsweise fuer Clock verwenden.
14
15 # NET NRESET LOC = P206 | IOSTANDARD=LVCMOS33;
16 # Dieser Pin liegt auf der RESET-Taste des Mainboards, Low-aktiv
17
18 #-----#
19 # Input-Output Messboard (IOM) an Connector 3
20 #
21 ##### INPUTS #####
22 #NET IOM_IN_SW<0> LOC = P76 | IOSTANDARD=LVCMOS33; # IN_0
23 #NET IOM_IN_SW<1> LOC = P75 | IOSTANDARD=LVCMOS33; # IN_1
24 #NET IOM_IN_SW<2> LOC = P74 | IOSTANDARD=LVCMOS33; # IN_2
25 #NET IOM_IN_SW<3> LOC = P73 | IOSTANDARD=LVCMOS33; # IN_3
26 #NET IOM_IN_SW<4> LOC = P72 | IOSTANDARD=LVCMOS33; # IN_4
27 #NET IOM_IN_SW<5> LOC = P71 | IOSTANDARD=LVCMOS33; # IN_5
28 #NET IOM_IN_SW<6> LOC = P70 | IOSTANDARD=LVCMOS33; # IN_6
29 #NET IOM_IN_SW<7> LOC = P69 | IOSTANDARD=LVCMOS33; # IN_7
30 #
31 NET CLK LOC = P66 | IOSTANDARD=LVCMOS33; # IN_T0
32 NET INIT LOC = P65 | IOSTANDARD=LVCMOS33; # IN_T1
33 #
34 ##### OUTPUTS #####
35 NET MAX<0> LOC = P80 | IOSTANDARD=LVCMOS33; # OUT_0
36 NET MAX<1> LOC = P82 | IOSTANDARD=LVCMOS33; # OUT_1
37 NET MAX<2> LOC = P83 | IOSTANDARD=LVCMOS33; # OUT_2
38 NET MAX<3> LOC = P84 | IOSTANDARD=LVCMOS33; # OUT_3
39 NET MAX<4> LOC = P85 | IOSTANDARD=LVCMOS33; # OUT_4
40 NET MAX<5> LOC = P86 | IOSTANDARD=LVCMOS33; # OUT_5
41 NET MAX<6> LOC = P87 | IOSTANDARD=LVCMOS33; # OUT_6
42 #NET IOM_OUT<7> LOC = P88 | IOSTANDARD=LVCMOS33; # OUT_7
43 #
44 #-----#
45 # Input-Output Messboard (IOM) an Connector 4
46 #
47 ##### INPUTS #####
48 #NET IOM_IN_SW<0> LOC = P123 | IOSTANDARD=LVCMOS33; # IN_0
49 #NET IOM_IN_SW<1> LOC = P122 | IOSTANDARD=LVCMOS33; # IN_1
50 #NET IOM_IN_SW<2> LOC = P121 | IOSTANDARD=LVCMOS33; # IN_2
51 #NET IOM_IN_SW<3> LOC = P120 | IOSTANDARD=LVCMOS33; # IN_3
52 #NET IOM_IN_SW<4> LOC = P119 | IOSTANDARD=LVCMOS33; # IN_4
53 #NET IOM_IN_SW<5> LOC = P118 | IOSTANDARD=LVCMOS33; # IN_5
54 #NET IOM_IN_SW<6> LOC = P117 | IOSTANDARD=LVCMOS33; # IN_6
55 #NET IOM_IN_SW<7> LOC = P116 | IOSTANDARD=LVCMOS33; # IN_7
56 #
57 #NET IOM_IN_T<0> LOC = P114 | IOSTANDARD=LVCMOS33; # IN_T0
58 #NET IOM_IN_T<1> LOC = P113 | IOSTANDARD=LVCMOS33; # IN_T1
59 #
60 ##### OUTPUTS #####
61 NET RAND<0> LOC = P127 | IOSTANDARD=LVCMOS33; # OUT_0
62 NET RAND<1> LOC = P128 | IOSTANDARD=LVCMOS33; # OUT_1
63 NET RAND<2> LOC = P131 | IOSTANDARD=LVCMOS33; # OUT_2
64 NET RAND<3> LOC = P134 | IOSTANDARD=LVCMOS33; # OUT_3
65 NET RAND<4> LOC = P135 | IOSTANDARD=LVCMOS33; # OUT_4
66 NET RAND<5> LOC = P136 | IOSTANDARD=LVCMOS33; # OUT_5
67 #NET IOM_OUT<6> LOC = P137 | IOSTANDARD=LVCMOS33; # OUT_6
68 #NET IOM_OUT<7> LOC = P138 | IOSTANDARD=LVCMOS33; # OUT_7
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