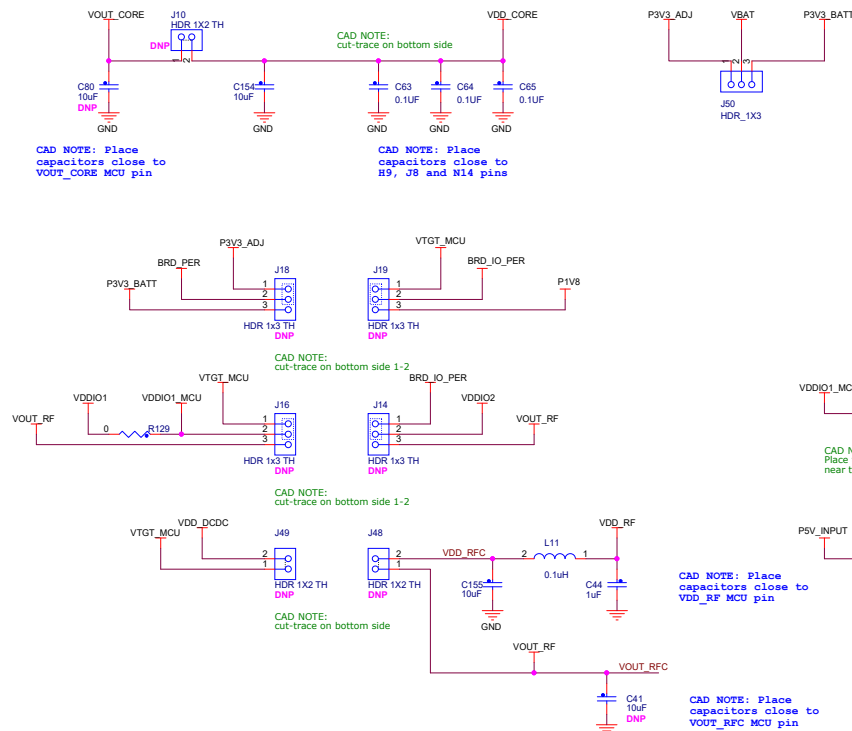
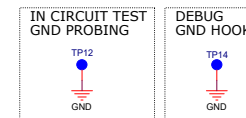
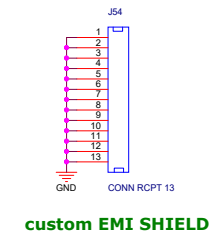
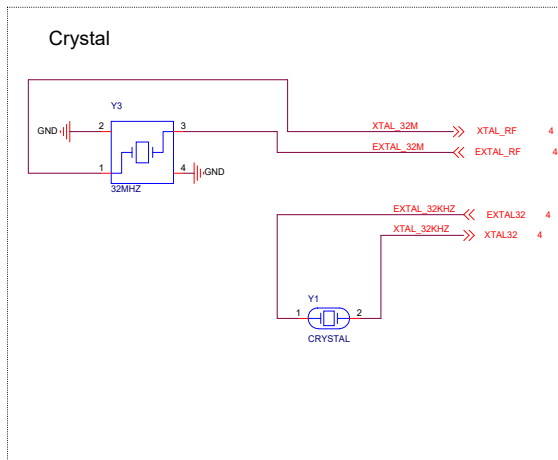
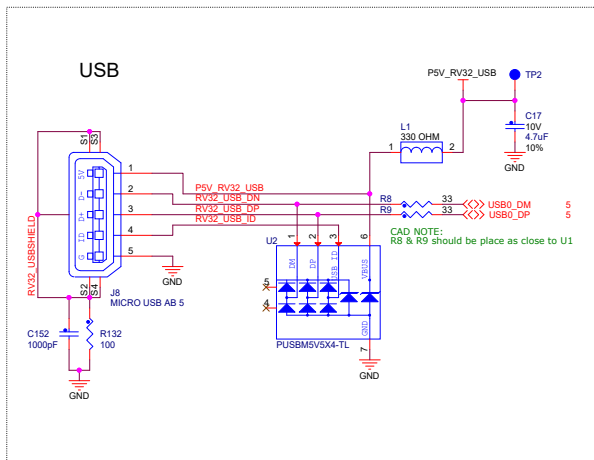
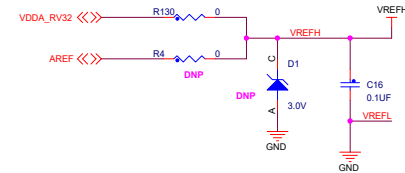
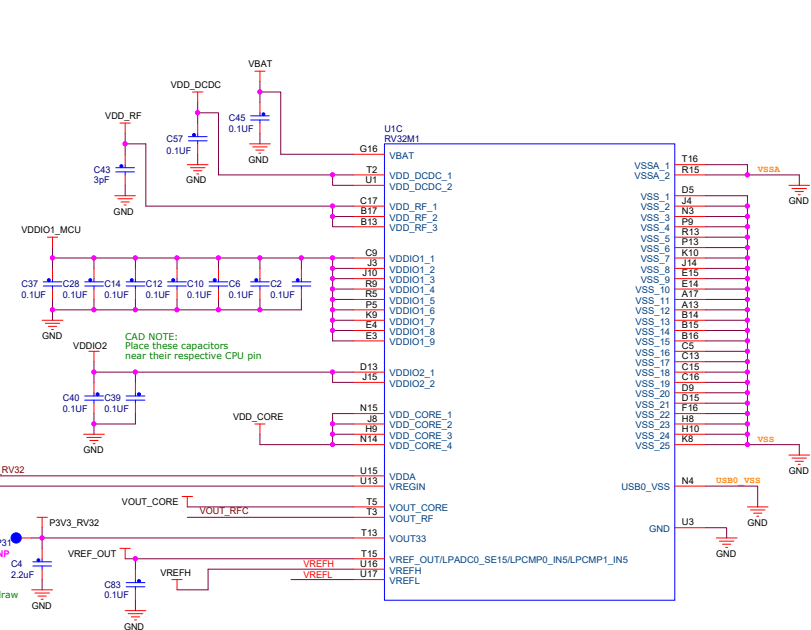


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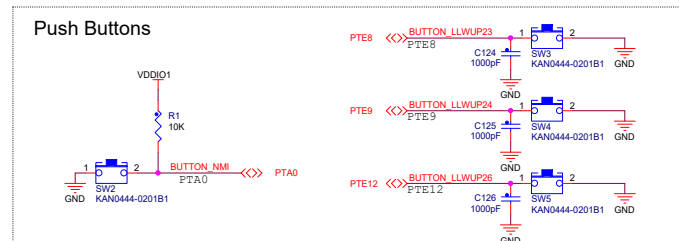
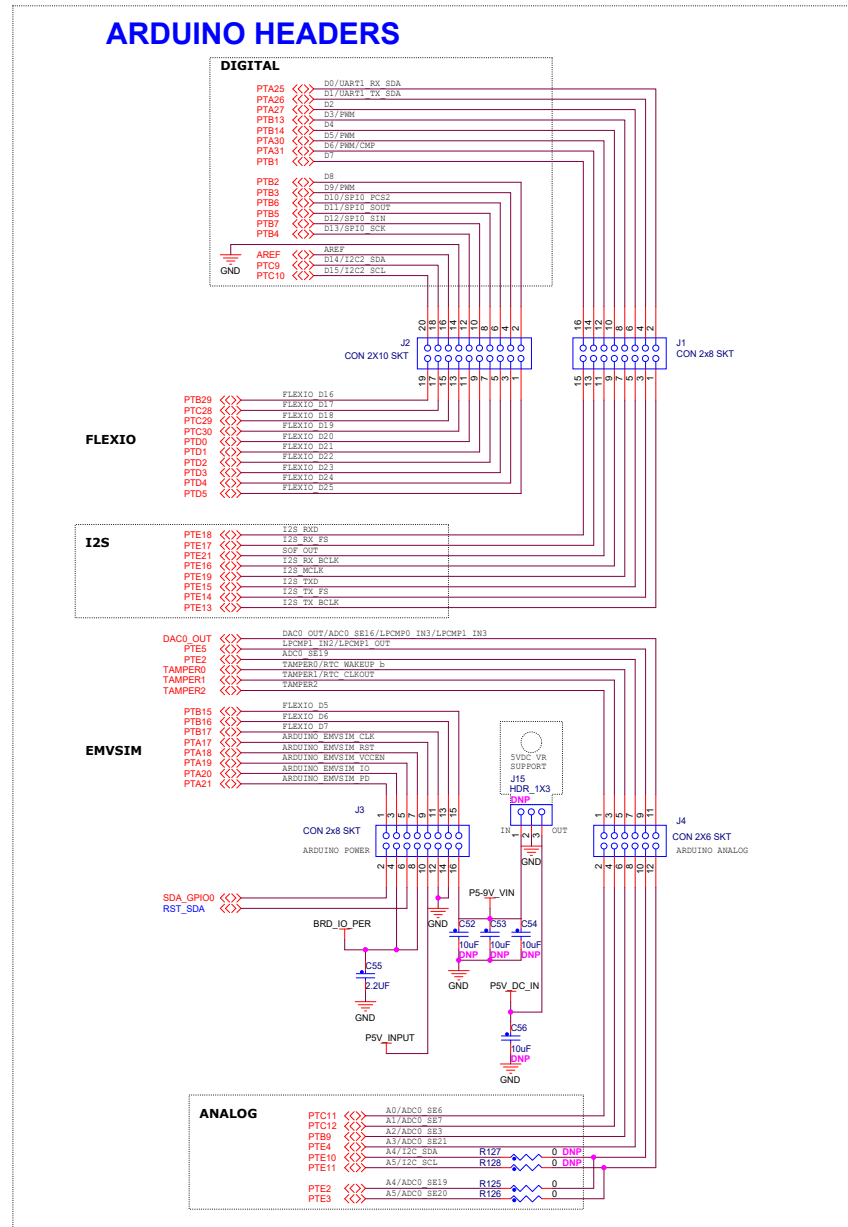
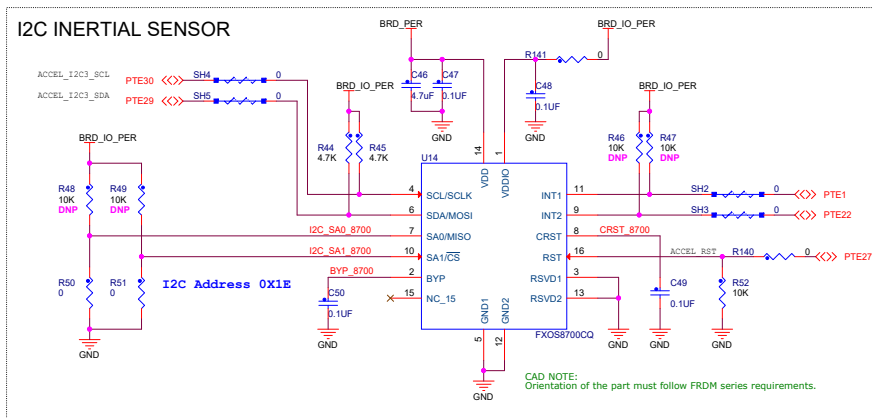
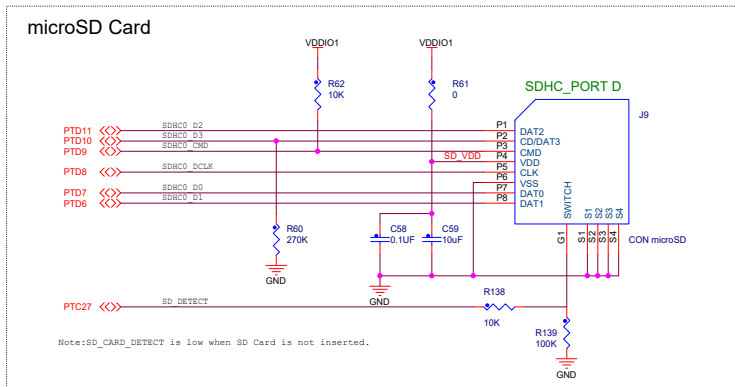
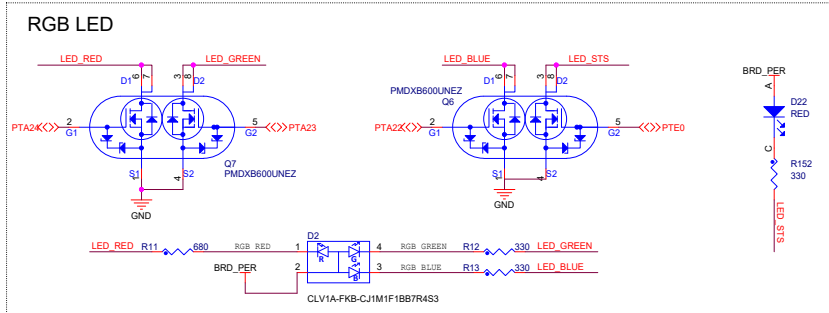
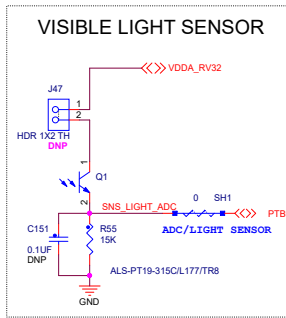
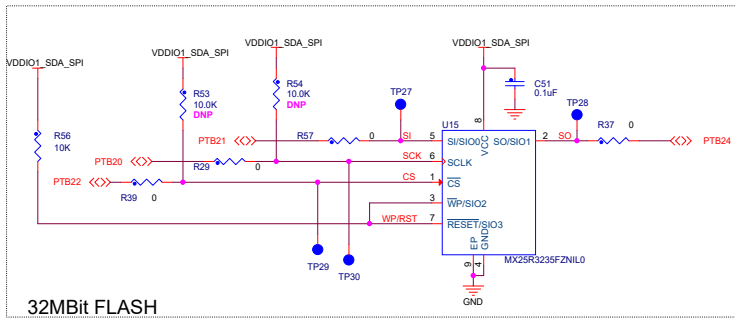
RV32M1-VEGA



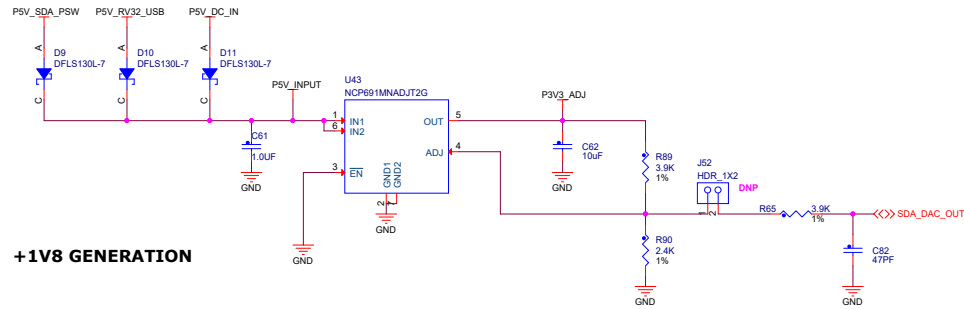
Description	J10	J48	J14	J16	J19	J18
Single Supply operation, IO @ 3.3V	1-2	1-2	1-2	1-2	1-2	
Single Supply operation, RF & IO @ 1.8V	1-2	1-2	2-3	2-3	Open	
Dual IO, 3.3V and 1.8V	1-2	1-2	1-2	1-2	2-3	
Full Bypass	Open	Open	Open	Open	Open	Open



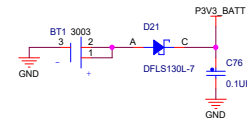
VEGA			
ICAP Classification: CP: IJO: PUB: X		Drawing Title: RV32M1-VEGA	
Page Title: MCU_INTERFACE_2			
Size C	Document Number SCH-34730, PDF: SPF-34730	Rev A1	
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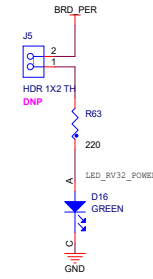
P3V3_ADJ



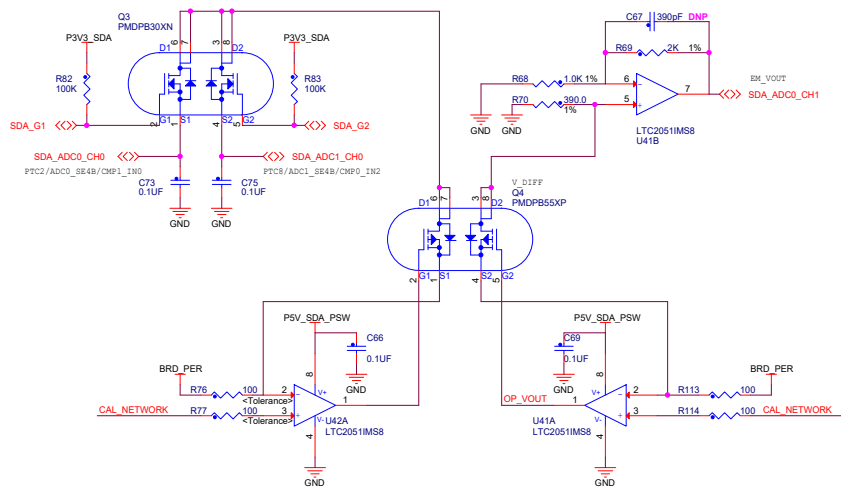
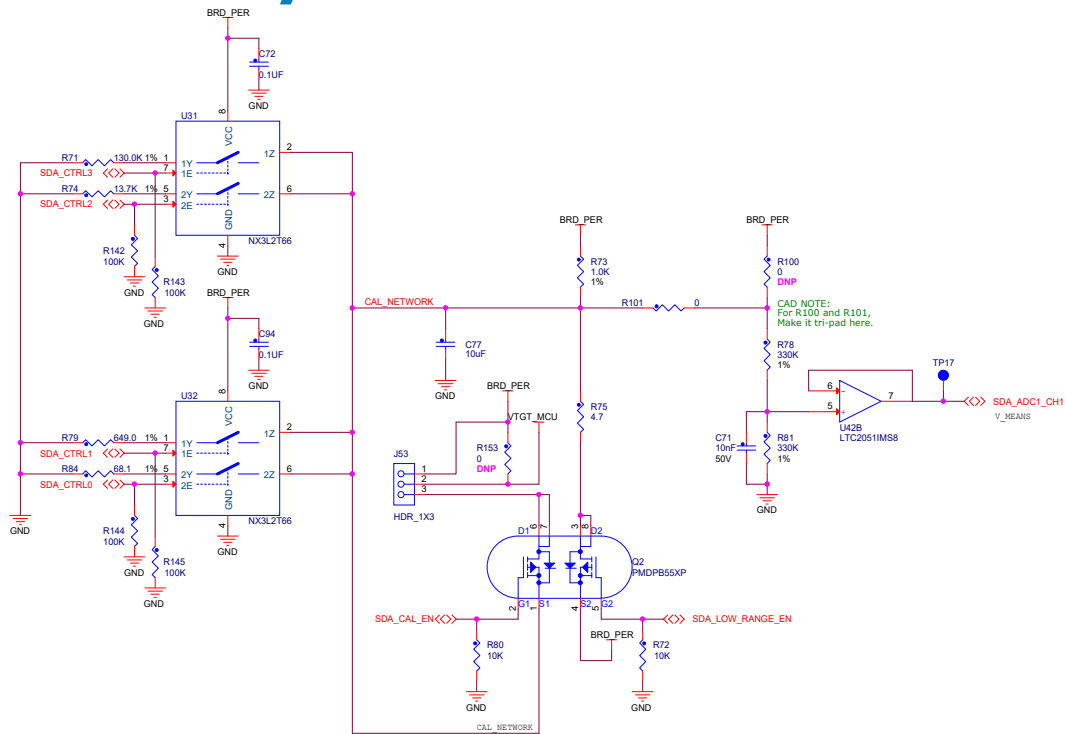
RV32 BATTERY



PWR LED



Circuitry for future enhancements



VEGA

ICAP Classification:		CP:	IUQ:	PUB:	X
Drawing Title:		RV32M1-VEGA			
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