



# HEART 2026 International Symposium on Highly Efficient Accelerators & Reconfigurable Technologies

## Heidelberg, Germany, June 17 – 19, 2026

### HEART 2026 – Call for Papers

Following its tradition, **HEART 2026** will continue to focus on high efficiency as a cross-cutting issue and seeks contributions for the main research track in, but not limited to, the following areas:

#### Architectures for Efficient Acceleration

- Novel systems/platforms based on FPGA, GPU, and other devices
- Heterogeneous processor architectures and systems for high-performance / low-power
- Domain-specific architectures

#### Design Methods and Tools for Efficient Acceleration

- Programming paradigms, languages, and frameworks
- High-level synthesis and compilers
- Runtime methodologies for heterogeneous systems
- Performance evaluation and analysis

#### Applications and Systems

- Application examples that benefit from efficient acceleration to a great extent
- Complete systems demonstrating increased energy efficiency and/or performance
- Comparisons between accelerator technologies and benchmarks

>> Non-traditional- and Emerging Computing <<

### Important Dates

Paper Submission  
**March 2<sup>nd</sup>, 2026**

Notification

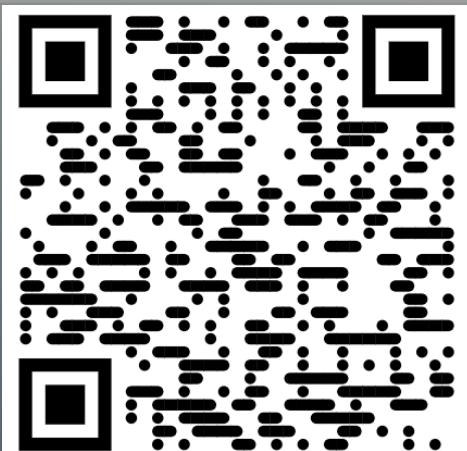
**April 13<sup>th</sup>, 2026**

Conference  
**June 17 – 19, 2026**

#### Contact

dirk.koch@uni-heidelberg.de

<https://heart2026.github.io/>





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