|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Etape** | **Fetch\_control\_bus\_o** | **Decode\_control\_bus\_o** | **Reg\_bank\_control\_bus\_o** | **Execute\_control\_bus\_o** | **Mem\_control\_bus\_o** | **Opcode\_supported\_uint** |
| **STEP\_1** | 0x0024 | 0x0011 | 0x0002 | 0x04d0 | 0x0000 | Sub\_r\_imm\_o |
| **STEP\_3** | 0x0020 | 0x0000 | 0x0002 | 0x2088 | 0x000d | Ldrb\_r\_r\_imm\_o |
| **STEP\_4** | 0x0030 | 0x0000 | 0x0002 | 0x2008 | 0x000d | ldrb\_r\_r\_r\_o |
| **STEP\_5** | 0x0020 | 0x0000 | 0x0002 | 0x0403 | 0x0000 | Lsr\_r\_r\_imm\_o |
| **STEP\_7** | 0x0000 | 0x0000 | 0x0002 | 0x0401 | 0x0000 | Asr\_r\_r\_imm\_o |
| **STEP\_10** | 0x0004 | 0x0010 | 0x0002 | 0x05c8 | 0x0000 | Mov\_r\_imm\_o |

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Etape** | **R0** | **R1** | **R2** | **R3** | **R4** | **SP** | **LR** | **PC** |
| **STEP\_1** | 0x0020 | 0x001a | 0x0026 | 0xfff4 | 0x0018 | 0x0000 | 0x0000 | 0x0010 |
| **STEP\_3** | 0x0020 | 0x0024 | 0xfff4 | 0xfff4 | 0x0018 | 0x0000 | 0x0000 | 0x001c |
| **STEP\_4** | 0x0000 | 0x0024 | 0xfff4 | 0xfff4 | 0x0018 | 0x0000 | 0x0000 | 0x0020 |
| **STEP\_5** | 0x0000 | 0x0024 | 0x0018 | 0xfff4 | 0x0000 | 0x0000 | 0x0000 | 0x002a |
| **STEP\_7** | 0x0002 | 0x0024 | 0x0002 | 0xfff4 | 0x001c | 0x0000 | 0x0000 | 0x003a |
| **STEP\_10** | 0x002a | 0x0000 | 0x0000 | 0xfff4 | 0x001c | 0x0000 | 0x0000 | 0x0062 |