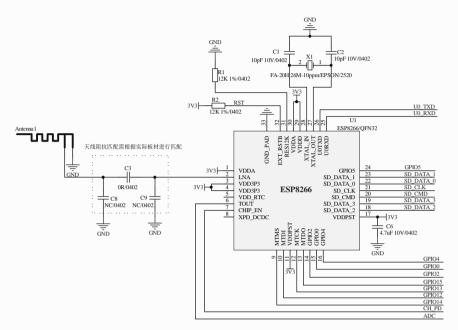
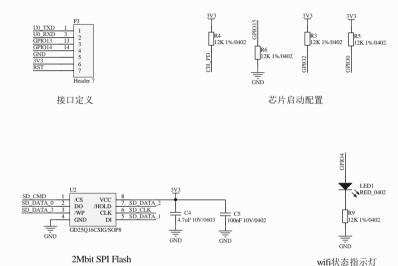
1 2 3 4 5 6 7 8

HEKR V1.1 wifi模块参考电路图



ESP8266



Piı	n Name	Function	Pin N	Name	Function	Pin	Name	Function					
1	VDDA	模拟电源3.0~3.3V	12 N	MTCK	GPIO13, HSPID	23	SDIO_DATA1	GPIO8, SD_MOSI					
2	LNA	射频天线接口	13 M	MTDO	GPIO15, HSPICS	24	GPIO5	GPIO5					
3	VDD3P3	PA电源3.3V	14 (GPIO2	GPIO2	25	U0RXD	GPIO3,					
4	VDD3P3	PA电源3.3V	15 (GPIO0	GPIO0, SPICS2	26	U0TXD	GPIO1, SPICS1					
5	VDD_RT	C NC/1.1V	16	GPIO4	GPIO4	27	XTAL_OUT	晶振输出					
6	TOUT	ADC输入引脚	17	VDDPST	数字和IO电源1.8~3.3V	V 28	XTAL_IN	晶振输入					
7	CHEP_E	N 芯片使能引脚: 高电平位	使能芯片 18 S	SDIO_DATA2	GPIO9, SD_2	29	VDDD	数字电源3.0~3.3V					
8	XPD_DC	DC sleep唤醒,GPIO16	19	SDIO_DATA3	GPIO10, SD_3	30	VDDA	模拟电源3.0~3.3V					
9	MTMS	GPIO14, HSPICLK	20 \$	SDIO_CMD	GPIO11, SD_CS	31	RES12K	串联12K电阻到地					
10	MTDI	GPIO12, HSPIQ	21 8	SDIO_CLK	GPIO6, SD_CLK	32	EXT_RSTB	外部复位引脚					D
11	VDDPST	数字和IO电源1.8~3.3V	22 8	SDIO_DATA0	GPIO7, SD_MISO								
									Title				
									Size	Number		Revision	+
									A3				
									Date: File:	2015/10/12 星期一 D:\HEKR项目\\Hekr V1.1.SchDoc	Sheet of Drawn By:		7
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