CoreAHBtoAPB3 v3.1 Release Notes

These release notes accompany the production release of CoreAHBtoAPB3 v3.1. This document provides details about the features, enhancements, system requirements, supported families, implementations, and known issues and workarounds.

Features

- Creates a bridge between advanced microcontroller bus architecture (AMBA®) advanced high-performance bus (AHB or AHB-Lite) and advanced peripheral bus (APB)
- Connects automatically to CoreAHB/CoreAHBLite and CoreAPB3 in SmartDesign
- Complies with AMBA 3 APB

Interfaces

CoreAHBtoAPB3 v3.1 supports an AHB or AHB-Lite slave interface connected to an AHB or AHB-Lite mirrored slave interface (as found on, for example, CoreAHB or CoreAHBLite) as well as an AMBA3 APB master interface that connects to an AMBA 3 APB mirrored master interface (as found on, for example, CoreAPB3).

Delivery Types

CoreAHBtoAPB3 v3.1 is licensed in two ways: Obfuscated or register transfer level (RTL).

Obfuscated

Complete RTL code is provided for the core, allowing the core to be instantiated with SmartDesign. Simulation, synthesis, and layout can be performed within Libero® System-on-Chip (SoC). The RTL code for the core is obfuscated.

RTL

Complete RTL source code is provided for the core.

Supported Families

- SmartFusion®2
- SmartFusion®
- Microsemi Fusion®
- IGLOO®
- IGLOOe
- IGLOO PLUS
- ProASIC®3
- ProASIC3E
- ProASIC3L
- Axcelerator®
- RTAX-S
- IGLOO®2
- RTG™4

Supported Tool Flows

Libero SoC software v11.1 or later supports CoreAHBtoAPB3 v3.1.

Installation Instructions

CoreAHBtoAPB3 is available through the Libero SoC IP Catalog. This can be downloaded from a remote web-based repository and installed into the local vault to make it ready to use. Once installed in the Libero SoC software, the core can be instantiated, configured, connected, and generated using the SmartDesign tool.

Refer to the *Using DirectCore in Libero IDE User Guide* `or *Libero SoC online help* for further instructions on core installation, licensing, and general use.

Discontinued Features and Devices

No features or devices have been discontinued in this release.

New Features and Devices

* Support for IGLOO2 and RTG4 devices has been added.

Known Issues and Workarounds

There are no known issues for CoreAHBtoAPB3 v3.1.

Release History

Table 1 Release History

Version	Date	Changes
3.1	November 2014	Added RTG4 and IGLOO2 support.
3.0	March 2013	Added explicit support for more device families. Width of HADDR and PADDR address bus ports increased from 24 bits to 32 bits.
2.0	November 2009	First production release

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