

INDIAN INSTITUTE OF TECHNOLOGY, ROPAR

CS204 Course Project - Phase II

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Project Description

The aim of this project is to simulate the machine level execution of RISC V 32-bit instructions using a high level language.

The Project also aims to give updates to the user regarding each step of the execution of the program. It also returns the final status of the memory and registers as output for the user to analyse the working of their programs thoroughly.

The Project currently allows the user to use 29 different instructions and can be extended to allow the use of any number of instructions by editing the .csv files as long as the instructions are supported by 32-bit RISC V ISA.

The program supports the use of 'knobs' which will enable the user to select whether or not they want pipelining, data forwarding, and other details to be printed at the end of running the program.

The program executes each instruction using five stages as described in the RISC V architecture.

There are 5 separate knobs, all with a different purpose.

1. **Knob1:** Used to enable/disable pipelining during runtime.
2. **Knob2:** Used to enable/disable data forwarding
3. **Knob3:** Used to enable/disable printing values in the register file at the end of each cycle.
4. **Knob4:** Used to enable/disable printing information in the pipeline registers at the end of each cycle, along with the cycle number.
5. **Knob5:** This is like enabling knob4 for a specific instruction. We will be able to see pipeline register for that particular instruction only.

Technologies employed

Back-end - Python3

1. **pandas** for reading **.csv** files.
2. **os** for getting and adding path to certain file locations.
3. **collections.defaultdict** to make a hash map for memory.
4. **sys** for reading and editing files with ease.
5. **termcolor.colored** for printing colored text in the terminal

Front-end - Python3

1. **PyQt5** for the Graphic User Interface.
2. **qdarkstyle** for dark theme in the GUI.
3. **json** for making dictionaries with ease in GUI

Implementation Details

We have implemented the project using the following method:

First it takes the input from a **.mc** file that contains the input in the required format, the content of this file can be modified using the GUI editor or by changing the main.mc file.

Then, it stores the **.data** part into the data memory and the **.text** part in the text part of the memory. Once that is completed, we run the program using the following method:

1. Instruction Fetch: PC is incremented by 4, and the instruction is loaded from the memory and stored in the Instruction Register.
2. Instruction Decode: We are identifying the instruction using the .csv file and **pandas** library and returning all the required fields. The registers RA and RB are also set during this stage.
3. Execute: The ALU executes the instruction by computing the desired output using values stored in registers in RA, RB, imm, etc. The required type of ALU instruction is determined by another .csv file. We are updating the output in the RZ register.
4. Memory Access: Memory is read and written in this stage. This stage is used only for load and store instructions. For the remaining instructions, this stage is redundant. The value of the RY register is also consequently updated.
5. Register Write-back: Here we are storing the value of the temporary register RY in the destination register, resulting in the register files being updated. The register update is extremely fast compared to memory read and update.

We have implemented data forwarding by passing the required values from the source buffer to the destination buffer without the need of stalling in most cases. This helps to reduce the number of stalls required. The types of data forwarding are:

1. Execute to Execute
2. Memory to Execute
3. Execute to Execute

The program detects hazards present which can cause an incorrect value to be register due to one instruction's stage being executed before another. This is fixed by either transferring data values directly with the help of data forwarding, or with the help of stalling which delays the next instruction so that the previous one is updated in time.

While data forwarding is enabled, there is no use of stalling except for one particular case where we use it along with Memory-to-Execute data forwarding.

Input/Output format details

Input Format:

```
0x0 0x00500513
0x4 0x008000EF
0x8 0x0440006F
$
0x10000000 0x64
```

The input is given through GUI if the GUI is run else it is taken from the input file.

The section given before the '\$' sign is the text segment of the code and all the lines after it signify the data segment of the code.

Output is generated in the 'generated' folder. It contains the following things

1. 'registers.txt' contains the register values
2. 'memory.txt' contains the data part of the memory
3. 'stats.txt' contains the statistical details of the program
4. 'outputLog.txt' contains the buffer data of each cycle

In *stats.txt* we have printed the following:

1. Total number of cycles
2. Total number of instructions executed
3. CPI
4. Data Transfer instructions executed
5. ALU instructions executed
6. Control instructions executed
7. Total Stall Count
8. Number of Data Hazards
9. Total Control Hazards
10. Total Branch misprediction
11. Number of stalls due to data hazards
12. Number of stalls due to control hazards


An example output for *stats.txt* is :

Total number of Cycles in the program :14
Total number of Instructions executed :4
CPI is :3.5
Data Transfer Instructions Executed :0
ALU instructions are :4
Control instructions :0
Total Stall Count are :6
Number of Data Hazards are :3
Total Control Hazards are :0
Total Branch mispredictions are :0
Number of stalls due to data hazards are :6
Number of stalls due to control hazards are :0

The instruction fed was:

0x0 0x00A50513
0x4 0x00A50513
0x8 0x00A50513
0xC 0x00A50513

Graphical User Interface



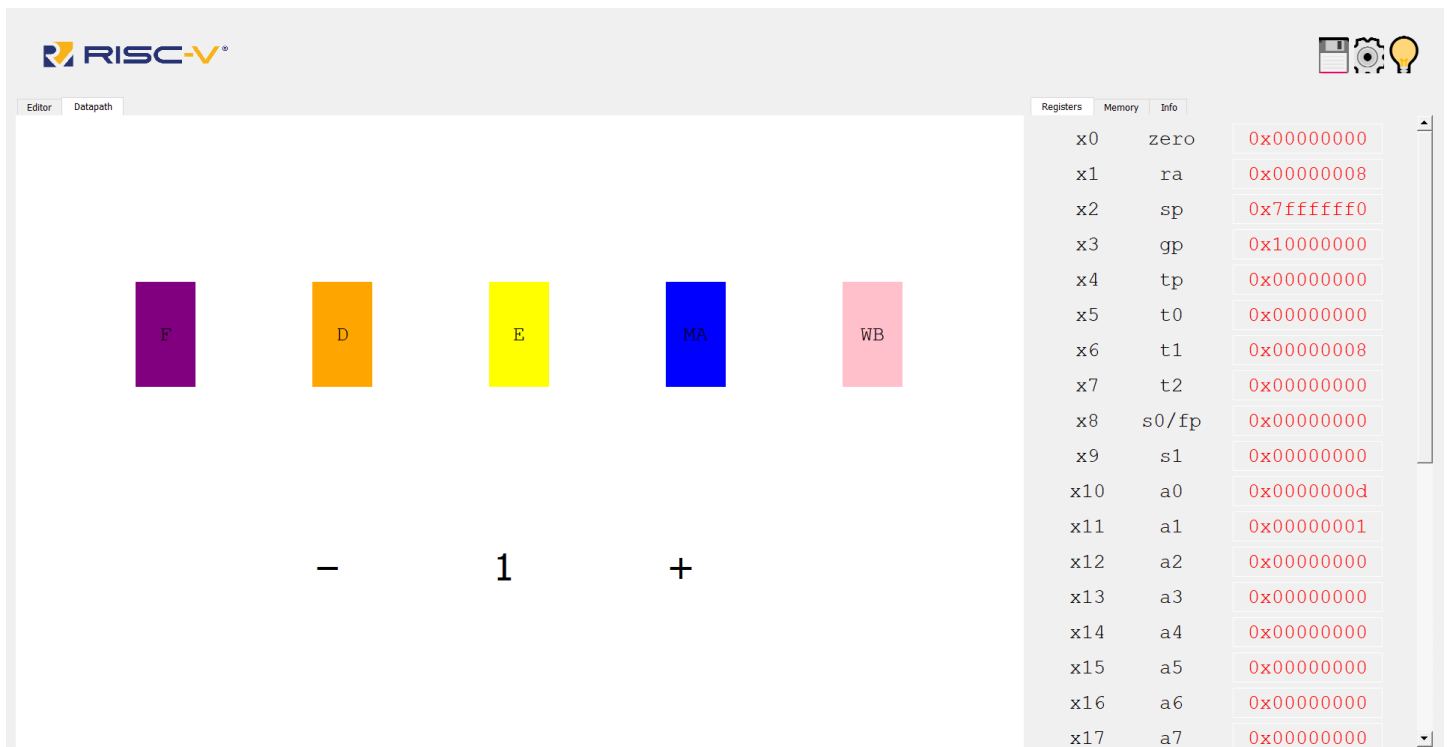
The RISC-V GUI Editor view displays assembly code in the Editor tab and register values in the Registers tab.

Editor Tab:

```
0x0 0x00700513
0x4 0x008000EF
0x8 0x0580006F
0xc 0xFF810113
0x10 0x00A12023
0x14 0x00112223
0x18 0x00100593
0x1c 0x00A5C663
0x20 0x00810113
0x24 0x00008067
0x28 0xFFF50513
0x2c 0xFE1FF0EF
0x30 0x00050333
0x34 0x00012503
0x38 0xFFE50513
0x3c 0xFFC10113
0x40 0x00612023
0x44 0xFC9FF0EF
0x48 0x00012303
0x4c 0x00410113
0x50 0x00412083
0x54 0x00810113
0x58 0x00650533
0x5c 0x00008067
```

Registers Tab:

Register	Name	Value
x0	zero	0x00000000
x1	ra	0x00000008
x2	sp	0x7fffffff0
x3	gp	0x10000000
x4	tp	0x00000000
x5	t0	0x00000000
x6	t1	0x00000008
x7	t2	0x00000000
x8	s0/fp	0x00000000
x9	s1	0x00000000
x10	a0	0x0000000d
x11	a1	0x00000001
x12	a2	0x00000000
x13	a3	0x00000000
x14	a4	0x00000000
x15	a5	0x00000000
x16	a6	0x00000000



The RISC-V GUI Datapath view displays a 5-stage pipeline diagram. The stages are represented by colored boxes: F (Fetch, purple), D (Decode, orange), E (Execute, yellow), MA (Memory Access, blue), and WB (Write Back, pink). The diagram shows the flow of data through these stages, with a subtraction operation (1 - 1) being performed.

Pipeline Stages:

- F (Fetch)
- D (Decode)
- E (Execute)
- MA (Memory Access)
- WB (Write Back)

Operation: 1 - 1

Registers Tab:

Register	Name	Value
x0	zero	0x00000000
x1	ra	0x00000008
x2	sp	0x7fffffff0
x3	gp	0x10000000
x4	tp	0x00000000
x5	t0	0x00000000
x6	t1	0x00000008
x7	t2	0x00000000
x8	s0/fp	0x00000000
x9	s1	0x00000000
x10	a0	0x0000000d
x11	a1	0x00000001
x12	a2	0x00000000
x13	a3	0x00000000
x14	a4	0x00000000
x15	a5	0x00000000
x16	a6	0x00000000
x17	a7	0x00000000

The image shows a RISC-V IDE interface. On the left, the 'Editor' tab displays assembly code. On the right, the 'Memory' tab shows a memory dump.

Editor Tab:

```

0x0 0x00700513
0x4 0x008000EF
0x8 0x0580006F
0xc 0xFF810113
0x10 0x00A12023
0x14 0x00112223
0x18 0x00100593
0x1c 0x00A5C663
0x20 0x00810113
0x24 0x00008067
0x28 0xFFF50513
0x2c 0xFE1FF0EF
0x30 0x00050333
0x34 0x00012503
0x38 0xFFE50513
0x3c 0xFFC10113
0x40 0x00612023
0x44 0xFC9FF0EF
0x48 0x00012303
0x4c 0x00410113
0x50 0x00412083
0x54 0x00810113
0x58 0x00650533
0x5c 0x00008067
  
```

Memory Tab:

Address	+1	+2	+3	+4
0x10000000	00	00	00	00
0x10000004	00	00	00	00
0x10000008	00	00	00	00
0x1000000c	00	00	00	00
0x10000010	00	00	00	00
0x10000014	00	00	00	00
0x10000018	00	00	00	00
0x1000001c	00	00	00	00
0x10000020	00	00	00	00
0x10000024	00	00	00	00

At the bottom right of the memory tab, there is a search box containing 'imn' and a search icon.

Here we can see the jump to register functionality, where we have stored a value in the register 0x10000500, and are jumping to it and seeing the value.

For example to jump on the location 0x10000500 write 10000500 in the Jump to box which is in the bottom right in the memory tab.

We have made the GUI in both light and dark theme. The theme can be changed by clicking on the "yellow bulb" which can be seen in the top right corner.

We have shown memory, registers and info on the right section of the GUI while on the left part there is Code Editor. In the Code Editor you can edit your code or copy paste your code and also save it. The info contains the details of the instruction being executed in each stage during a cycle. First of all we will paste the code in the Editor on the left side and then save and compile the code by clicking the respective 'save' button which is the first button on the top right and 'compile' button. The code will get updated in **main.mc** file and then the code will be executed and a ✓ will appear. The memory and registers will be updated simultaneously.

- We can jump to the memory using the 'Jump to' box on the bottom right corner in the memory section.
- All the registers can be seen in the register file by scrolling in the register file section.
- Executing **main.py** always generates a GUI window where you can code in the Editor.
- We can also execute non GUI code by writing →python main.py
- The DataPath explains how the data is flowing through each stage.
- Here the 5 stages can be seen in the DataPath tab.
- The number below it is the cycle number for which the DataPath is shown.
- You can see the DataPath in the respective cycle by going to that cycle.
- Click on + to go to the next cycle and on - to go to the previous cycle.