31 27	26 25	24 20	19 15	14 12	11 7	6 0	
funct7		rs2	rs1	funct3	rd	opcode	R-type
imm[11:0]			rs1	funct3	rd	opcode	I-type
Trap-Return Instructions							
0001000		00010	00000	000	00000	1110011	SRET
0011000		00010	00000	000	00000	1110011	MRET
Interrupt-Management Instructions							
0001000		00101	00000	000	00000	1110011	WFI
Supervisor Memory-Management Instructions							
0001001		rs2	rs1	000	00000	1110011	SFENCE.VMA
Hypervisor Memory-Management Instructions							
0010001		rs2	rs1	000	00000	1110011	HFENCE.VVMA
0110001		rs2	rs1	000	00000	1110011	HFENCE.GVMA
Hypervisor Virtual-Machine Load and Store Instructions							
0110000		00000	rs1	100	rd	1110011	HLV.B
0110000		00001	rs1	100	rd	1110011	HLV.BU
0110010		00000	rs1	100	rd	1110011	HLV.H
0110010		00001	rs1	100	rd	1110011	HLV.HU
0110100		00000	rs1	100	rd	1110011	HLV.W
0110010		00011	rs1	100	rd	1110011	HLVX.HU
0110100		00011	rs1	100	rd	1110011	HLVX.WU
0110001		rs2	rs1	100	00000	1110011	HSV.B
0110011		rs2	rs1	100	00000	1110011	HSV.H
0110101		rs2	rs1	100	00000	1110011	HSV.W
Hypervisor Virtual-Machine Load and Store Instructions, RV64 only							
0110100		00001	rs1	100	rd	1110011	HLV.WU
0110110		00000	rs1	100	rd	1110011	HLV.D
0110111		rs2	rs1	100	00000	1110011	HSV.D
Svinval Memory-Management Instructions							
0001011		rs2	rs1	000	00000	1110011	SINVAL.VMA
0001100		00000	00000	000	00000	1110011	SFENCE.W.INVAL
0001100		00001	00000	000	00000	1110011	SFENCE.INVAL.IR
0010011		rs2	rs1	000	00000	1110011	HINVAL.VVMA
0110011		rs2	rs1	000	00000	1110011	HINVAL.GVMA

Table 1: RISC-V Privileged Instructions