

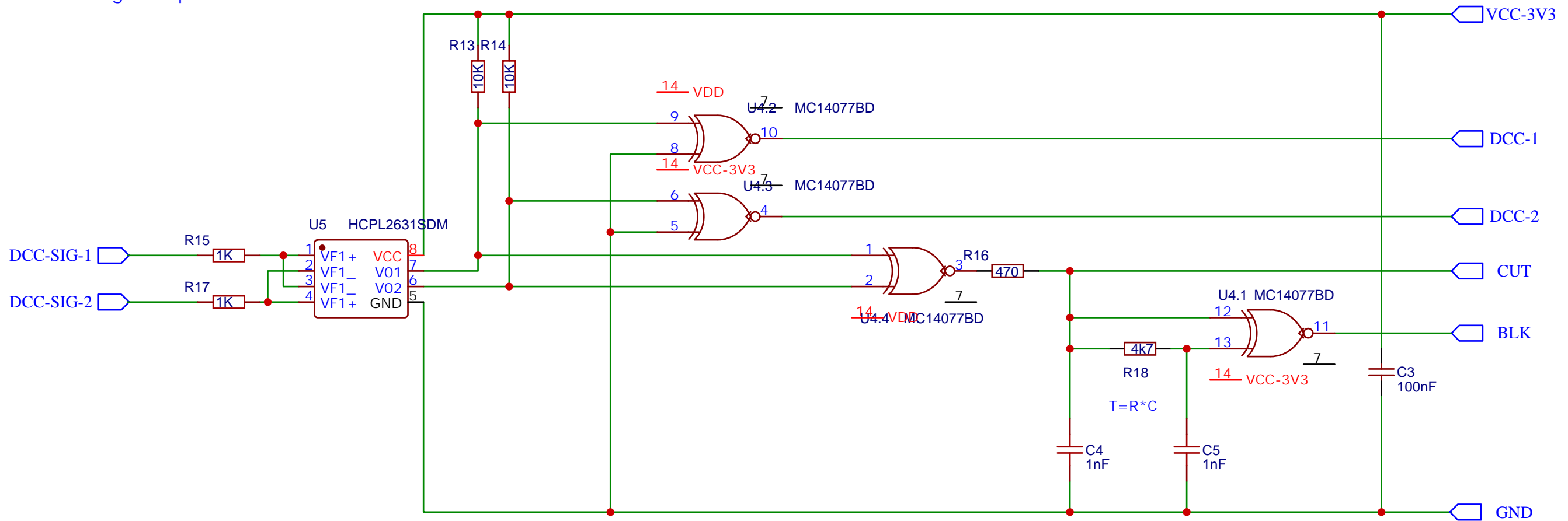
Non-Volatile Memory

Main Controller - RASPBERRY Pi Pico

CAN Bus Line Driver

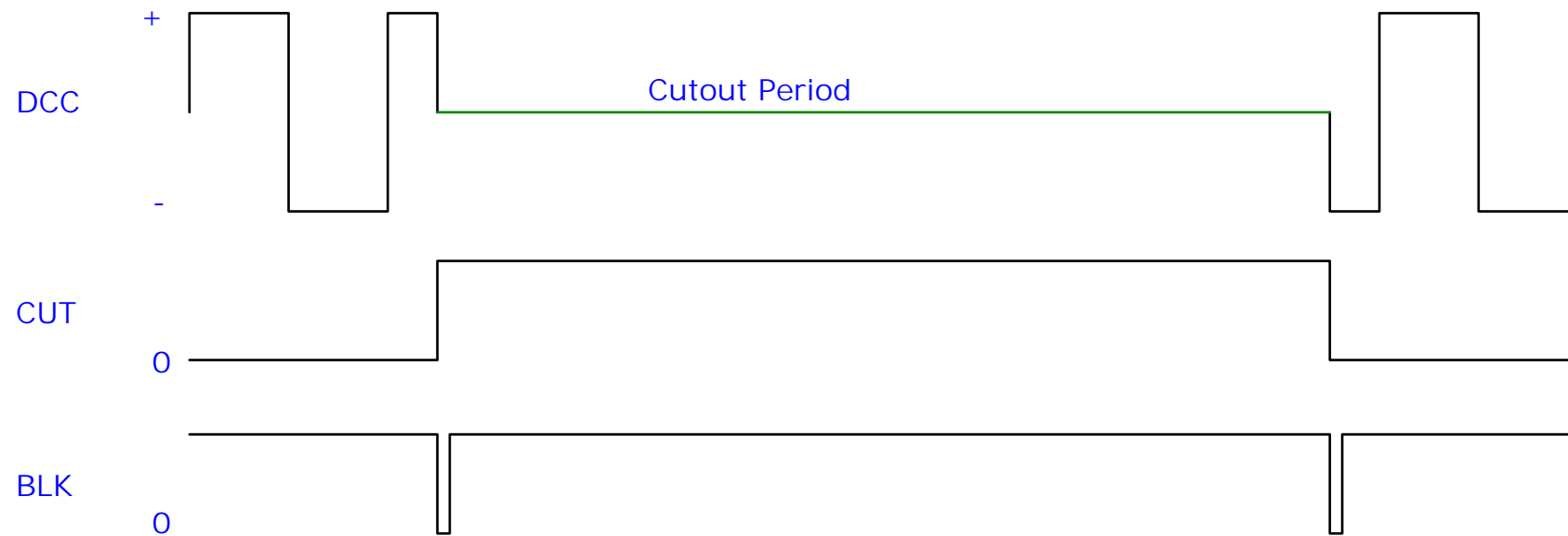
Level shifters

TITLE: LcsNodes-Block-Combo-Controller Main Controller		REV: B.02.00
嘉立创EDA	Company: Your Company	Sheet: 2/7
	Date: 2024-02-01 Drawn By: helmut-f	



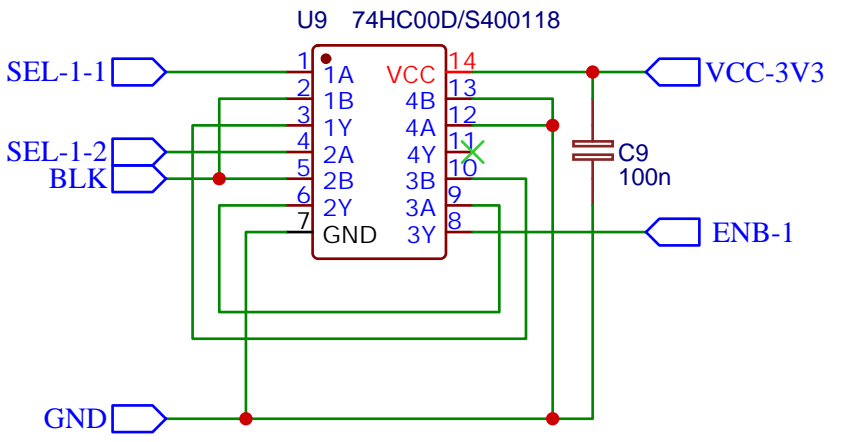
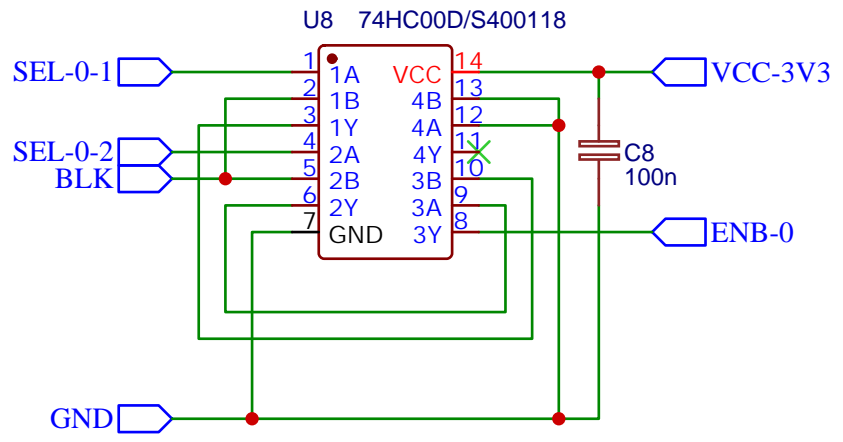
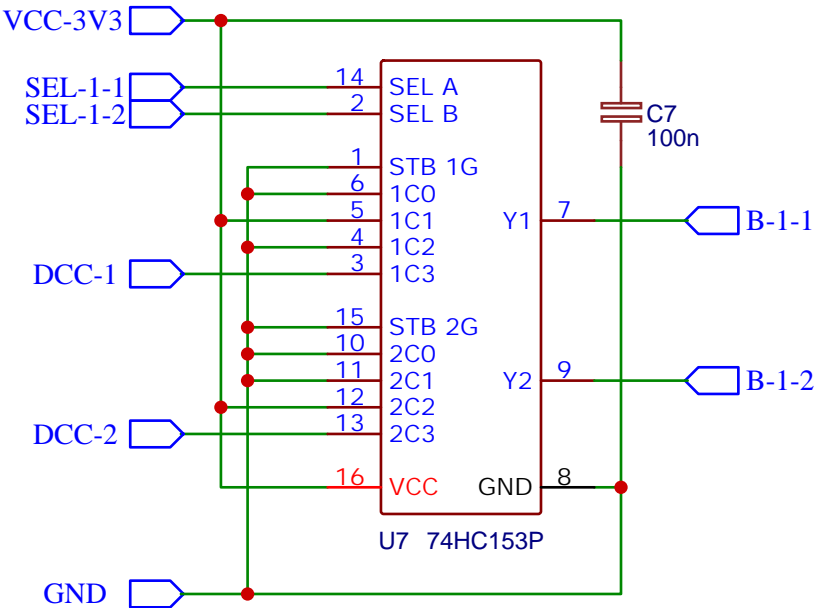
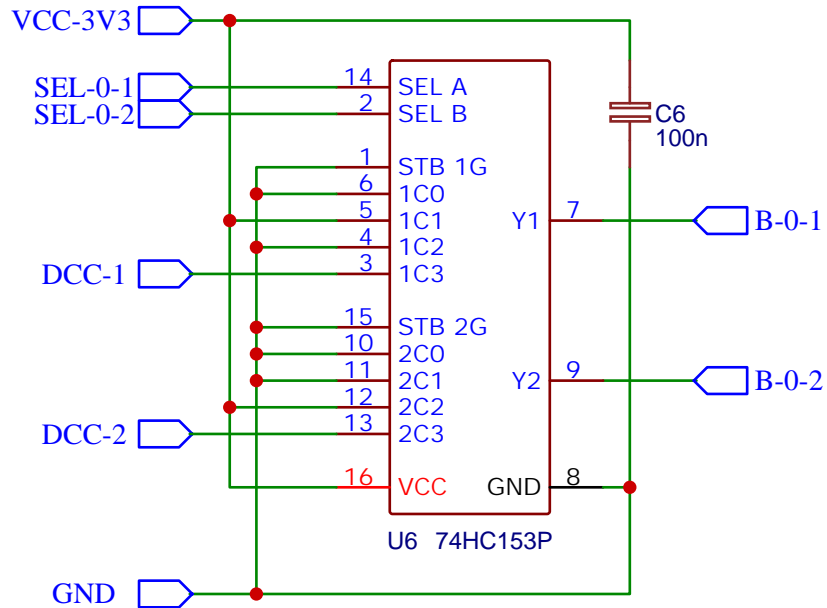
DCC Input:

- 00->CUTOUT
- 01->DCC+
- 10->DCC-



TITLE: LcsNodes-Block-Combo-Controller LCS Bus - DCC Signal Input		REV: B.02.00
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H-Bridge Control Logic



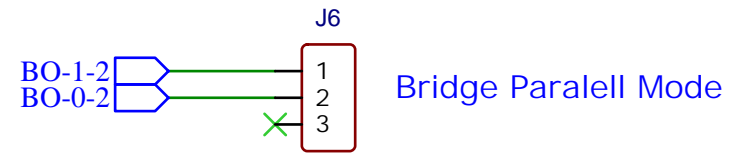
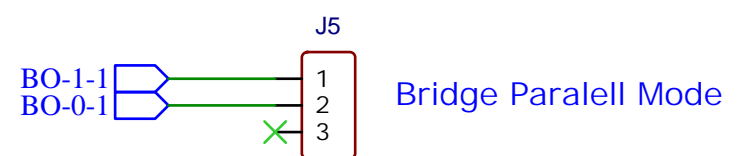
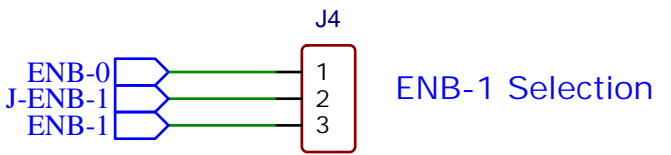
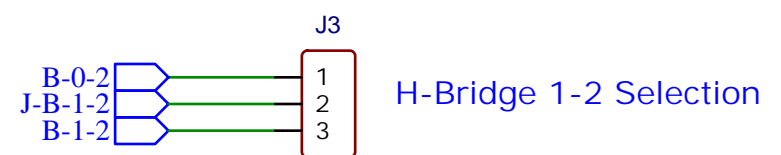
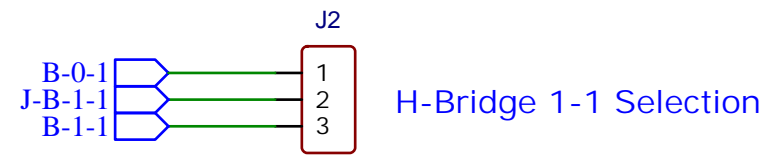
Control Logic Signals:

Sel-2	Sel-1	->	B-x-1	B-x-2	ENB-x	STATE
0	0	->	GND	GND	GND	"Z"
0	1	->	VCC	GND	PWM/BLK	"FWD"
1	0	->	GND	VCC	PWM/BLK	"REV"
1	1	->	DCC1	DCC2	DCC/BLK	"DCC"

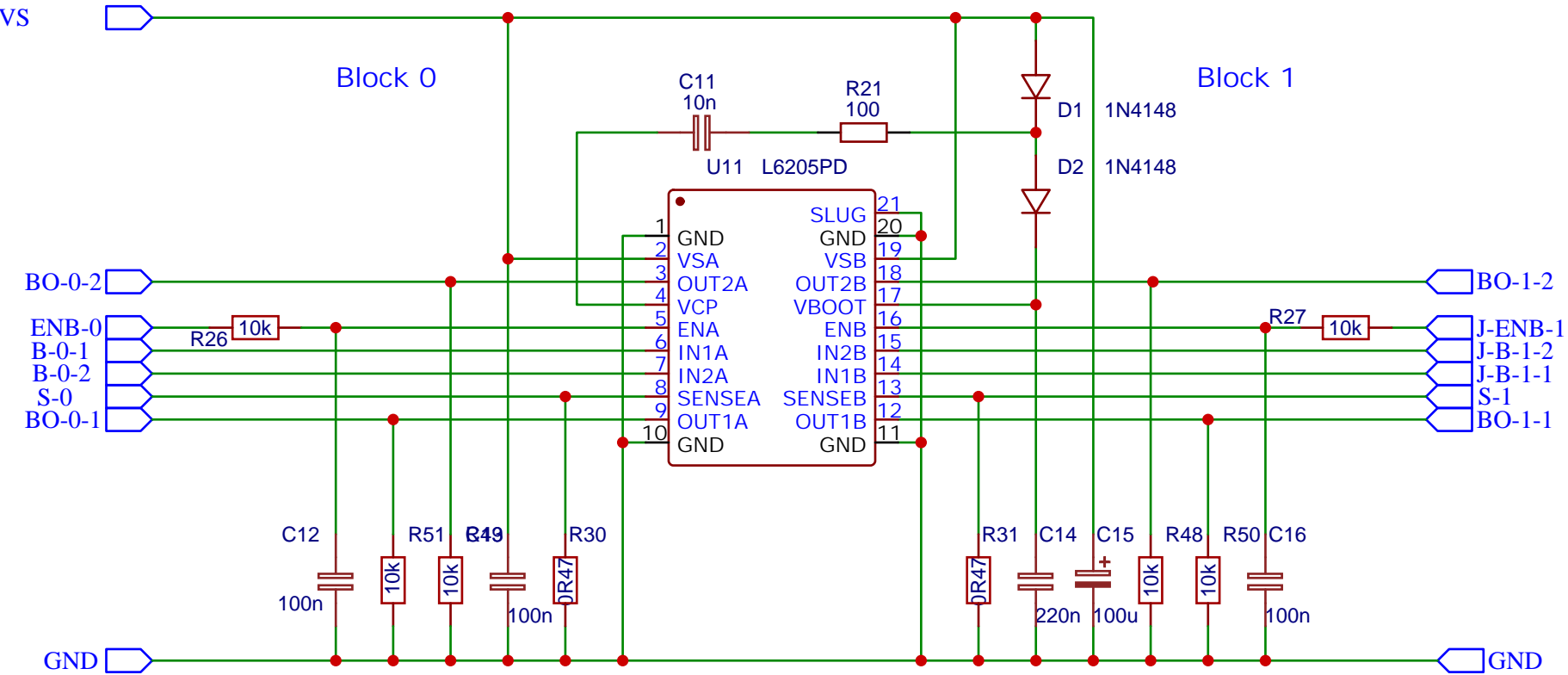
ENB == LOW -> Z

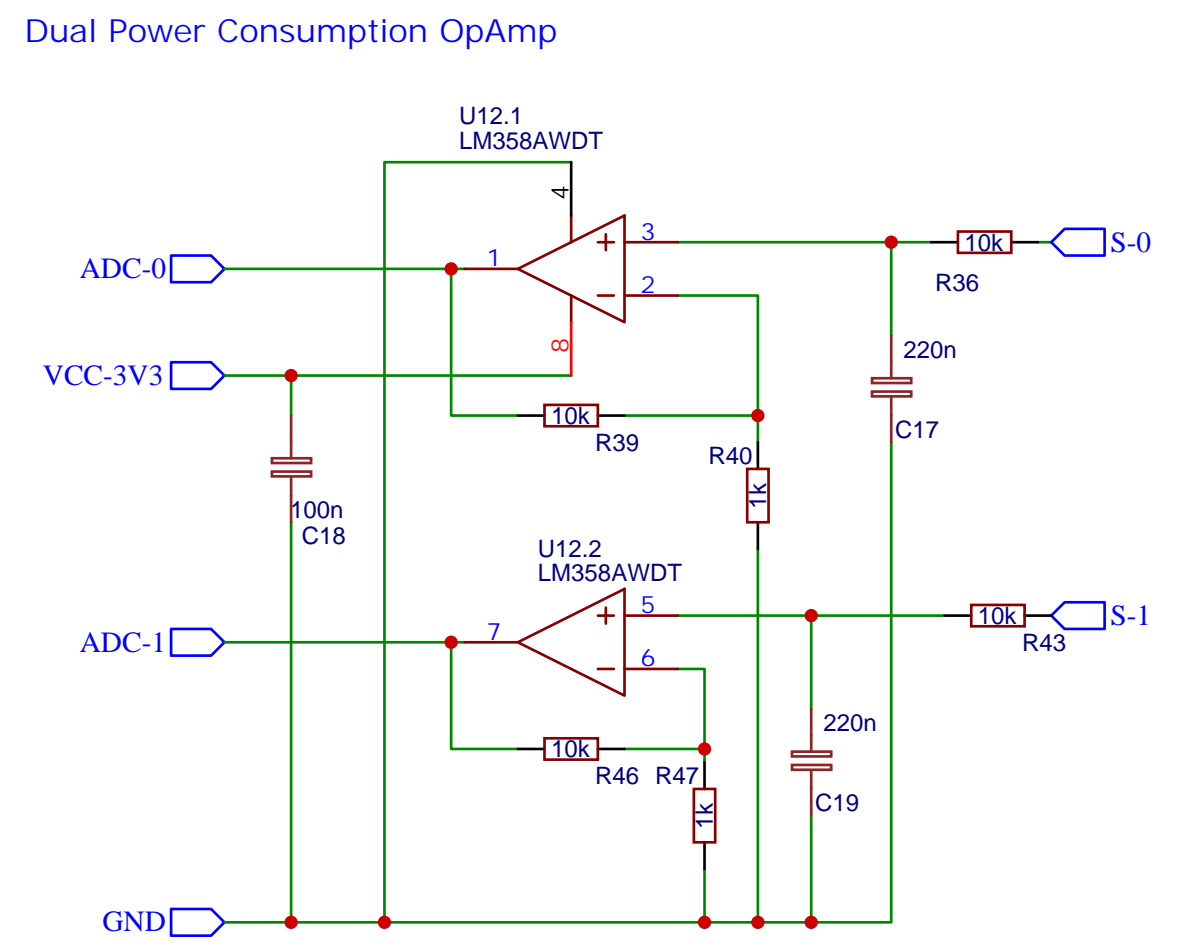
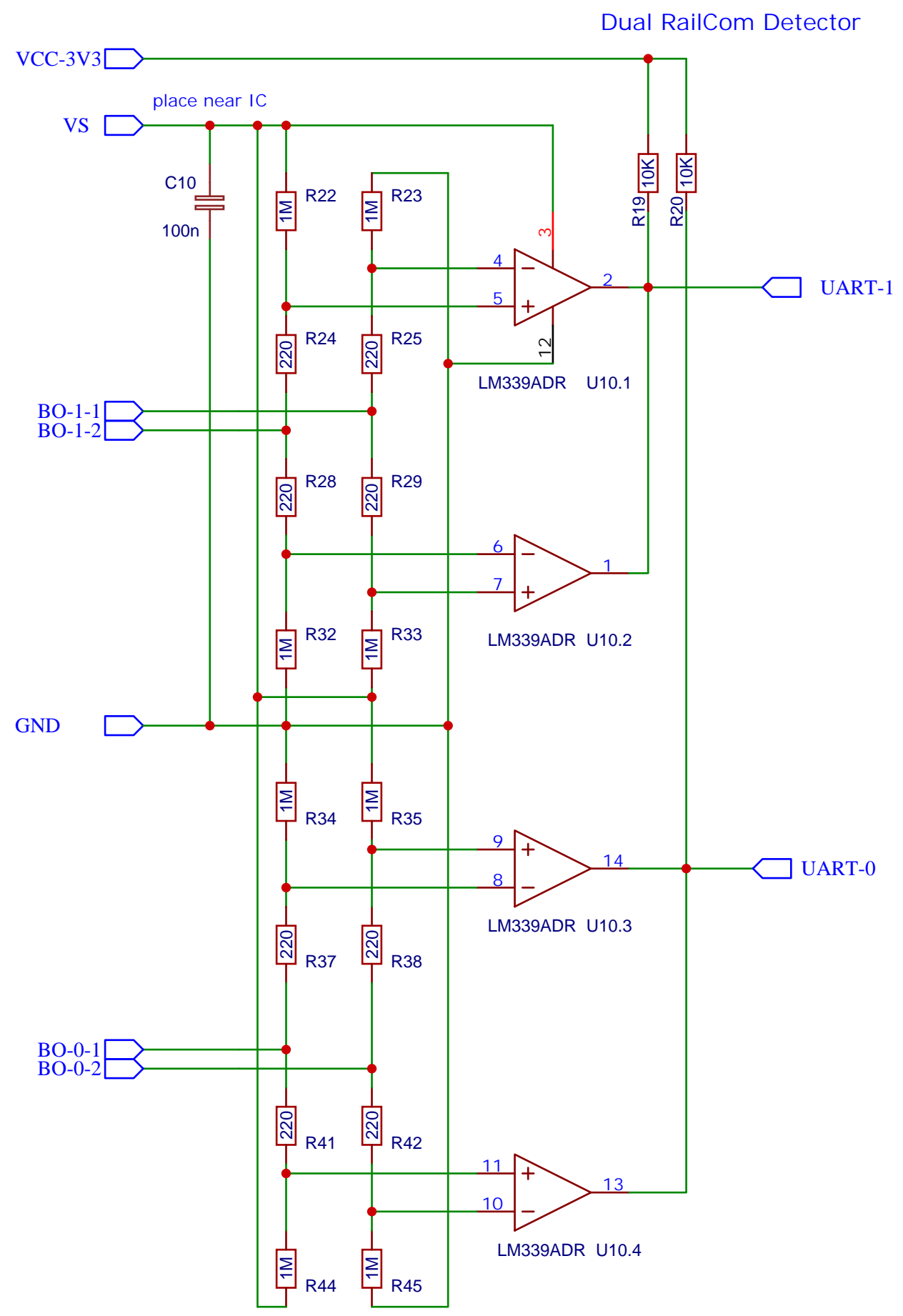
The Dual Bridges can be configured to run as a Mono Block.
Jumper position (1-2) -> MONO mode.
Jumper position (2-3) -> DUAL mode.

WARNING: ALL jumpers blocks must be set to the correct option.



Dual H-Bridge

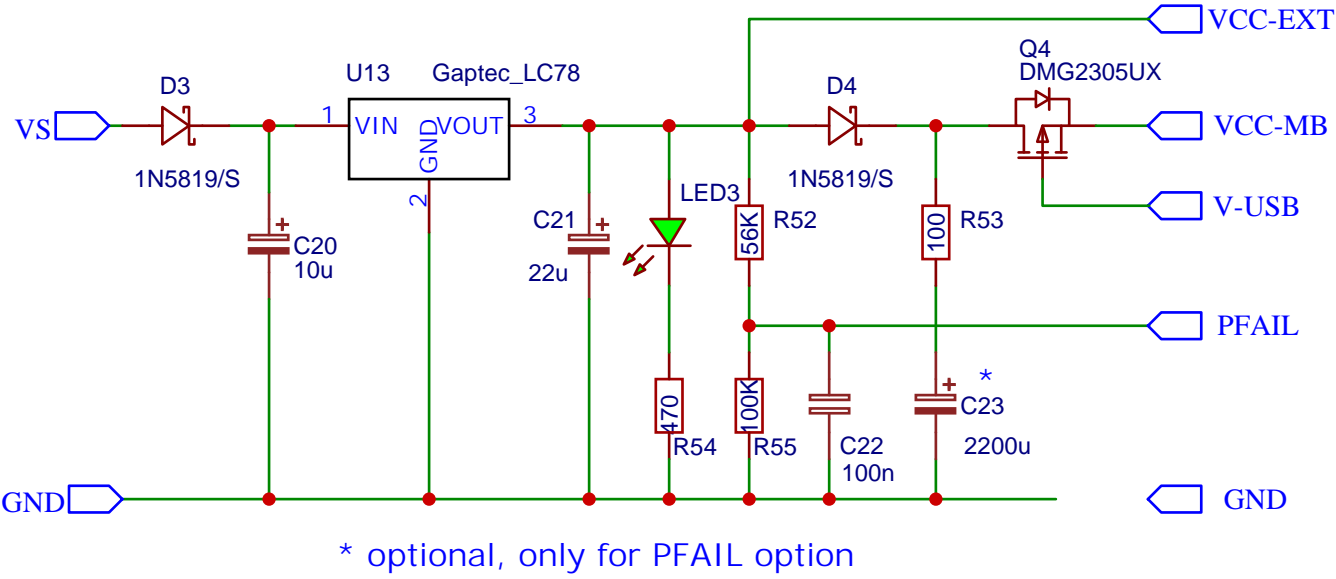
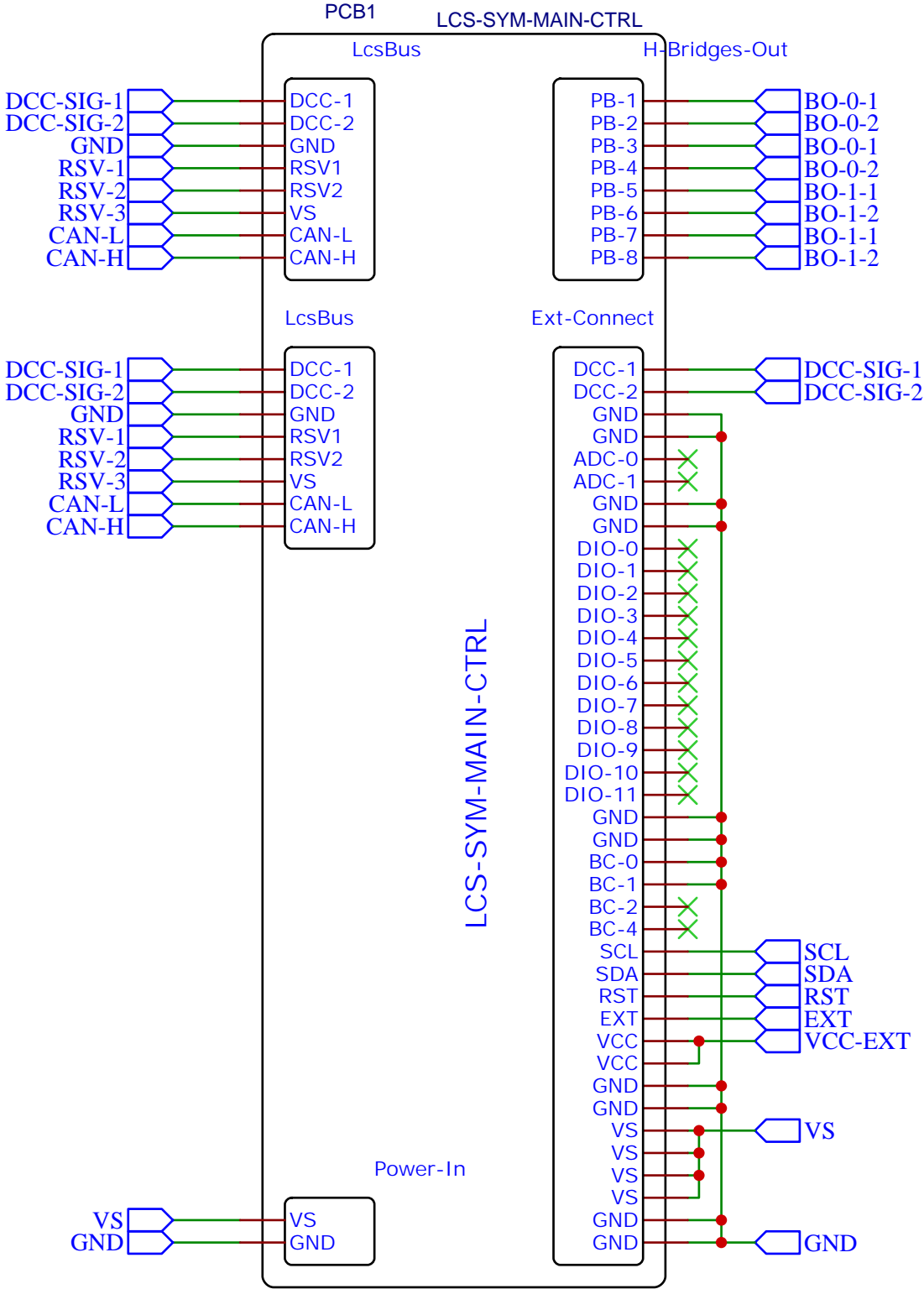




TITLE: LcsNodes-Block-Combo-Controller Current Meas and RailCom		REV: B.02.00
EasyEDA	Company: Your Company	Sheet: 6/7
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Block Controller PCB connectors

Power Supply with Powerfail Option



TITLE: LcsNodes-Block-Combo-Controller Power Supply, Connectors		REV: B.02.00
嘉立创EDA	Company: Your Company	Sheet: 7/7
	Date: 19-01-2025	Drawn By: helmut-f