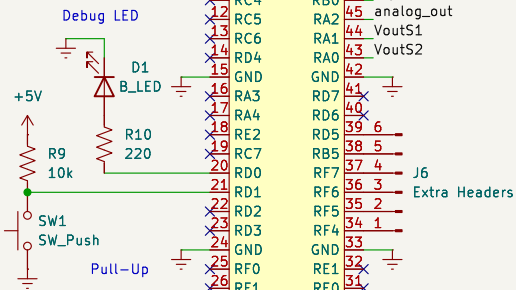


The diagram illustrates a 4-bit DAC circuit using four MCP6004_HK op-amp modules (U1A, U1B, U1C, U1D). The circuit is powered by a +5V supply. A voltage divider consisting of resistors R1 (220k) and R2 (2k) is connected to the +5V supply, with the midpoint labeled Vref. A potentiometer RV1 (10k) is also connected to the +5V supply, with its wiper connected to the non-inverting input of U1A. The inverting input of U1A is connected to the output of U1B (VoutS1). The output of U1A is Vout1. The output of U1B is VoutS1. The output of U1C is Vout2, and the output of U1D is Vout3. The circuit is configured to produce a 4-bit DAC output.



Pinout diagram for J7 Connector:

- Pin 1: +9V
- Pin 2: digital_in
- Pin 3: digital_out
- Pin 4: (Marked with a blue 'X')
- Pin 5: (Marked with a blue 'X')
- Pin 6: analog_out
- Pin 7: (Marked with a blue 'X')
- Pin 8: Ground

Connector: J7

Edited by HK

The diagram shows a sensor module labeled S2 (SEN0257_HK) with three pins. Pin 1 is labeled 'Signal' and is connected to 'VoutS2'. Pin 2 is labeled 'VCC' and is connected to a '+5V' supply. Pin 3 is labeled 'GND' and is connected to ground. The module is represented by a yellow box with a red border.

All resistors are rated 1/4 W
All capacitors are rated 50V