

The diagram illustrates a 4-bit DAC circuit using four MCP6004 op-amp modules (U1A, U1B, U1C, U1D). The circuit is powered by a 5V supply. A voltage divider (R1, R2) provides a reference voltage (Vref) to the non-inverting input of U1A. U1A is configured as a voltage follower, with its output (Vout1) connected to the inverting input of U1B. U1B is configured as a summing amplifier, with Vref at its non-inverting input and Vout1 at its inverting input. U1C and U1D are also configured as summing amplifiers, each with two inputs and one output. The circuit is labeled with component values and pin numbers.

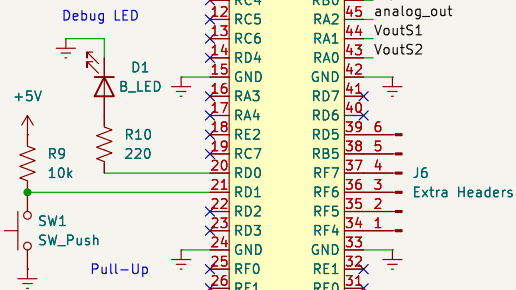


Diagram of the J7 connector pinout:

- Pin 1: +9V
- Pin 2: digital\_in
- Pin 3: digital\_out
- Pin 4: (unlabeled)
- Pin 5: (unlabeled)
- Pin 6: analog\_out
- Pin 7: (unlabeled)
- Pin 8: Ground

Edited by HK

S2  
SEN0257\_HK

All resistors are rated 1/4 W  
All capacitors are rated 50V