# CAD For VLSI (CS6230) Final Project

Project Title: P2. Pipelined double precision(fp64) floating point adder Author: HARIHARAN P (EE20B042) AND JAWHAR S (EE20B049) Date: 10 Dec 2023

# Test Cases used Binary value = (-1) Sign bit \* 2 (Exponent – 1023) \* 1.Mantissa

# Positive + positive

#### **Negative + negative**

## **Positive + Negative**

## **Negative + Positive**

#### -X + +X

#### +Infinity + X

#### -Infinity + X

NAN + X
8) A= 64'b1111111111110000000000000000000000000
B = 64'b0000000011010010001000000000000000000
// C = 01111111111111111111111111111111111
01111111111111111111111111111111111111
+Infinity + -Infinity
9) A= $64$ 'b0111111111110000000000000000000000000
B = 64'b1111111111110000000000000000000000000
// C = 01111111111111111111111111111111111
01111111111111111111111111111111111111

//Cannot calculate