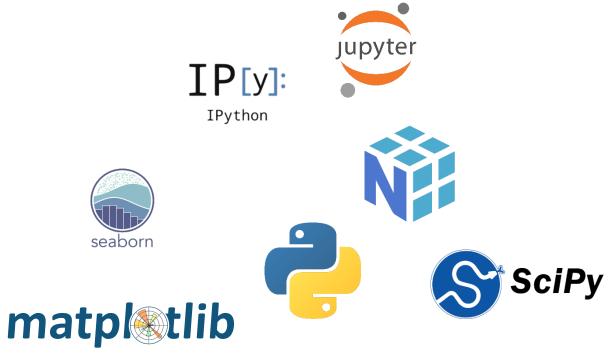








# **Python: The Scientific Language**

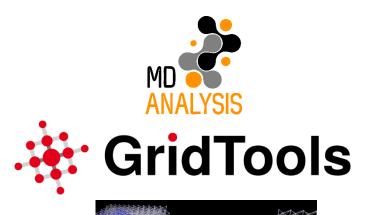








Vast software ecosystem







**Productivity** 



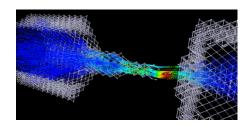




# **Python: An HPC language?**



# **GridTools**



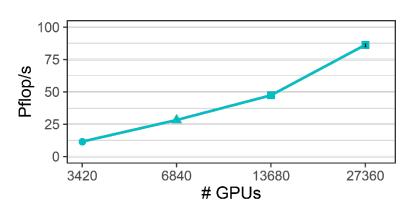




**Productivity** 







**Performance** 







#### C Code

```
int i, j, k;
for (i = 0; i < _PB_NI; i++) {
   for (j = 0; j < _PB_NJ; j++)
   C[i][j] *= beta;
   for (k = 0; k < _PB_NK; k++) {
      for (j = 0; j < _PB_NJ; j++)
      C[i][j] += alpha * A[i][k] * B[k][j];
   }
}</pre>
```

# Python/NumPy Code

```
C[:] = alpha * A @ B + beta * C
```

- Explicit matrix product operator
- Implicit scalar-array operations
- Implicit array-array operations





#### C Code

```
int i, j, k;
for (i = 0; i < _PB_NI; i++) {
   for (j = 0; j < _PB_NJ; j++)
   C[i][j] *= beta;
   for (k = 0; k < _PB_NK; k++) {
      for (j = 0; j < _PB_NJ; j++)
      C[i][j] += alpha * A[i][k] * B[k][j];
   }
}</pre>
```

# Python/NumPy Code

```
C[:] = alpha * A @ B + beta * C
```

- Explicit matrix product operator
- Implicit scalar-array operations
- Implicit array-array operations

	CPython	Numba	Pythran	GCC	ICC	CuPy	
GEMM	1.0x	1.4x	1.4x	0.008x	0.2x	17.0x	
2MM	1.0x	1.5x	1.3x	0.001x	0.06x	12.9x	
3MM	1.0x	1.6x	1.2x	0.0006x	0.9x	9.3x	

2x Intel Xeon Gold 6130 CPU (32 cores)





#### C Code

```
int i, j, k;
for (i = 0; i < _PB_NI; i++) {
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      for (j = 0; j < _PB_NJ; j++)
      C[i][j] += alpha * A[i][k] * B[k][j];
   }
   naive algorithm</pre>
```

# Python/NumPy Code



Explicit matrix product operator



Implicit scalar-array operations

Implicit array-array operations

#### abstracts away BLAS call

	CPython	Numba	Pythran	GCC	ICC	CuPy	
GEMM	1.0x	1.4x	1.4x	0.008x	0.2x	17.0x	
2MM	1.0x	1.5x	1.3x	0.001x	0.06x	12.9x	
3MM	1.0x	1.6x	1.2x	0.0006x	0.9x	9.3x	

2x Intel Xeon Gold 6130 CPU (32 cores)





#### C Code

```
cblas_dgemm(
  CblasRowMajor,
  CblasNoTrans, CblasNoTrans,
  _PB_NI, _PB_NJ, _PB_NK,
  alpha, A, _PB_NK, B, _PB_NJ,
  beta, C, _PB_NJ
);

BLAS call
```

# Python/NumPy Code



Explicit matrix product operator



Implicit scalar-array operations

Implicit array-array operations



	CPython	Numba	Pythran	GCC	ICC	CuPy	
GEMM	1.0x	1.4x	1.4x	0.008x	0.2x	17.0x	
2MM	1.0x	1.5x	1.3x	0.001x	0.06x	12.9x	
3MM	1.0x	1.6x	1.2x	0.0006x	0.9x	9.3x	

2x Intel Xeon Gold 6130 CPU (32 cores)





#### C Code

# Python/NumPy Code

```
for t in range(TMAX):
    ey[0, :] = _fict_[t]
    ey[1:, :] -= 0.5 * (hz[1:, :] - hz[:-1, :])
    ex[:, 1:] -= 0.5 * (hz[:, 1:] - hz[:, :-1])
    hz[:-1, :-1] -= 0.7 * (
        ex[:-1, 1:] - ex[:-1, :-1] +
        ey[1:, :-1] - ey[:-1, :-1])
```



#### C Code

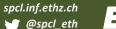
## Python/NumPy Code

```
for t in range(TMAX):
    ey[0, :] = _fict_[t]
    ey[1:, :] -= 0.5 * (hz[1:, :] - hz[:-1, :])
    ex[:, 1:] -= 0.5 * (hz[:, 1:] - hz[:, :-1])
    hz[:-1, :-1] -= 0.7 * (
        ex[:-1, 1:] - ex[:-1, :-1] +
        ey[1:, :-1] - ey[:-1, :-1])
```

	CPython	Numba	Pythran	GCC	ICC	CuPy	
FDTD-2D	1.0x	4.1x	1.3x	3.7x	41.3x	42.4x	
Jacobi-2D	1.0x	18.2x	21.8x	7.1x	58.6x	75.2x	
Heat-3D	1.0x	50.1x	2.3x	24.0x	179.0x	71.0x	

2x Intel Xeon Gold 6130 CPU (32 cores)







#### C Code

extraneous intermediate results

operations can be fused as shown in C-code

# Python/NumPy Code







#### C Code

```
int t, i, j;
for(t = 0; t < PB_TMAX; t++) {
 for (j = 0; j < PB_NY; j++)
   ey[0][j] = _fict_[t];
 for (i = 1; i < PB NX; i++)
```

# Python/NumPy Code

```
for t in range(TMAX):
 ey[0, :] = fict[t]
 ey[1:, :] -= 0.5 * (hz[1:, :] - hz[:-1, :])
 ex[:, 1:] -= 0.5 * (hz[:, 1:] - hz[:, :-1])
 |hz|:-1, :-1| -= 0.7
```

# Python doesn't analyze data movement

```
for (j = 0; j < _PB_NY - 1; j++)
 hz[i][j] = hz[i][j] - SCALAR_VAL(0.7)* (ex[i][j+1] - ex[i][j] +
                  ey[i+1][j] - ey[i][j]);
```

```
114 . , 4 . .
tmp2 = 0.5 * tmp1
ex[:, 1:] -= tmp2
```

extraneous intermediate results

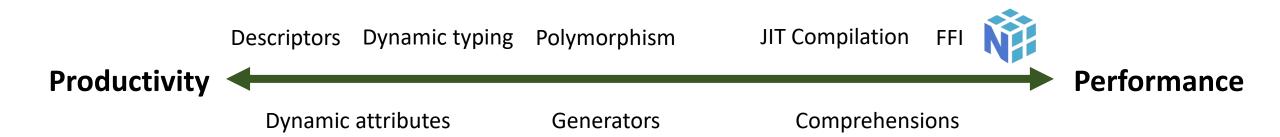
operations can be fused as shown in C-code







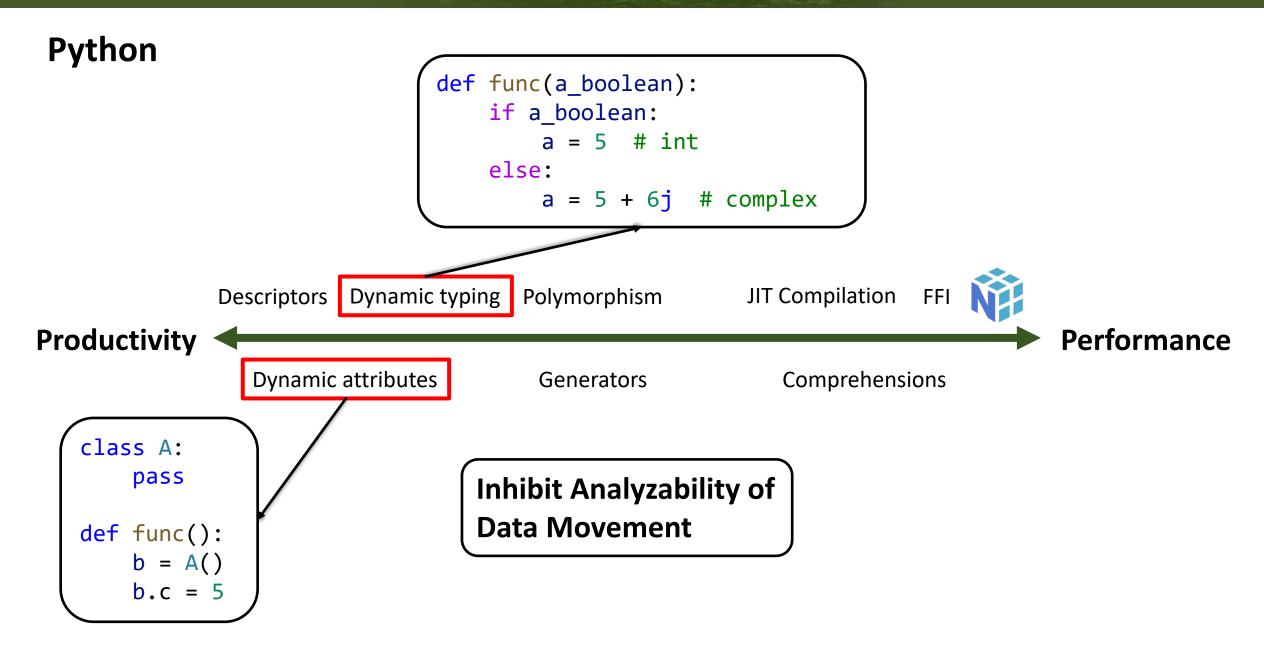
# **Python**

















**Performance** 

# Data-Centric (DaCe) Python

```
def func(a_boolean):
    a: complex
    if a_boolean:
        a = 5 \# int
    else:
        a = 5 + 6j \# complex
```

Descriptors Dynamic typing JIT Polymorphism JIT Compilation



#### **Productivity**

Dynamic attributes

**Restricted Generators** 

**Restricted Comprehensions** 

```
class A:
  def __init__(self):
    self.c = 0
def func():
  b = A()
  b.c = 5
```

Analyzable data movement





**Performance** 

# Data-Centric (DaCe) Python

```
@dace.program
def gemm(alpha, beta, A, B, C):
A = np.ndarray((M, K), dtype=np.float64)
B = np.ndarray((K, N), dtype=np.float64)
C = np.ndarray((M, N), dtype=np.float64)
gemm(1.5, 0.2, A, B, C)
```

Annotated/JIT

Descriptors Weak typing

@dace.program def gemm(alpha: dace.float64, beta: dace.float64, A: dace.float64[M, K], B: dace.float64[K, N], C: dace.float64[M, N]): func = gemm.compile()

Ahead-of-Time Compilation

JIT Compilation



# **Productivity**



**Restricted Generators** 

JIT Polymorphism

Generator Unrolling

**Restricted Comprehensions** 

Parallel Loop Annotation

Data Communication/Distribution Interface

```
for i in range(NX):
    for j in range(1, NY):
        do something()
for i, j in dace.map[0:NX, 1:NY]:
    do something in parallel()
```

Analyzable data movement

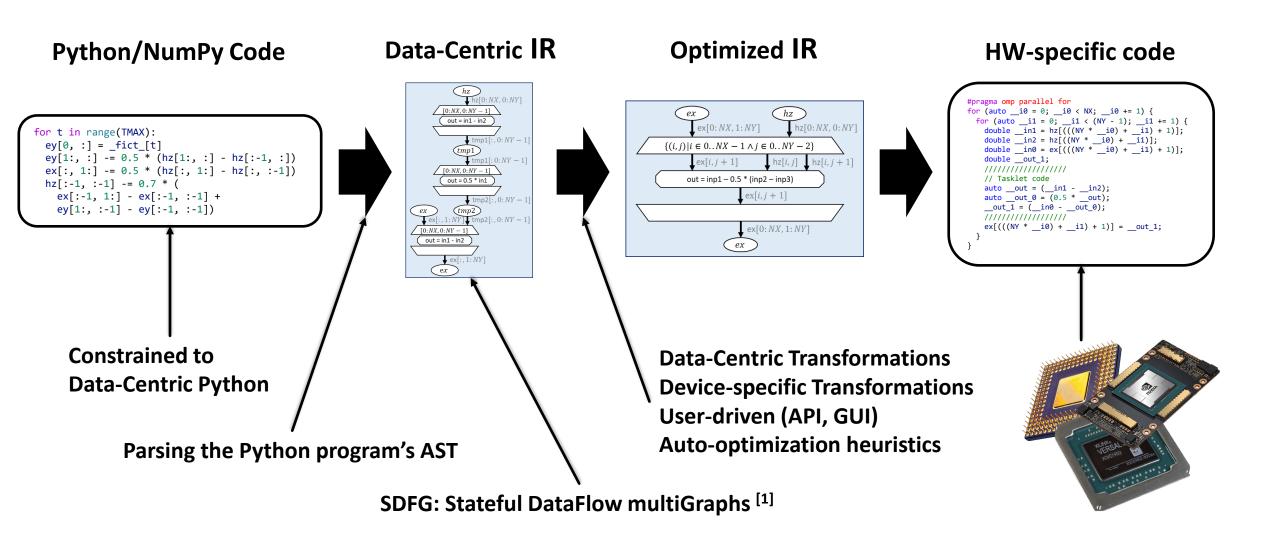
Guided optimization hints

```
dace.comm.Isend(A[1], nw, 3, req[0])
dace.comm.Isend(A[-2], ne, 2, req[1])
dace.comm.Irecv(A[0], nw, 2, req[2])
dace.comm.Irecv(A[-1], ne, 3, req[3])
dace.comm.Waitall(reg)
```





### **Data-Centric Workflow**

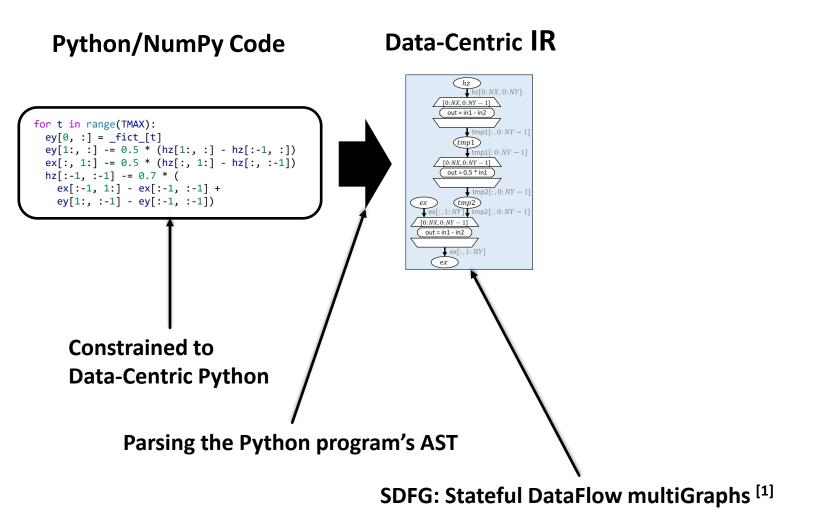








# **Data-Centric Workflow**



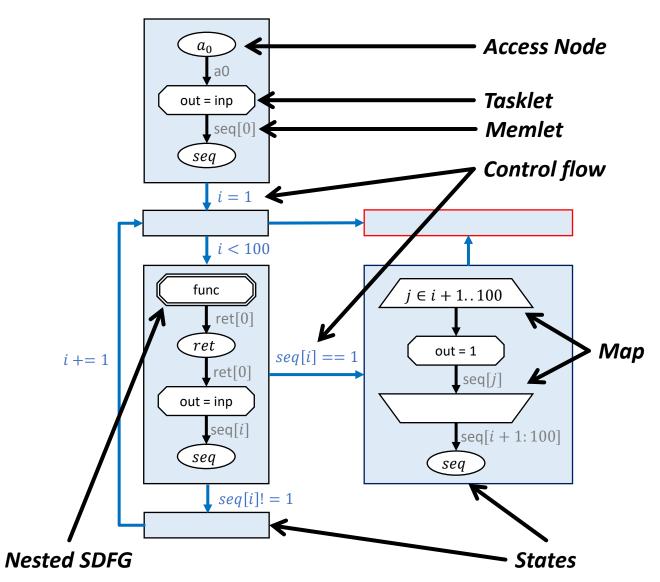




#### **Data-Centric Model and SDFG**

```
@dace.program
def top_level_func():
    seq = np.ndarray((100,), np.int32)
    seq[0] = a0
    for i in range(1, 100):
        seq[i] = func(seq[i-1])
        if seq[i] == 1:
            seq[i+1:] = 1
            break
return seq
```

Separating containers from computation Explicit data movement Control flow when necessary Coarsening

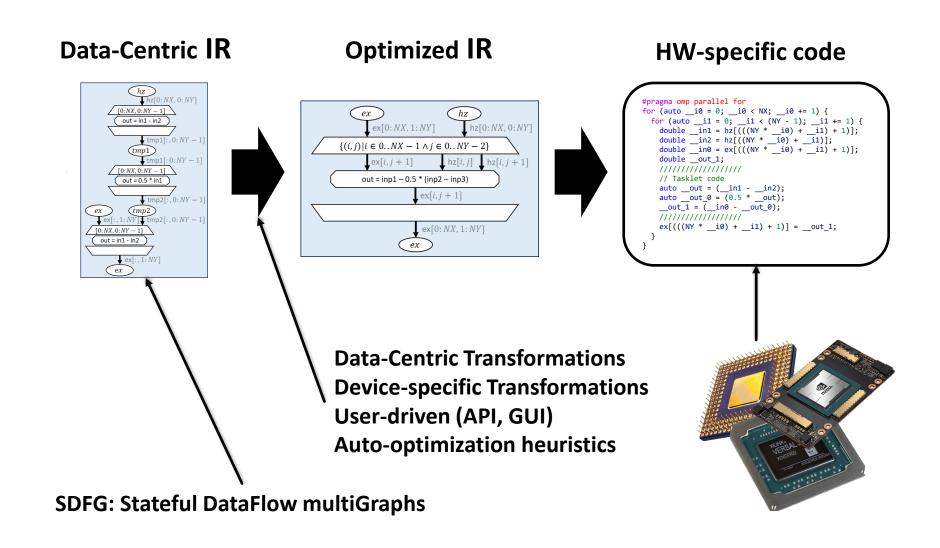








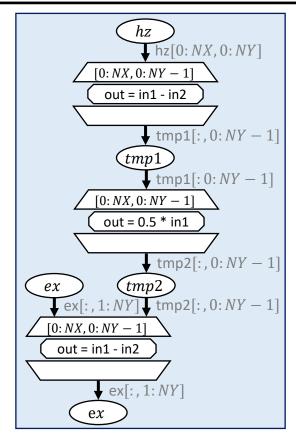
## **Data-Centric Workflow**







```
for t in range(TMAX):
    ey[0, :] = _fict_[t]
    ey[1:, :] -= 0.5 * (hz[1:, :] - hz[:-1, :])
    ex[:, 1:] -= 0.5 * (hz[:, 1:] - hz[:, :-1])
    hz[:-1, :-1] -= 0.7 * (
        ex[:-1, 1:] - ex[:-1, :-1] +
        ey[1:, :-1] - ey[:-1, :-1])
```

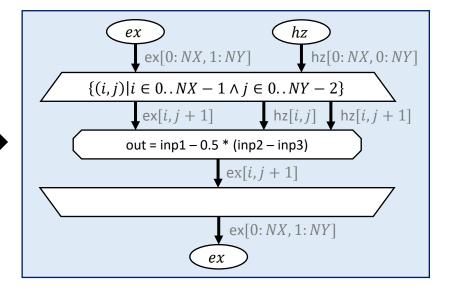


```
for t in range(TMAX):
    ey[0, :] = _fict_[t]
    ey[1:, :] -= 0.5 * (hz[1:, :] - hz[:-1, :])
    ex[:, 1:] -= 0.5 * (hz[:, 1:] - hz[:, :-1])
    hz[:-1, :-1] -= 0.7 * (
    ex[:-1, 1:] - ex[:-1, :-1] +
    ey[1:, :-1] - ey[:-1, :-1])
```

```
\mathbf{I} hz[0: NX, 0: NY]
         [0: NX, 0: NY - 1]
          out = in1 - in2
                  + \text{tmp1}[:, 0: NY - 1]
              [tmp1]
                  \frac{1}{2} tmp1[: 0: NY - 1]
         [0: NX, 0: NY - 1]
          out = 0.5 * in1
                  + \text{tmp2}[:, 0: NY - 1]
              (tmp2
 [-1] \exp[:, 1: NY] = [-1] \exp[:, 0: NY - 1]
[0: NX, 0: NY - 1]
 out = in1 - in2
         \rightarrow ex[:, 1: NY]
```

```
Subgraph Fusion
```

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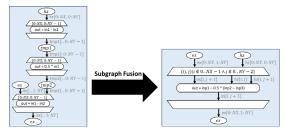








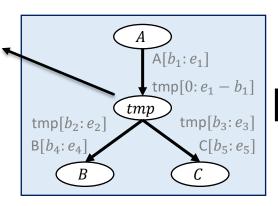
#### **Subgraph Fusion**



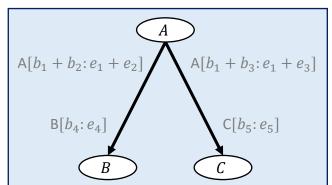




**Redundant copy** 



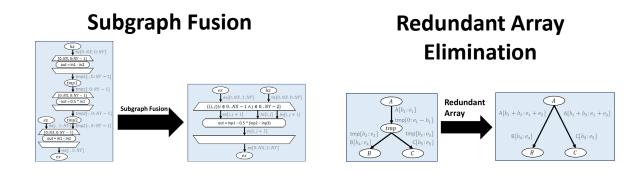
# Redundant Array

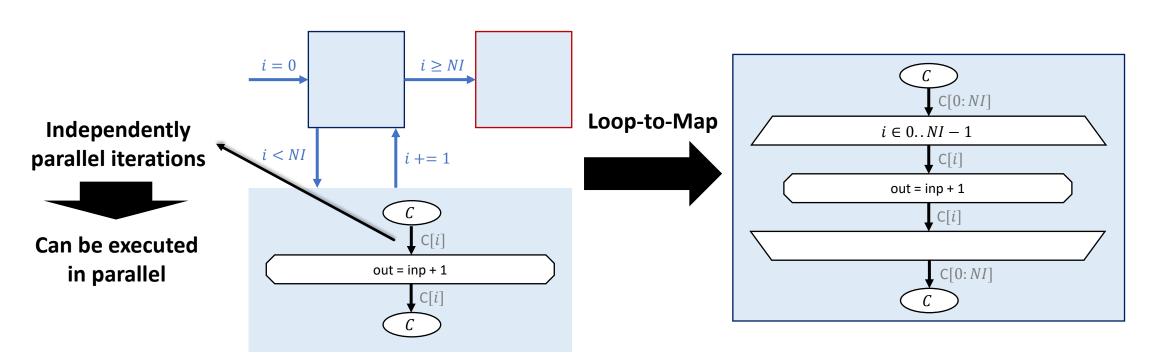








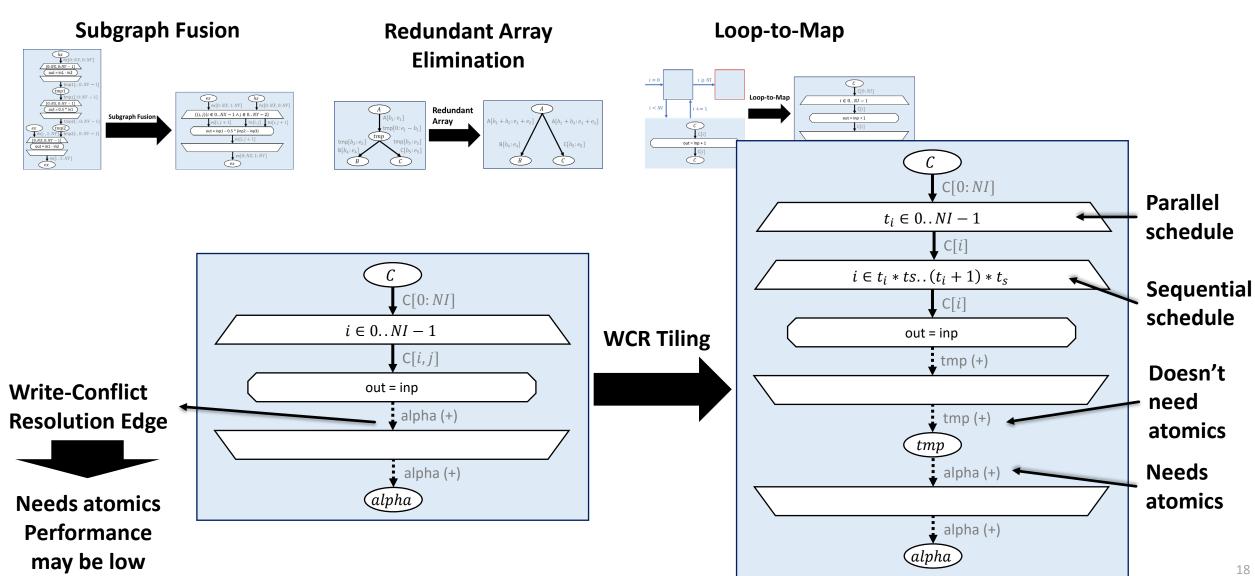






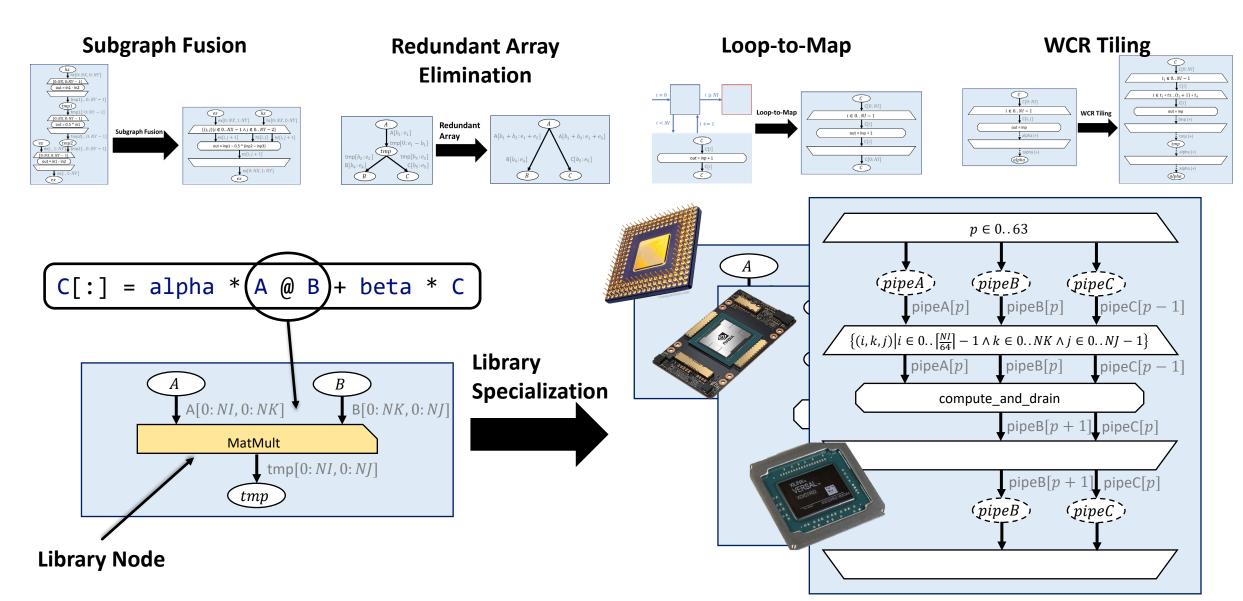










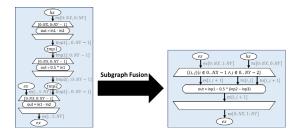




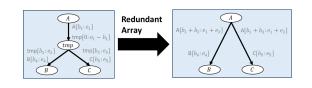




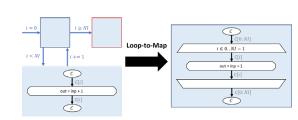
#### **Subgraph Fusion**



#### **Redundant Array Elimination**



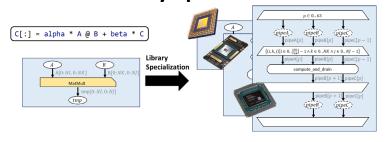
#### Loop-to-Map







#### **Library Specialization**

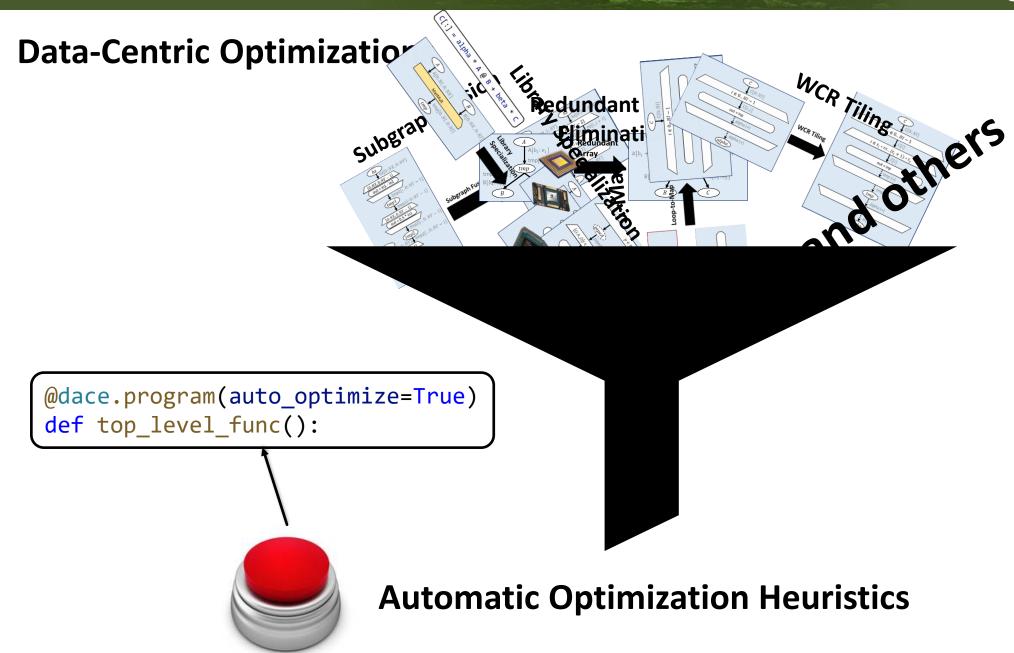


# ... and others









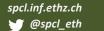


# **Auto-Optimizer Results**

	CPython	Numba	Pythran	GCC	ICC	DaCe CPU	CuPy	DaCe GPU
GEMM	1.0x	1.4x	1.4x	0.008x	0.2x	2.3x	17.0x	17.0x
2MM	1.0x	1.5x	1.3x	0.001x	0.06x	2.4x	12.9x	13.0x
3MM	1.0x	1.6x	1.2x	0.0006x	0.9x	1.7x	9.3x	9.2x
FDTD-2D	1.0x	4.1x	1.3x	3.7x	41.3x	170.0x	42.4x	112.4x
Jacobi-2D	1.0x	18.2x	21.8x	7.1x	58.6x	56.2x	75.2x	477.0x
Heat-3D	1.0x	50.1x	2.3x	24.0x	179.0x	454.0x	71.0x	1200.0x

2x Intel Xeon Gold 6130 CPU (32 cores)







# **Measuring Python Performance with NPBench**



# Only **Automatic Optimization Heuristics**



CPython+NumPy 10.6x



**CPU** 

Numba 2.47x



3.93x



9.27x



subset with a C-baseline



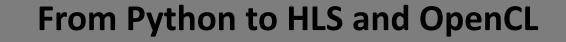


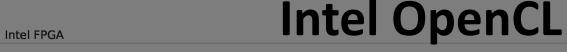


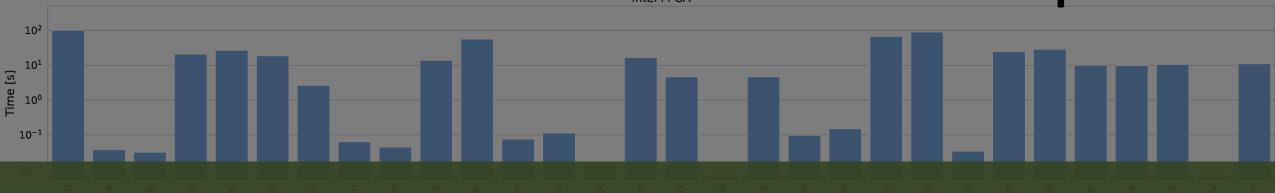




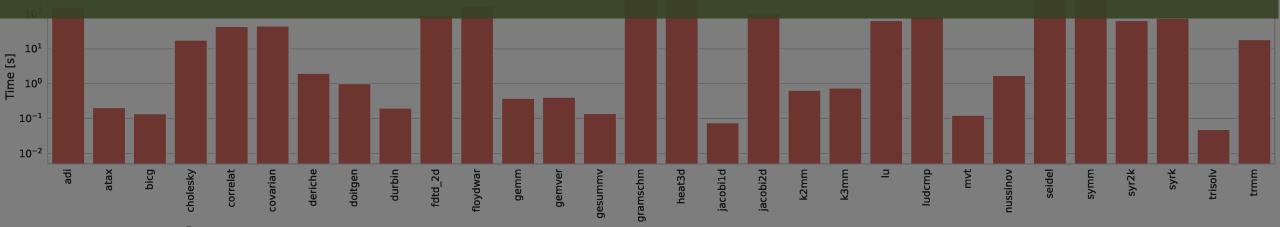








# DaCe compiles Python/NumPy code directly to FPGA!

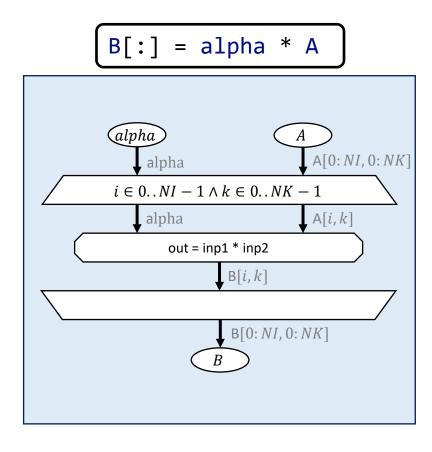


Xilinx HLS



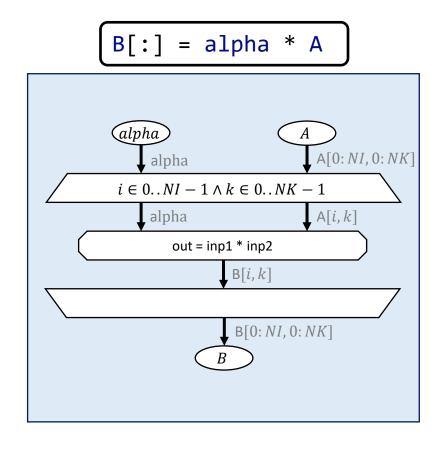


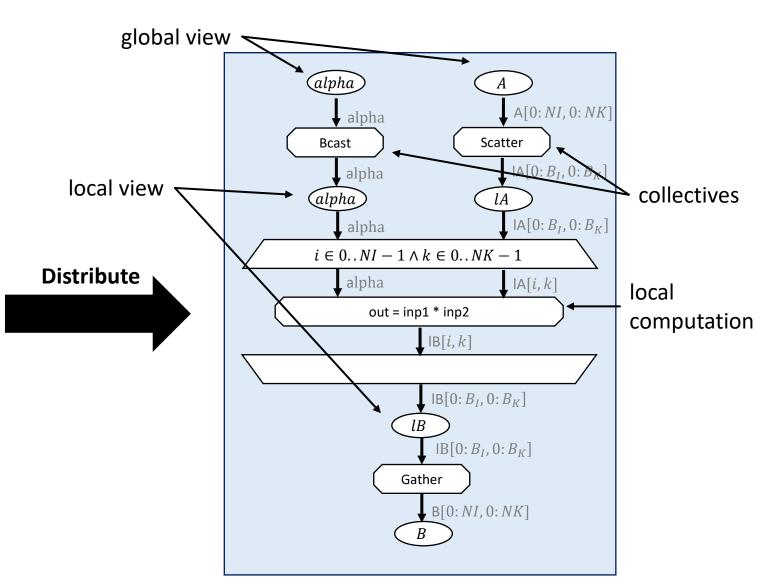
# **Transforming for Scale**





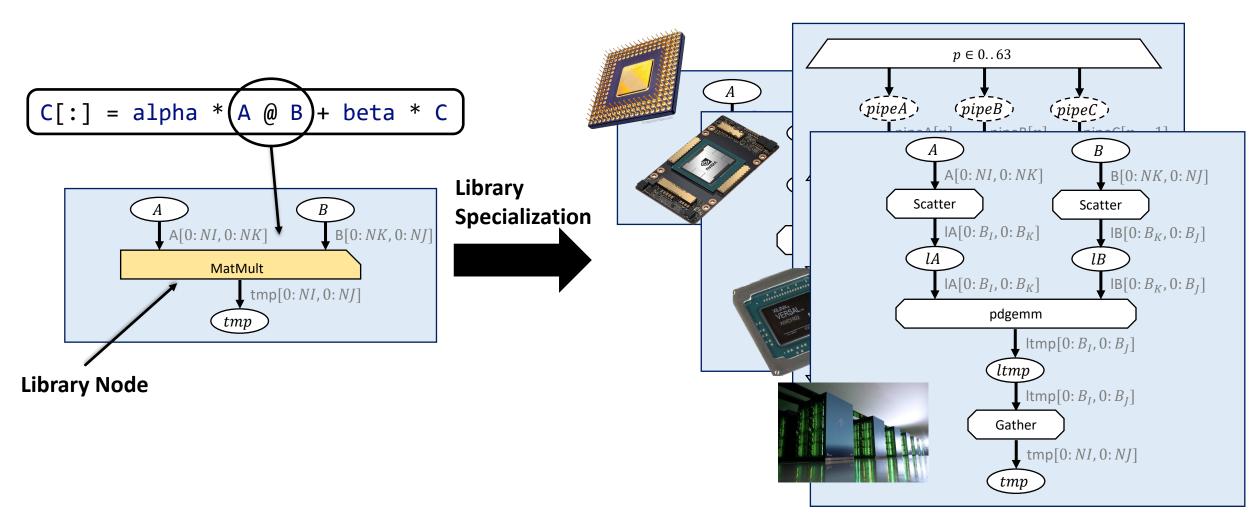
# **Transforming for Scale**







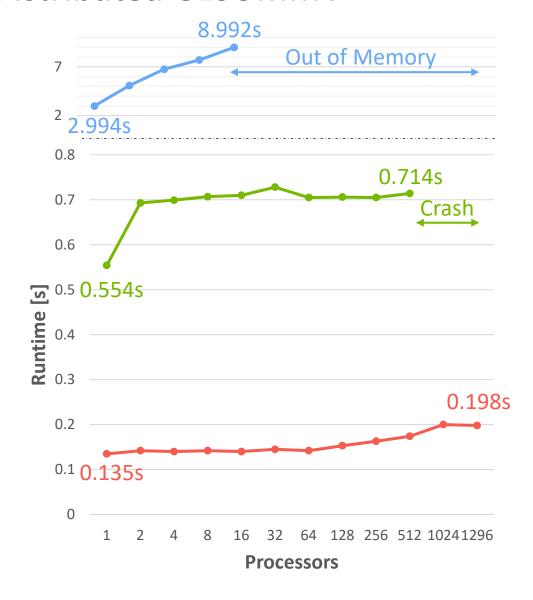
# **Library Specialization for Scale**

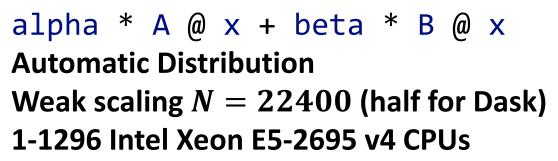


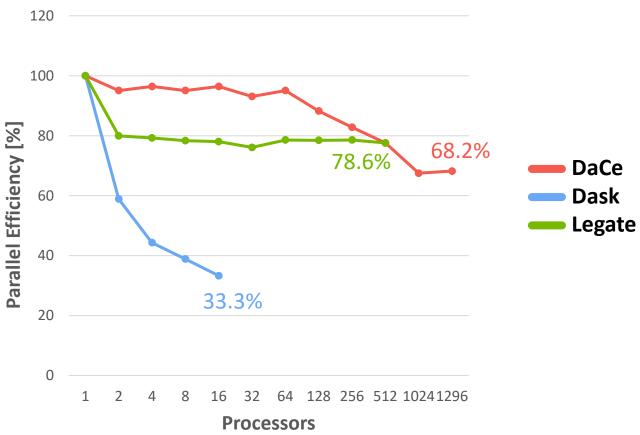




# **Distributed GESUMMV**









#### **Direct Control via Local Views**

```
for t in range(1, TSTEPS):
   B[1:-1] = 0.33333 * (A[:-2] + A[1:-1] + A[2:])
```

**Global to Local View** 

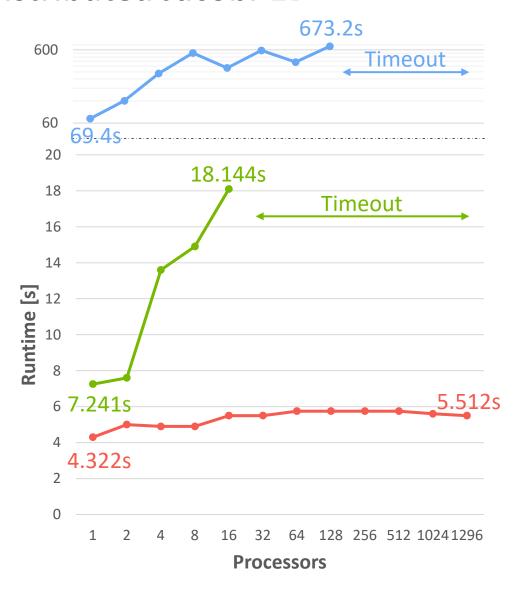
**User-driven Communication Pattern using the Data Communication Interface** 

```
req = np.empty((4,), dtype=MPI_Request)
for t in range(1, TSTEPS):
    dc.comm.Isend(A[1], nw, 3, req[0])
    dc.comm.Isend(A[-2], ne, 2, req[1])
    dc.comm.Irecv(A[0], nw, 2, req[2])
    dc.comm.Irecv(A[-1], ne, 3, req[3])
    dc.comm.Waitall(req)
    lB[1+woff:-1-eoff] = 0.33333 * (
        lA[woff:-2-eoff] + lA[1+woff:-1-eoff] + lA[2+woff:-eoff])
```

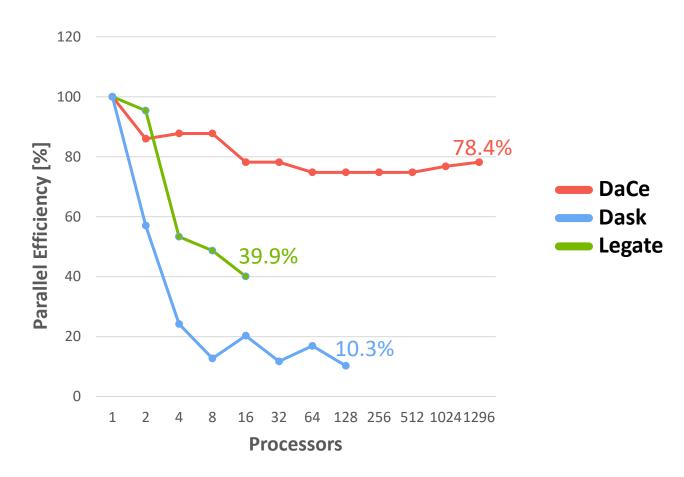




# **Distributed Jacobi-2D**



# Data Communication Interface Weak scaling N=1300 1-1296 Intel Xeon E5-2695 v4 CPUs

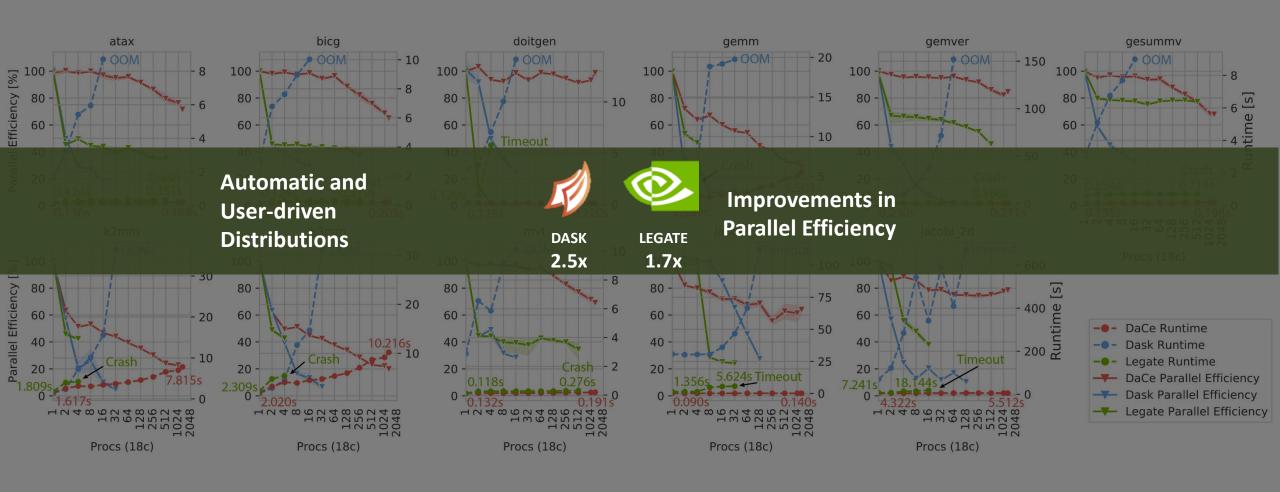








# **Distributed Results**

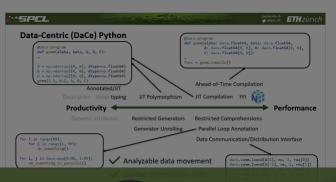








# **Conclusions**



# **Productivity**



# https://github.com/spcl/dace

# **Performance**





**Portability**