

Logic Circuit and Design Syllabus

정보컴퓨터공학부 유영환 (ymomo@pusan.ac.kr)

Textbook & Class



❖ Title: Digital Design and Computer Architecture (RISC-V Edition)

- Author: S. L. Harris and D. Harris
- Publisher: Morgan Kaufmann Publishers

❖ Lecture notes from PLATO

❖ Video clips

- [How Computers Work: Binary & Data](#)
- [How Computers Work: Circuits & Logic](#)

Schedule



Week	Contents	Week	Contents
1	Ch1. From Zero to One	9	Ch3. Sequential Logic Design
2		10	Ch5. Digital Building Blocks
3	Ch2. Combinational Logic Design	11	
4		12	Ch6. Architecture
5		13	Ch7. Microarchitecture
6	Ch3. Sequential Logic Design	14	Ch8. Memory Systems
7		15	Final Exam (6/19)
8	Midterm Exam (4/24)		

Instructor & Grading



❖ Instructor: Yoo, Younghwan

- Email: ymomo@pusan.ac.kr
- Phone: 510-2485
- Office: Room #509, Bldg. #06
- Office Hour: Tue 13:00~15:00



❖ Teaching Assistant:

- Email:
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- Office: Room #403-1, Bldg. #06

❖ Grading

- Midterm (40%), Final (40%), Assignment (10%), Class Participation (10%)