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| **Logic Circuit Design Homework #02** | | | |
| Due date | Apr. 15th, 2024 | Instructor | Yoo, Younghwan |
| Student ID |  | Name |  |

1. Write a Boolean equation in sum-of-products canonical form for each of the truth tables.

2. Minimize each of the Boolean equations from Problem 1 using Boolean theorems. Show the minimization process.

3. Simplify each of the following Boolean equations. Sketch a combinational circuit implementing the simplified equation.

4. Write a minimized Boolean equation for the function performed by the circuit in the figure below:

