# CS 181u Applied Logic

# Lecture 11

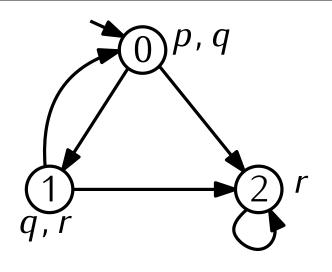
# Today's class

Quick Review
LTL and CTL

## Verifying properties of a stack

Modeling, specifying, and verifying stack properties.

# Linear Temporal Logic (LTL) Review



Some paths of  $\mathcal{M}$ 

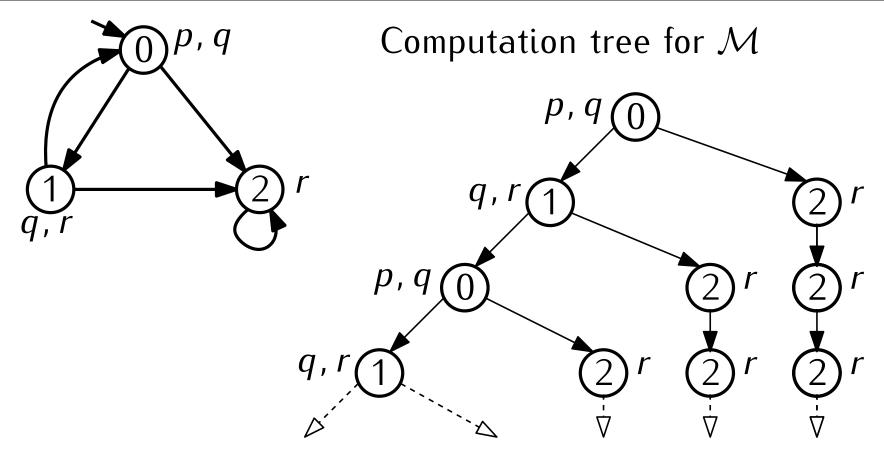
$$0 \rightarrow 1 \rightarrow 0 \rightarrow 2 \rightarrow 2 \rightarrow \bullet \bullet \bullet$$

$$p, q q, r p, q r r$$

$$\mathcal{M} \models \phi \Leftrightarrow \forall \pi \ [\pi \models \phi]$$

LTL Model Checking

# Computation Tree Logic (CTL) Review



Computation Tree Logic (CTL) expresses properties of "alternative timelines".

$$\mathcal{M} \models \phi \Leftrightarrow \forall s \in I \ s \models \phi$$

CTL Model Checking

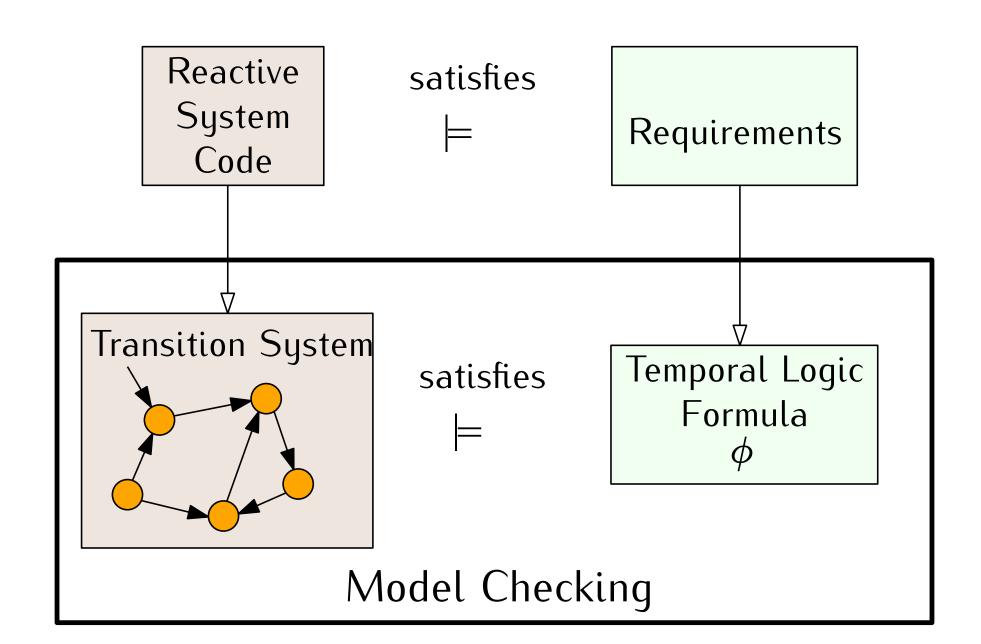
HW questions?

LTL questions?

CTL questions?

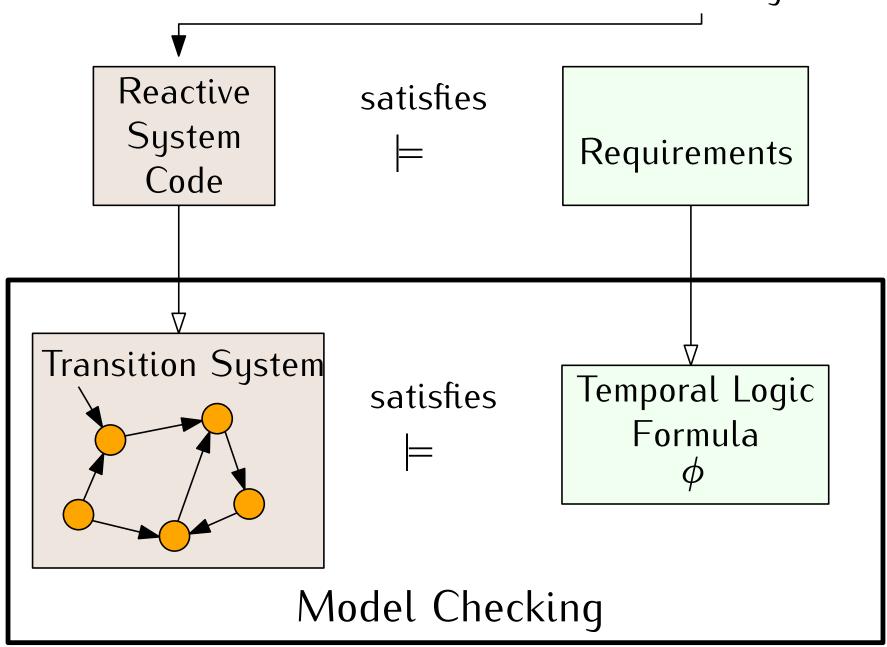
vSMV questions?

# The Big Picture

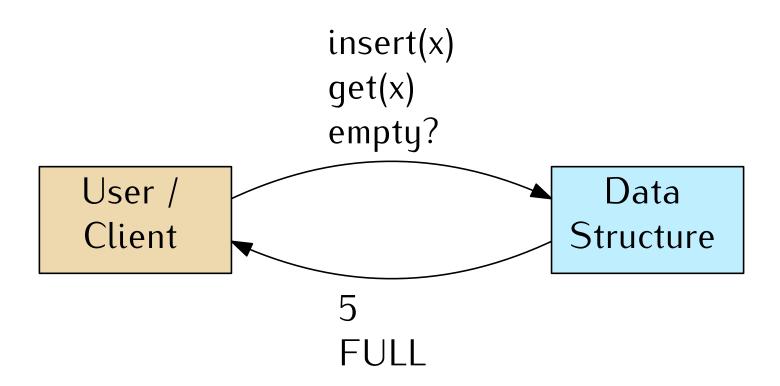


# The Big Picture

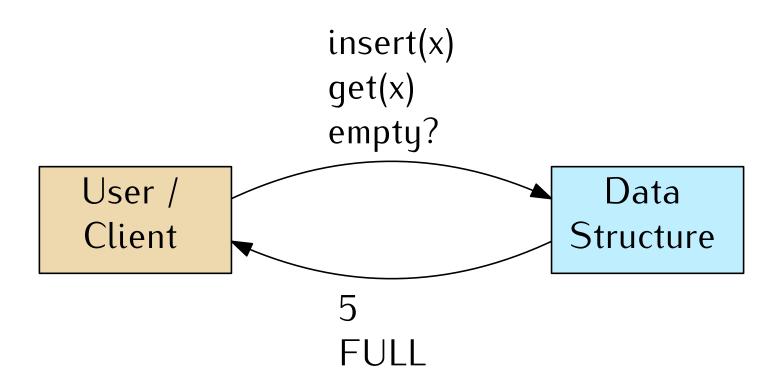
Can we think of a data structure as a reactive system?



# Data Structures as Reactive Systems

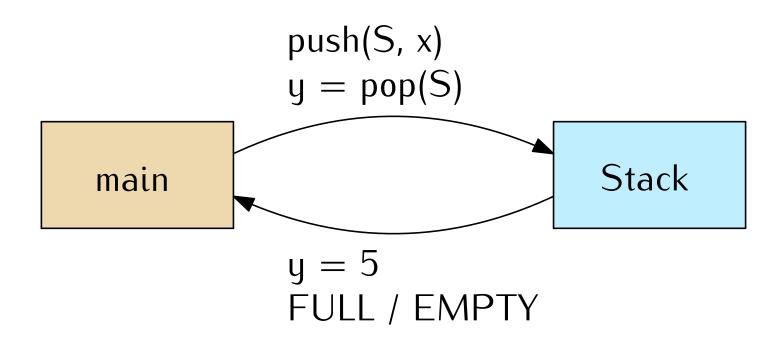


# Data Structures as Reactive Systems

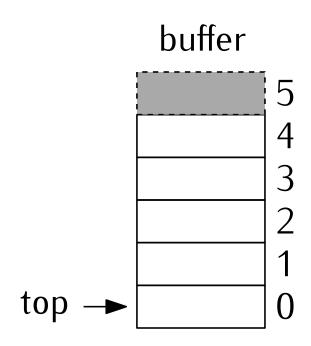


### Both the user / client and data structure:

Internal state that changes over time (temporal aspects). Indefinite lifetime, "runs" forever.

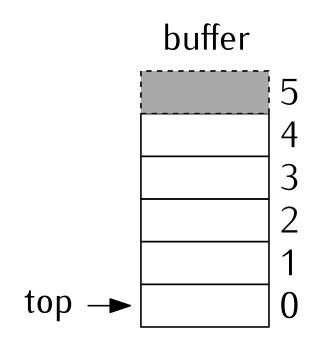


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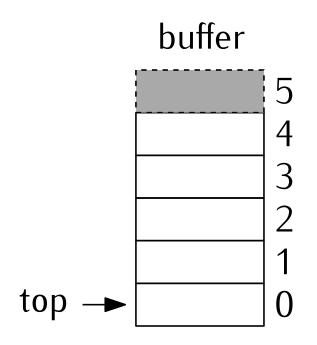
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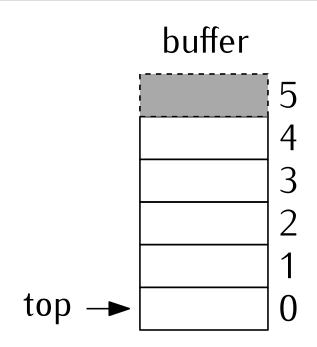


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When top = 0, stack is empty.

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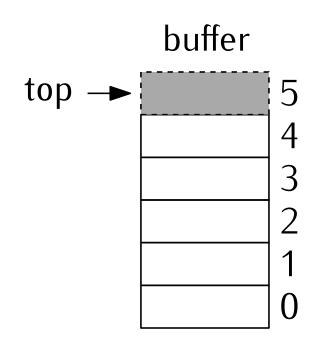
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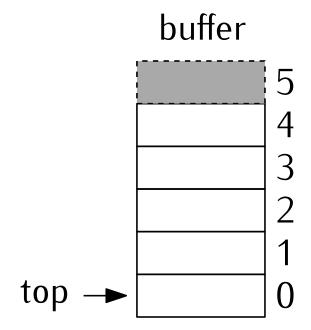
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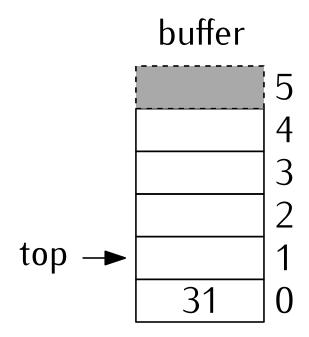
When top = SIZE, stack is full.

Attempting to push when stack is full does nothing.



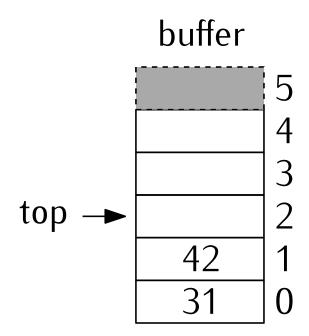
An example execution

push 31



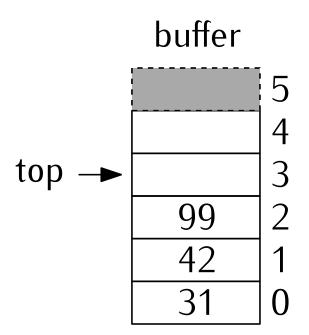
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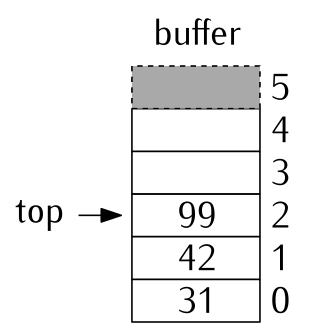
push 31
push 42

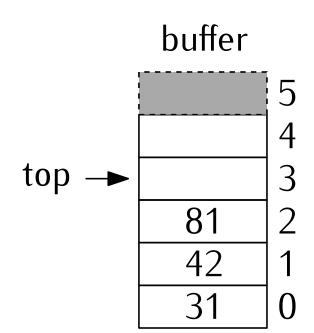


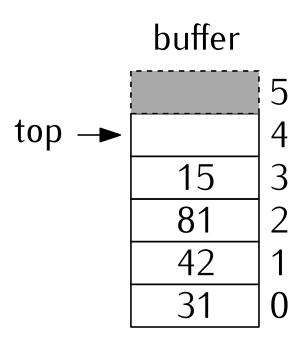
An example execution

push 31push 42push 99

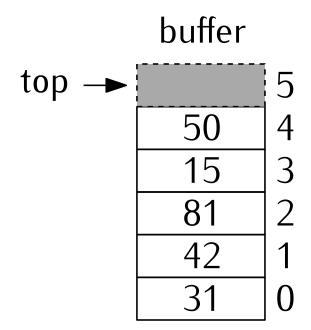








```
push 31
push 42
push 99
v = pop v = 99
push 81
push 15
push 50
```



### An example execution

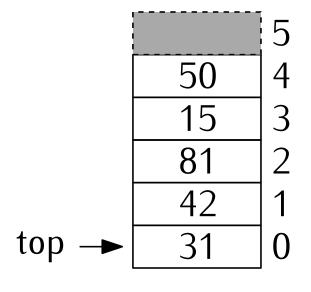
```
push 31
push 42
push 99
v = pop v = 99
push 81
push 15
push 50
push 25 nothing happens
```

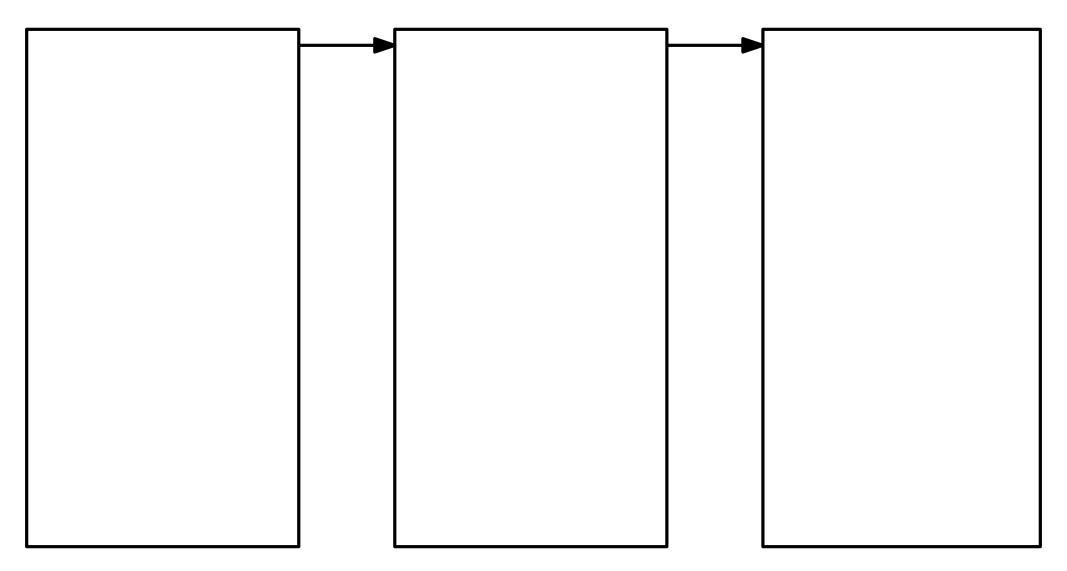
# buffer top → 5 50 4 15 3 81 2 42 1 31 0

### An example execution

```
push 31
push 42
push 99
v = pop \quad v = 99
push 81
push 15
push 50
push 25 nothing happens
v = pop \quad v = 50
v = pop \quad v = 15
v = pop \quad v = 81
v = pop \quad v = 42
v = pop \quad v = 31
v = pop \quad v = NULL
```

### buffer

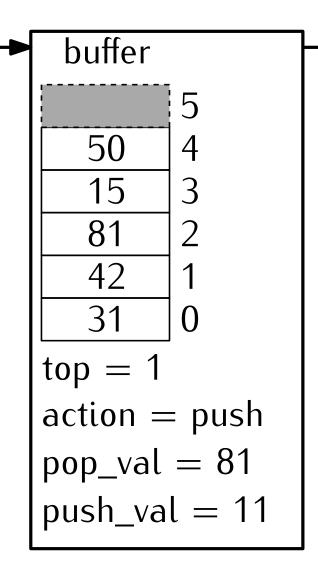


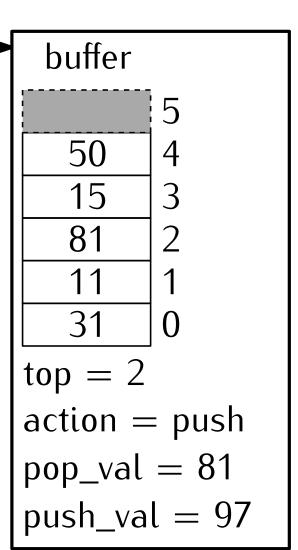


buffer	<b></b>		
5			
50 4 15 3			
81 2			
31 1 0			
top = 2			
action = pop			
pop_val = 15			
push_val = 50			

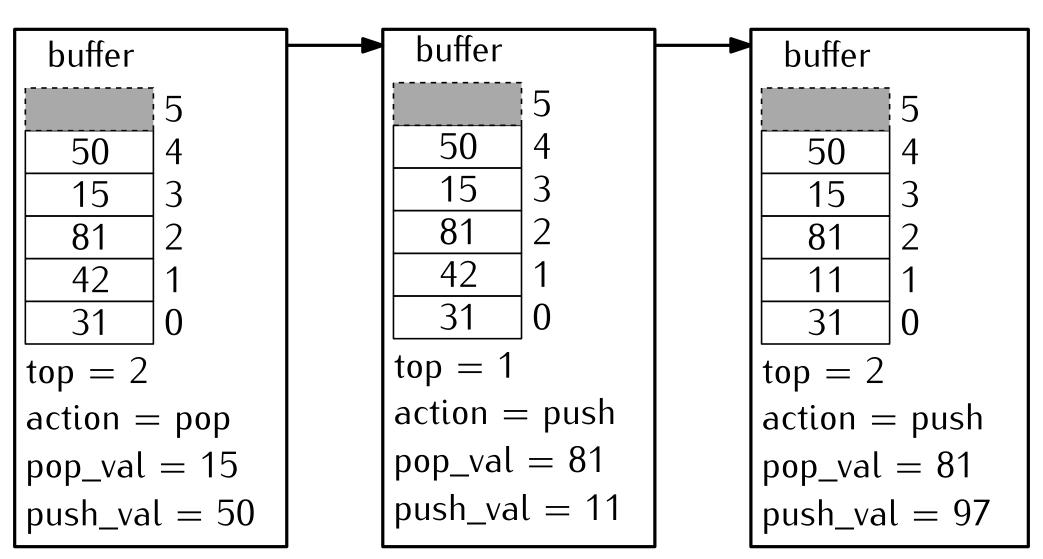
buffer	buffer	
5	50 5 50 4	
50 4 15 3	15 3	
81 2 42 1	81 2 42 1	
31 0	31 0	
top = 2	top = 1	
action = pop	action = push	
pop_val = 15	pop_val = 81	
push_val = 50	push_val = 11	

buffer				
!	5			
50	4			
15	3			
81	2			
42	1			
31	0			
top = 2				
action = pop				
$pop_val = 15$				
push_val = 50				





What do the states and transitions look like?



Our vSMV model should define all possible state transitions.

Overview

```
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action = { push, pop }
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next(buffer) := update a location in the buffer based on top if action = push. If action = pop, no update.

```
#define SIZE 5

MODULE main
VAR
    pop_val : {NULL, x, y, z};
    push_val : {x,y,z};
    action : {push, pop};
    s : stack(action, push_val, pop_val);
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Why  $\{x, y, z\}$ ?

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Why 
$$\{x, y, z\}$$
?

We have abstracted away the type of the stack. This abstraction will allow us to make statements about three distinct values in the stack, without worrying about what they are.

```
MODULE stack (action, push_val, pop_val)
 VAR
    top : 0 .. SIZE;
    buffer: array 0.. SIZE - 1 of \{NULL, x, y, z\};
  DEFINE
    full := top = SIZE;
    empty := top = 0;
  ASSIGN
    init(top) := 0;
    next(top) :=
      case
        (action = push) & (top < SIZE) : top + 1;
        (action = pop) & (top > 0) : top - 1;
        TRUE : top;
      esac;
    next(pop_val) :=
      case
        action = pop & !empty : buffer[top];
       TRUE: NULL;
      esac;
```

How to update the state of the buffer? We'd like to write something like next(buffer) := ?? In  $\nu SMV$ , we have to update individual array elements.

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```
next(buffer[3]) := ? : conditional test then-exp else-exp
```

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CTL property: For all possible system states, if the stack is full, then it is possible that the stack is eventually empty.

The most important property of a stack:

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Last In, First Out (LIFO)

This will be part of your next HW.

Karl Popper: The Logic of Scientific Discovery

"My proposal is based on an asymmetry between verifiability and falsifiability; an asymmetry which results from the logical form of universal statements. For these are never derivable from singular statements, but can be contradicted by singular statements."

#### Edsger Dijkstra

Program testing can be used to show the presence of bugs, but never to show their absence!

Titus Bartik, et. al.: Designing for Dystopia: Software Engineering Research for the Post-apocalypse. (FSE 2016)

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Literary theorists have long recognized the trade-offs in optimistic and pessimistic thinking through utopias and dystopias.

Research suggests that scientists are overwhelmingly optimistic, and subject to the effect of optimism bias [1].

Software engineering researchers have a tendency to be optimistic.

Though useful, optimism bias bolsters unrealistic expectations towards desirable outcomes.

Framing software engineering research through dystopias mitigates optimism bias and engender more diverse and thought-provoking research directions.

[1] D. A. Armor and S. E. Taylor. When predictions fail: The dilemma of unrealistic optimism.

#### In class activity:

- 1. Come up with one or two interesting stack properties that would be important to verify. Write it down as a legible English sentence.
- 2. In a group of two or three, swap properties. Identity if the property is LTL or CTL. Translate the property to LTL or CTL.
- 3. Regroup and discuss your properties and translations.
- 4. Choose one to write on the board (both in English and CTL / LTL) to explain to the rest of the class.

#### Some hints:

What could go wrong? Negate that property.

What should go right? Assert that property.

What should happen if push x is followed directly by pop?

What should happen if we try to pop an empty stack?

Our examples from earlier:

$$G(0 \le s.top \land s.top \le SIZE)$$
  
 $G \neg (s.full \land s.empty)$   
 $AG (s.full \rightarrow EF s.empty)$ 

### **Future homework**

Translate and verify some stack properties.

Model and verify a queue.

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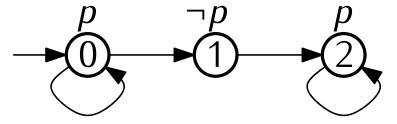
In words, two formulas are not equivalent if we can find a transition system that satisifes one formula but not the other.

Consider these two temporal formulas F G p AF AG p

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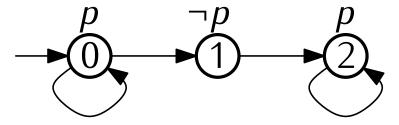
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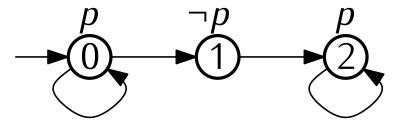


Paths of  $\mathcal{M}$  look like:

 $0^{\omega}$  or  $0*1.2^{\omega}$ 

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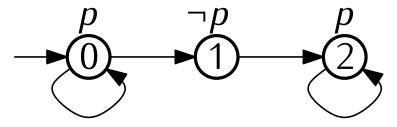
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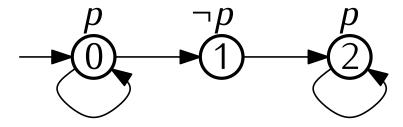
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 $\mathcal{M} \models F G p$ 

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Computation tree:

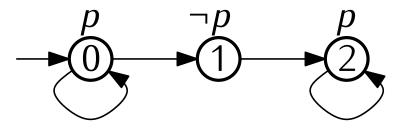
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 $p_{\bigcirc}$ 

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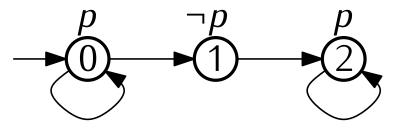
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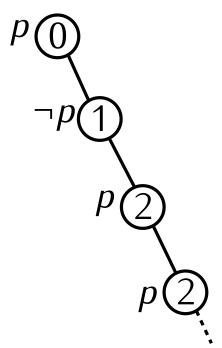


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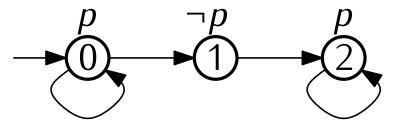
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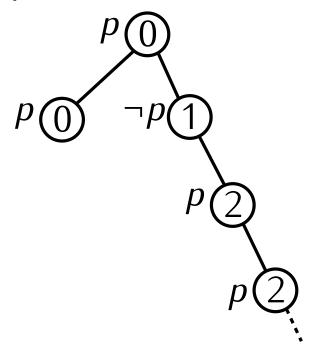


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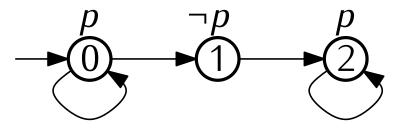
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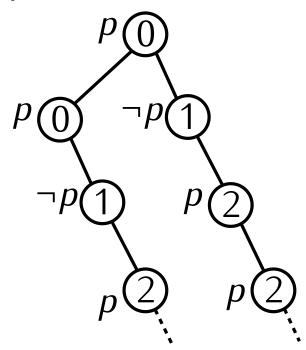


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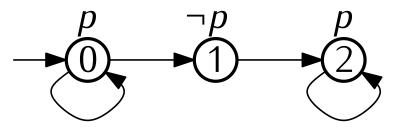
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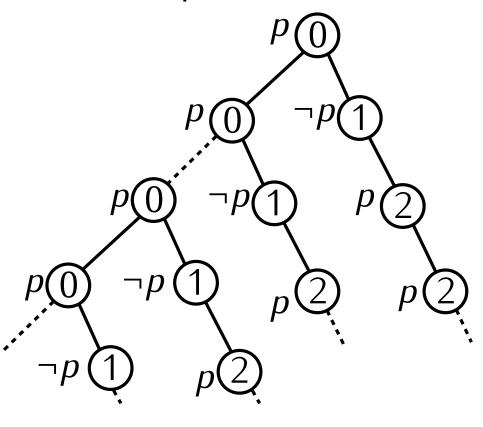


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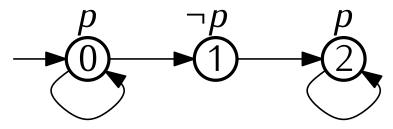
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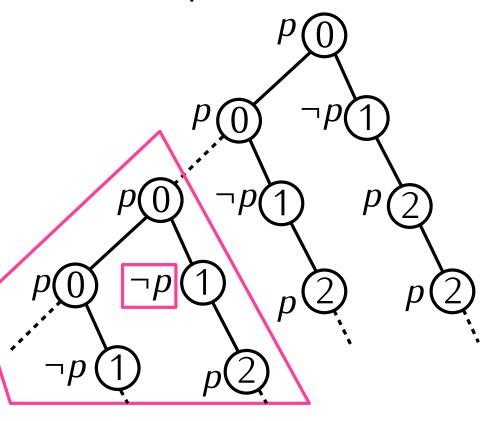


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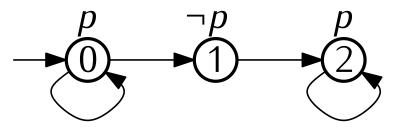
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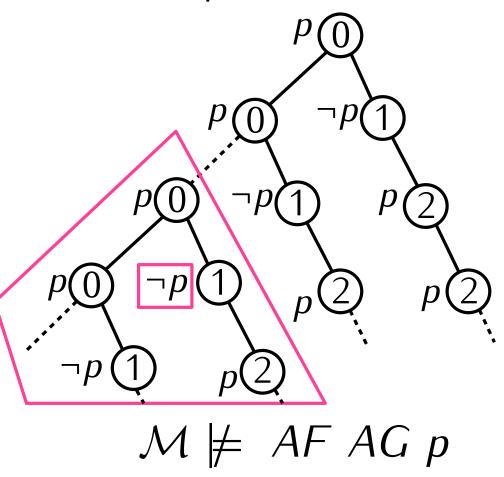


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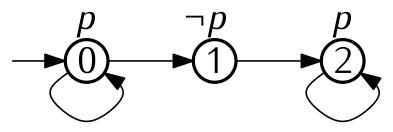
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Paths of  $\mathcal{M}$  look like:

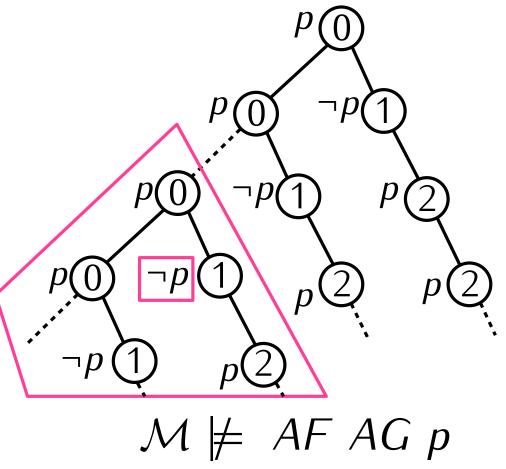
$$0^{\omega}$$
 or  $0*1.2^{\omega}$ 

Sequences of propositions:

$$p, p, p, p, p, \ldots$$
  
 $p, p, p, \ldots, \neg p, p, p, p, \ldots$ 

$$\mathcal{M} \models F G p$$

Computation tree:



On your HW: Show two formulas not equivalent.