HTCS		НТС7								нтс9									нтсп									
		1_Cache7							НТС10	L_Cache9								HTC12	1_Cachell									
		Decoder?							Decoder9									Decoder11										
1,Cache8		Lanel21	Lane122	Lane123	Lane124	Lane125	Lane126	Lame127	Lane128	L_Cache10		Lanel53	Lane154	Lane155	Lane156	Lane157	Lane158	Lane159	Lane160	I_Cache12	Lane185	Lane186	Lane187	Lane188	Lanel89	Lane190	Lane191	Lane192
		Lanel13	Lanel14	Lane115	Lanel16	Lanel17	Lanel18	Lanel19	Lane120			Lanel45	Lane146	Lane147	Lane148	Lanel49	Lane150	Lane151	Lane152		Lanel77	Lane178	Lane179	Lanel80	Lanel81	Lane182	Lane183	Lane184
Decoder8							╙					\square							<u> </u>	Decoder12			<u> </u>		<u> </u>	Ш		
	Reg8	Lane105	Lane106	Lane107	Lanel@	Lanel09	Lanel10	Lanel11	Lane112	Reg9 ALU9	Reg10	Lane137	Lane138	Lane139	Lane140	Lanel41	Lane142	Lane143	Lane144	Reg11 Reg12 ALU11 ALU12	Lanc169	Lane170	Lane171	Lanel72	Lanel73	Lane174	Lane175	Lane176
D_Cache7 D Write_Back8	D_Cache8	Lanc97	Lane98	Lane99	Lane100	Lane101	Lane102	Lane103	Lanc104	D_Cache9 Write_Back	D_Cache10	Lanc129	Lane130	Lane131	Lane132	Lane133	Lane134	Lane135	Lane136	D_Cachel1 D_Cachel2 Write_Back12	Lanel61	Lane162	Lane163	Lane164	Lanel65	Lanel66	Lane167	Lanc168
Write_Back8	WTHE_BREEAS	Write_Back7								Write_Back	Write_Back9							Write_Back12	Write_Back11									
ИТС2		нтсі									нтсз								нтся									
		I_Cache1								нтс4		I_Cache3							нтс6	LCache5								
		Decoder I									Decoder3								Decoder5									
I_Cache2		Lane25	Lanc26	Lane27	Lane28	Lane29	Lane30	Lane31	Lane32	LCsche4		Lame57	Lane58	Lane59	Lane60	Lane61	Lanc62	Lanc63	Lanc64		Lane89	Lane90	Lanc91	Lane92	Lane93	Lane94	Lane95	Lanc96
1,0.0002	Lanel7	Lane 18	Lane19	Lanc 20	Lanc21	Lane22	Lane23	Lane24	Lane49		Lane50	Lane51	Lane52	Lane53	Lane54	Lane55	Lane56	I_Cache6	Lane81	Lame82	Lane83	Lane84	Lane85	Lane86	Lane87	Lanc88		
	Reg2	Lanc9	Lanc10	Lanell	Lane12	Lane13	Lane14	Lane15	Lane16	Deceder4	Reg4	Lame41	Lune42	Lane43	Lane44	Lane45	Lane46	Lane47	Lane48	Decoder6 Reg5 Reg6	Lane73	Lane74	Lane75	Lane76	Lane77	Lane78	Lane79	Lanc80
D_Cachel D	ALU2 D_Cache2	Lanel	Lane2	Lane3	Lane4	Lane5	Lanc6	Lane7	Lane8		ALU4 D_Cache4	Lane33	Lane34	Lane35	Lane36	Lane37	Lane38	Lane39	Lanc40	ALUS ALU6 D_Cache5 D_Cache6	Lame65	Lane66	Lanc67	Lane68	Lanc69	Lane70	Lane71	Lane72
Write_Back2		Write_Back1								Write_Back	54	Write_Back3							Write_Back6	Write_BackS								