HTC2	HTCl				HTC4	нтсэ				
						1				
	E.Ordel					LGdd				
I_Cache2					I_Cache4					
										
		Decor	lerl				Decoder2			
Decoder2					Decoder4					
						_				
Regl	Lanel	Lane2	Lane3	Lane4	Reg2	Lane5	Lanc6	Lane7	Lane8	
ALUI					ALU2					
D_Cache1					D_Cache2					
Write_Back2					Write_Back4					
		Write_	Back I				Weie_Back3			
				1.2_0	ache1					