

6

5

4

3

2

1

D

C

B

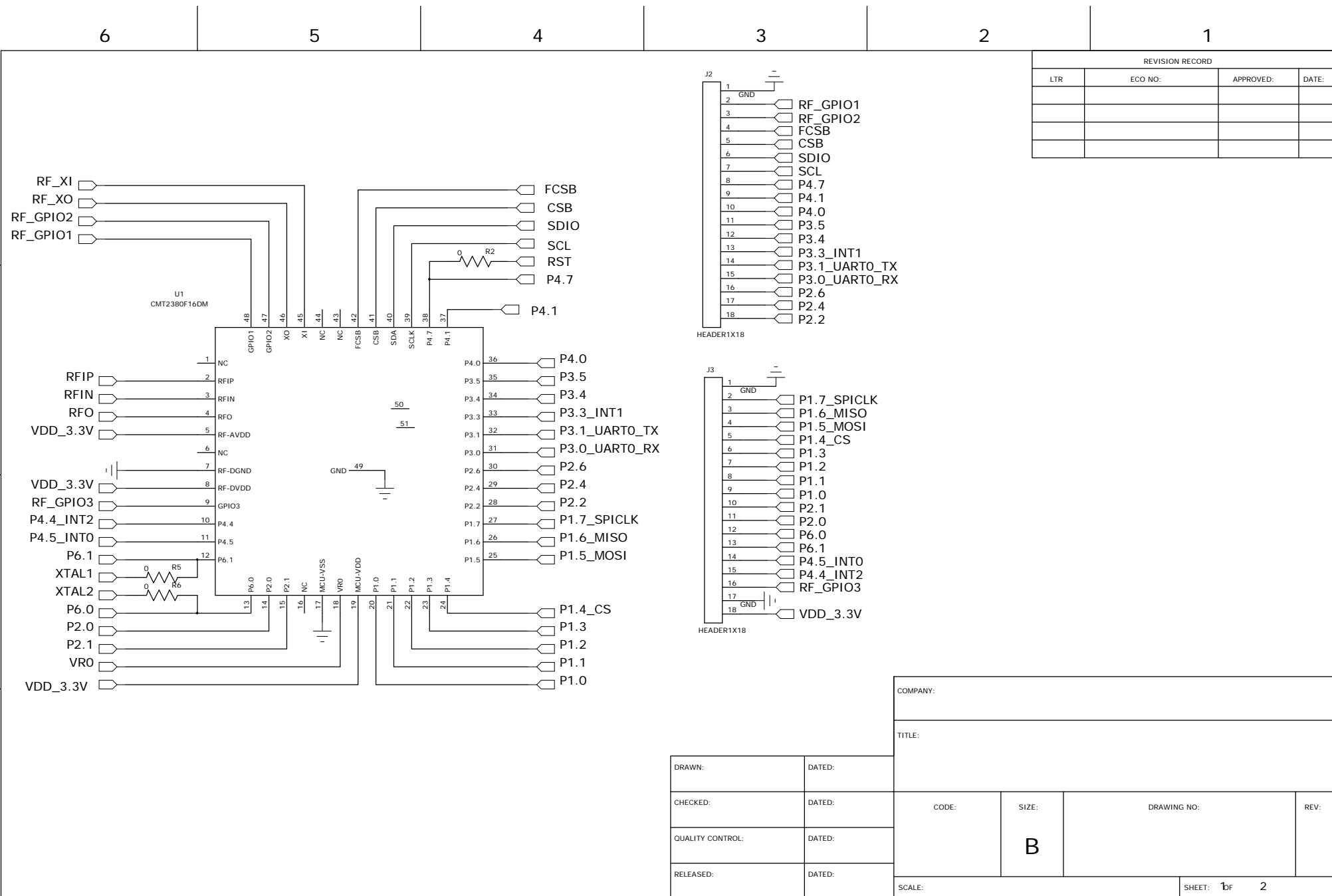
A

D

C

B

A



6

5

4

3

2

1

D

C

D

C

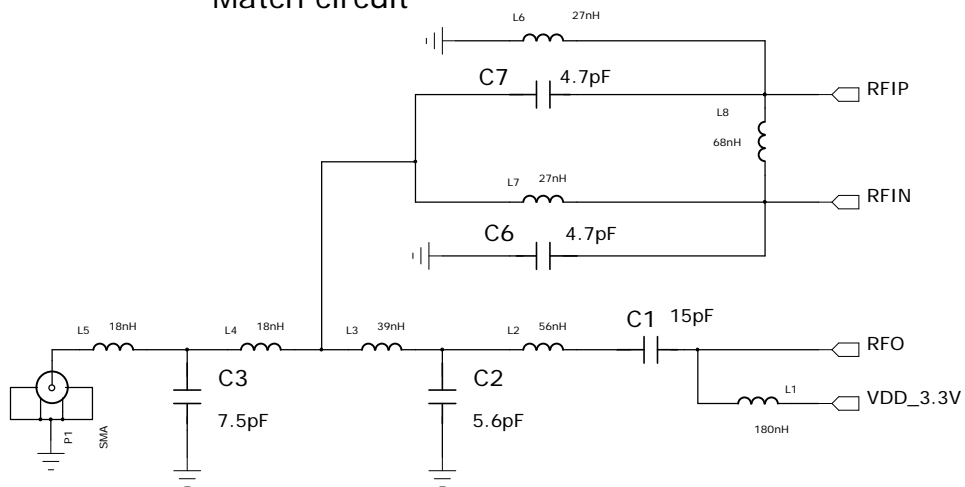
B

B

A

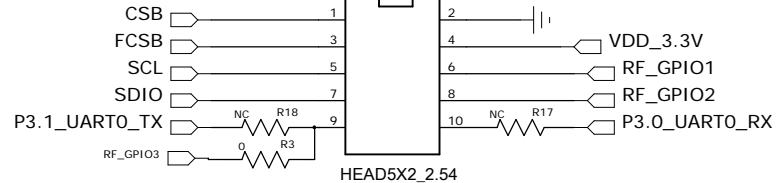
A

Match circuit

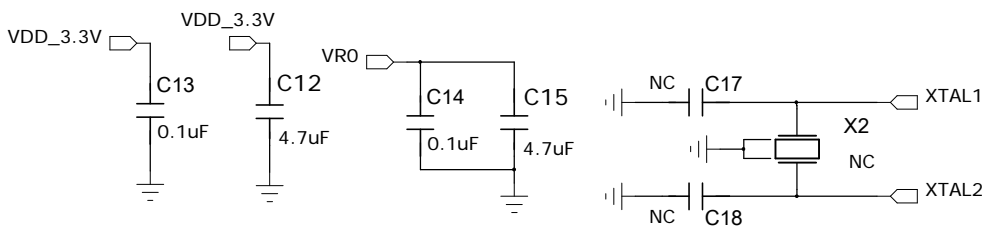


RF_GPIO

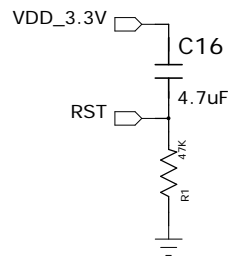
RF_PROGRAM



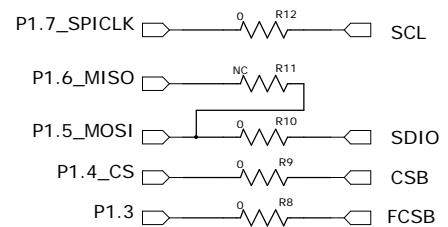
MCU_VDD & MCU_XTAL



RST



SPI

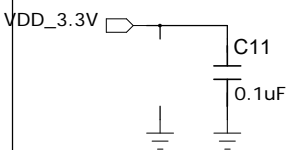


Software SPI: R10 = NC Hardware SPI: R10 = 470

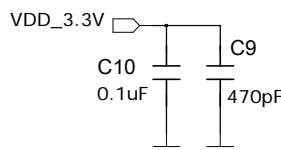
COMPANY:

TITLE:

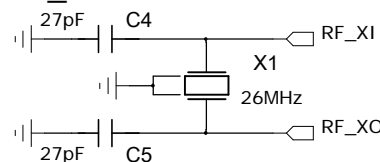
DVDD



AVDD



RF_XTAL



DRAWN:

DATED:

CHECKED:

DATED:

QUALITY CONTROL:

DATED:

RELEASED:

DATED:

CODE:

SIZE:

DRAWING NO:

REV:

B

SCALE:

SHEET: 2 of 2