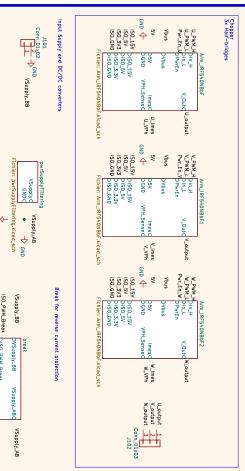
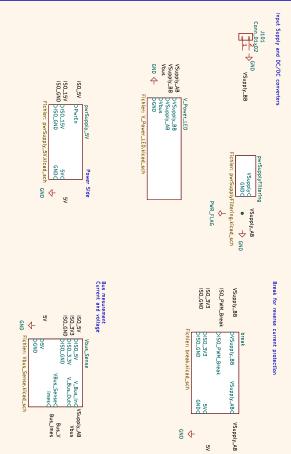
Power Side

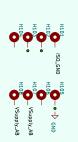


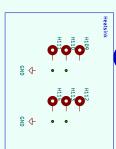


FAN 2 outputs

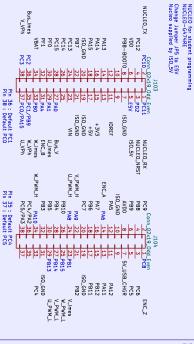
ISO_GND ISO_PWM_FAN

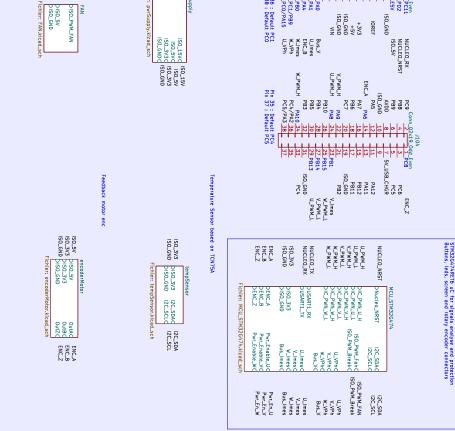


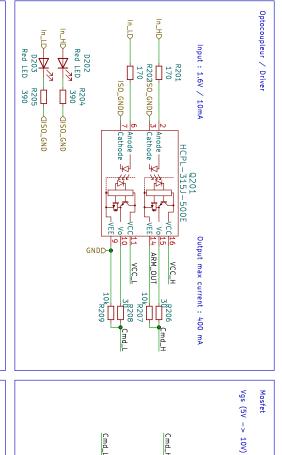


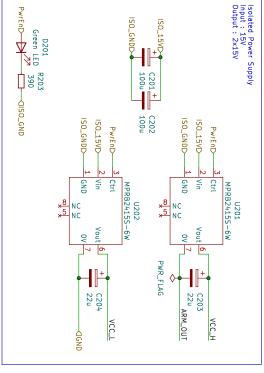


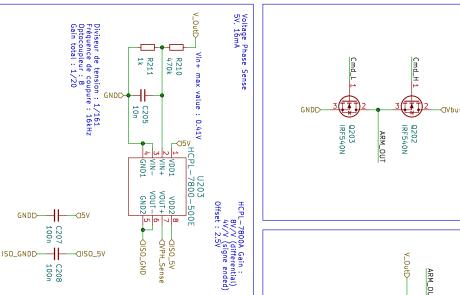
MCU Side

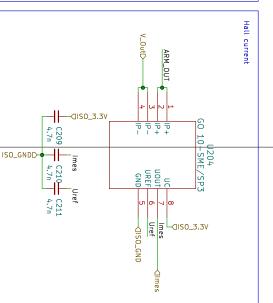








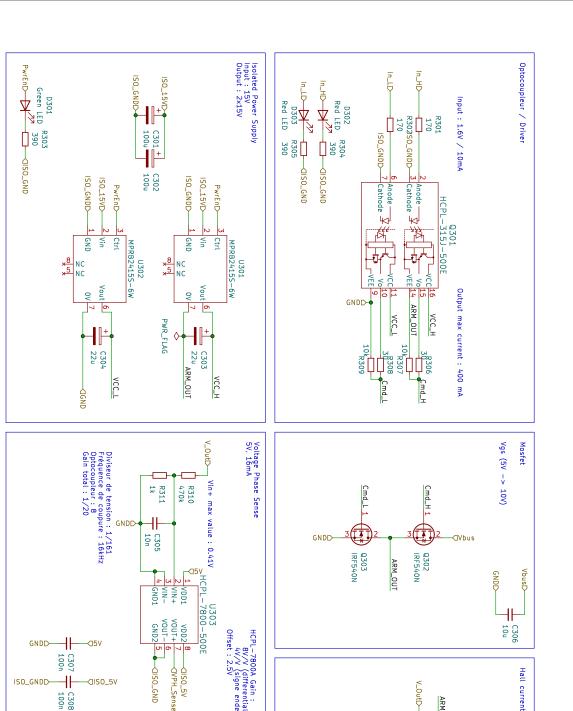




VbusD

± C206

GNDD



HCPL-7800A Gain:

8V/V (differential)

4V/V (signe ended)

Offset: 2.5V

- GVPH_Sense

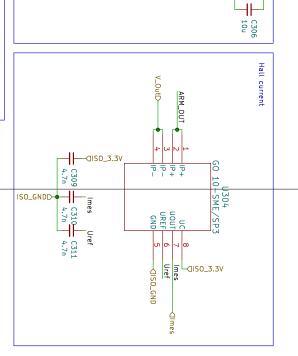
GNDD

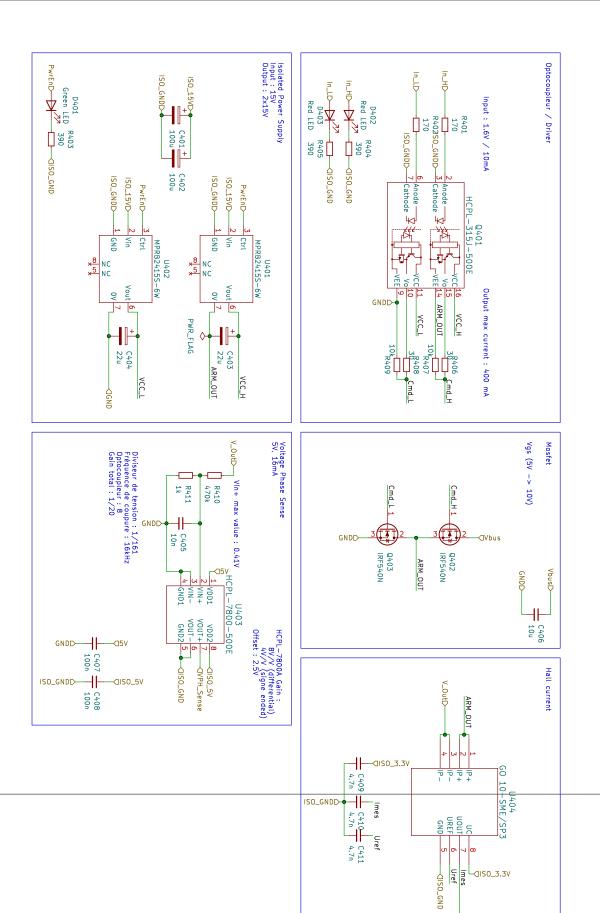
ISO_GNDD

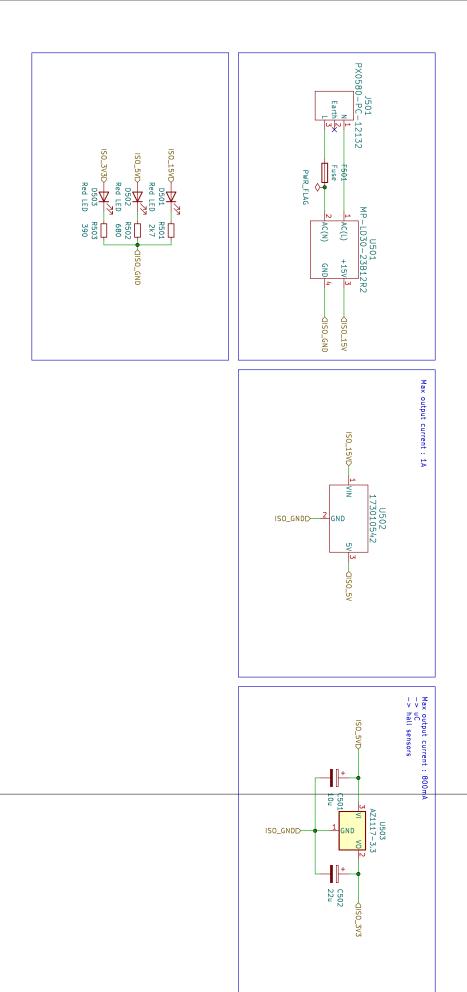
C307 — C308

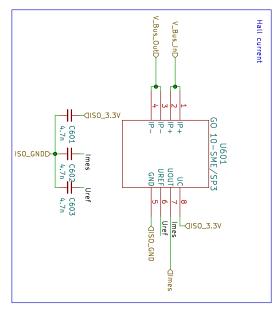
⊲5V

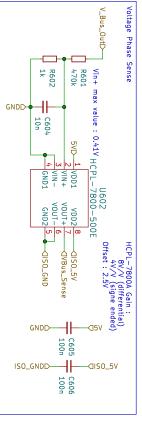
-⊲IS0_5V

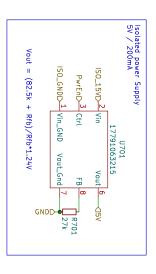


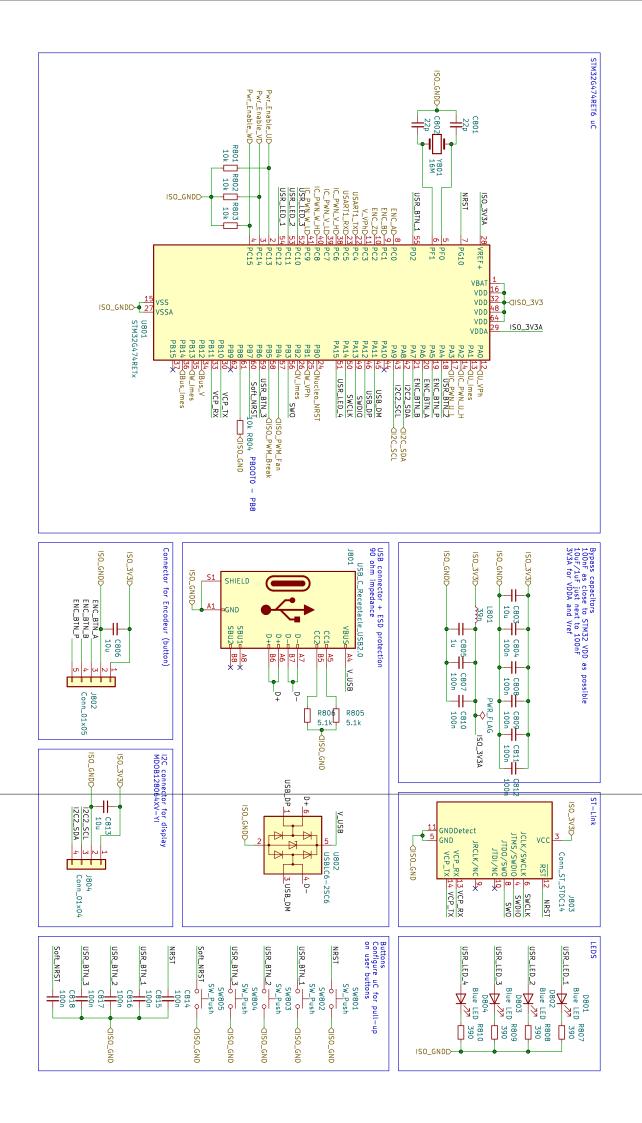


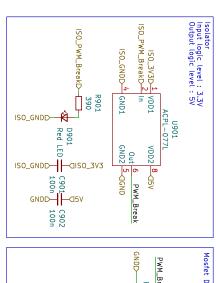


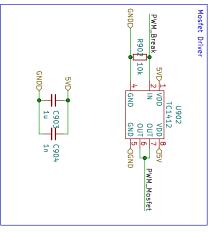


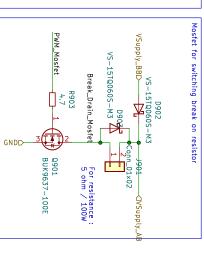


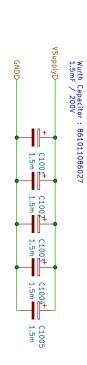


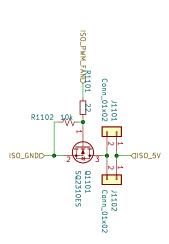


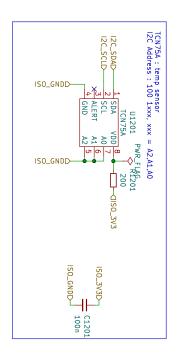


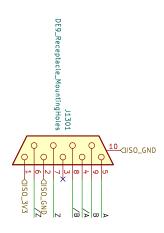


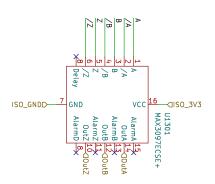


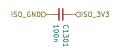












VCC_Enc

