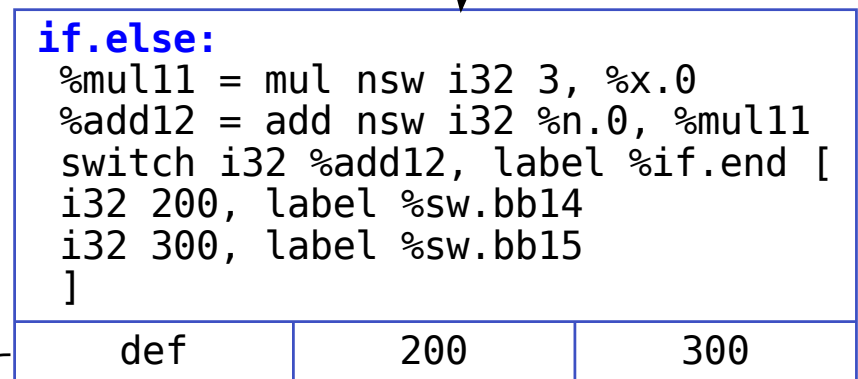
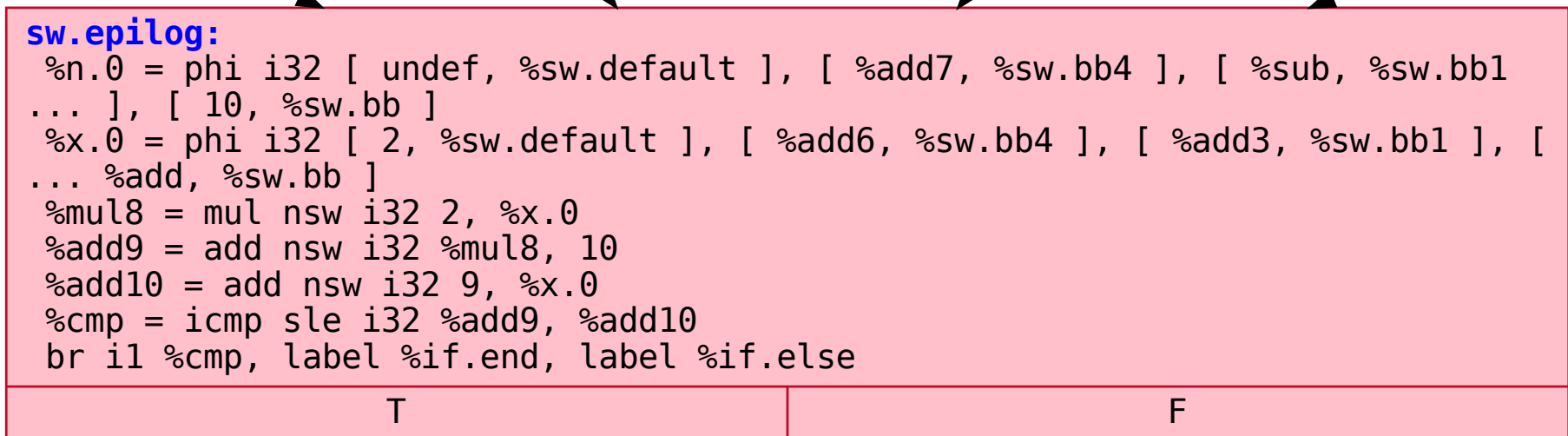


**sw.default:**  
br label %sw.epilog

**sw.bb:**  
%mul = mul nsw i32 2, 1  
%add = add nsw i32 %mul, 5  
br label %sw.epilog

**sw.bb1:**  
%mul2 = mul nsw i32 2, 1  
%add3 = add nsw i32 %mul2, 5  
%sub = sub nsw i32 %add3, 2  
br label %sw.epilog

**sw.bb4:**  
%mul5 = mul nsw i32 2, 1  
%add6 = add nsw i32 %mul5, 1  
%add7 = add nsw i32 %add6, 2  
br label %sw.epilog



**if.end:**  
%add17 = add nsw i32 %add9, %x.0  
store i32 %add17, i32\* %m, align 4  
br label %end

**sw.bb14:**  
br label %end

**sw.bb15:**  
call void @exit(i32 0) #6  
unreachable

**end:**  
ret i32 0