The Hot Path SSA Form in LLVM

Algorithms & Applications

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This presentation presents the details of building a robust and efficient implementation of the **Hot Path SSA (HPSSA)** form in the LLVM compiler infrastructure.

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References

This presentation presents the details of building a robust and efficient implementation of the **Hot Path SSA (HPSSA)** form in the LLVM compiler infrastructure.

The Hot Path SSA form is based on the following research papers.

- Subhajit Roy and Y.N. Srikant. The Hot Path SSA Form: Extending the Static Single Assignment Form for Speculative Optimizations. In CC '10: International Conference on Compiler Construction. 2010. CC 2010:304-323
- Smriti Jaiswal, Praveen Hegde and Subhajit Roy. Constructing HPSSA over SSA. In Proceedings of the 20th International Workshop on Software and Compilers for Embedded Systems. 2017. SCOPES 2017: 31-40

Presentation Outline

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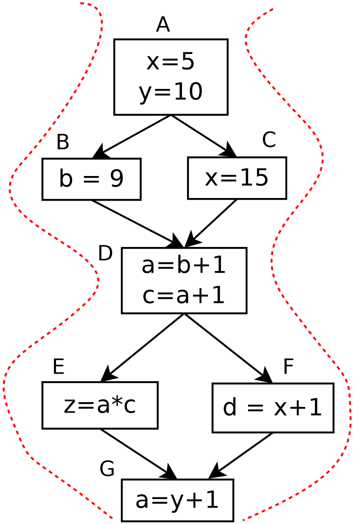
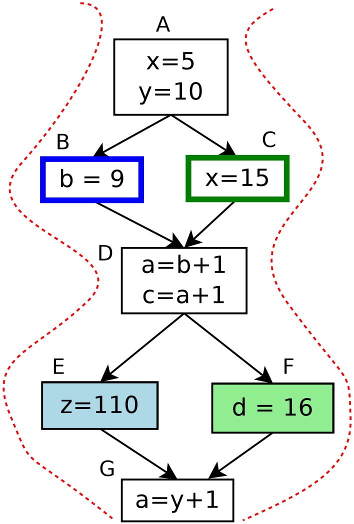
Profile-guided analysis on paths



Summary

- Profile-guided analysis across paths is stronger—can capture correlations between control-flow of basic-blocks
- Collecting path-profiles seems challenging—requires “recording” of a sequence of basic-blocks

Profile-guided analysis on paths



Summary

- Profile-guided analysis across paths is stronger—can capture correlations between control-flow of basic-blocks
- Collecting path-profiles seems challenging—requires "recording" of a sequence of basic-blocks

Profiling acyclic paths

Ball-Larus Acyclic Profiling [Ball & Larus, MICRO'96]

- Core idea: assign an identifier to each path, that can be calculated efficiently at runtime
- Record frequencies against these identifiers (instead of a sequence of node identifiers)

Profiling acyclic paths

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- Record frequencies against these identifiers (instead of a sequence of node identifiers)

Capturing still longer paths (k-iteration paths)

- Allows capturing correlations across loop iterations [Roy & Srikant, CGO'09]; a generalization of the Ball-Larus algorithm
- Subsequent work by other groups [D'Elia & Demetrescu, OOPSLA'13]; uses a prefix forest to record BL paths

Profile-guided analyses

- **Code understanding**
  - Can expose refactoring opportunities

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- **Program testing and verification**
  - Data-driven synthesis of invariants
  - Guided testing for low frequency paths

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- **Code understanding**
  - Can expose refactoring opportunities
- **Program testing and verification**
  - Data-driven synthesis of invariants
  - Guided testing for low frequency paths
- **Profile-guided optimizations**

Why is path-profile-guided analysis hard?

disparate data-structures, one for **program representation** and other for **profile information**.

Program Representation

```
int main(void) {
    int a = 90;
      ....
}
```

CFG, AST,
TAC, ASM ...

+

Profile Information

BB1→BB7→BB3→ ... : 7890
BB1→BB2→BB8→ ... : 9500

Array,
HashTable, Map, ...

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Why is path-profile-guided analysis hard?

- There has been enough interest in path-profile-guided analysis and optimizations....
- ...however, designing path-profile-guided variants of traditional optimizations remained hard
- ...hard enough to justify *publications per optimization*
    - Gupta, Benson, Fang. Path profile guided partial dead code elimination using predication. PACT '97.
    - Gupta, Benson, Fang. Path profile guided partial redundancy elimination using speculation. ICCL '98.
    - ...

Our Objective

Can we **weave** profile information into the program representation

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....into a **single, consistent** data-structure

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... that provides the convenience and elegance of an **SSA-like** intermediate form

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Can we **weave** profile information into the program representation

....into a **single, consistent** data-structure

... that provides the convenience and elegance of an **SSA-like** intermediate form

...allowing the design of profile-guided versions of “traditional” optimizations with **trivial algorithmic modification** of the base algorithms

... and PGO is easy with the Hot Path SSA (HPSSA) Form!

```
// Function to process "llvm.tau" function intrinsic.
void SpecSCCPInstVisitor::visitTauNode(Instruction &Tau) {
  // Code similar to that in visitPHINode(...).
  if (Tau.getType()->isStructTy())
      return (void)markOverdefined(&Tau);
  if (TauState.isOverdefined())
    return (void)markOverdefined(&Tau);
  // additional code.
  unsigned NumActiveIncoming = 0;
  SpecValueLatticeElement &TauState = getValueState(&Tau),
    beta = getValueState(Tau.getOperand(1)),
    x0 = getValueState(Tau.getOperand(0));

  for (unsigned i = 1, e = (Tau.getNumOperands() - 1); i != e; ++i){
    SpecValueLatticeElement IV = getValueState(Tau.getOperand(i));
    beta.mergeIn(IV);
    NumActiveIncoming++;
    if (beta.isOverdefined())
      break;
  }

  if (beta.isConstantRange()
      && beta.getConstantRange().isSingleElement())
    beta.markSpeculativeConstantRange(beta.getConstantRange());
  if (beta.isConstant())
    beta.markSpeculativeConstant(beta.getConstant());

  x0.mergeInSpec(beta, TauState) ;
  ... // futher processing similar to visitPHINode();
}
```

```
// Omit handling of "llvm.tau" intrinsic
// as a regular Instruction.
void SpecSCCPInstVisitor::solve() {
  ...
  ...
  for (auto& I : *&(*(BB))) {
    CallInst* CI = dyn_cast<CallInst>(&I);
    if (CI != NULL) {
      Function* CF = CI->getCalledFunction();
      if (CF != NULL &&
        CF->getIntrinsicID() ==
        Function::lookupIntrinsicID("llvm.tau")){
        visitTauNode(I);
      } else {
        visit(I);
      }
    } else {
      visit(I);
    }
  }
  ... // rest of the code.
}
```

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… and PGO is easy with the Hot Path SSA (HPSSA) Form!

```cpp
// Function to process "llvm.tau" function intrinsic.
void SpecSCCPInstVisitor::visitTauNode(Instruction &Tau) {
  // Code similar to that in visitPHINode(...).
  if (Tau.getType()->isStructTy())
      return (void)markOverdefined(&Tau);
  if (TauState.isOverdefined())
    return (void)markOverdefined(&Tau);
  // additional code.
  unsigned NumActiveIncoming = 0;
  SpecValueLatticeElement &TauState = getValueState(&Tau),
    beta = getValueState(Tau.getOperand(1)),
    x0 = getValueState(Tau.getOperand(0));

  for (un
    SpecV
    beta.
    NumAct
    if (beta.isOverdefined())
      break;
  }

  if (beta.isConstantRange()
      && beta.getConstantRange().isSingleElement())
    beta.markSpeculativeConstantRange(beta.getConstantRange());
  if (beta.isConstant())
    beta.markSpeculativeConstant(beta.getConstant());

  x0.mergeInSpec(beta, TauState) ;
  ... // futher processing similar to visitPHINode();
}
```

```cpp
// Omit handling of "llvm.tau" intrinsic
// as a regular Instruction.
void SpecSCCPInstVisitor::solve() {
  ...
  ...
  for (auto& I : *&(*(BB))) {
    CallInst* CI = dyn_cast<CallInst>(&I);
                                      Function();
                                      ("llvm.tau")){
          visitTauNode(I);
      } else {
        visit(I);
      }
    } else {
      visit(I);
    }
  }
  ... // rest of the code.
}
```

Only these **few lines** were enough to create a **new path profile guided analysis**, *Speculative Sparse Conditional Constant Propagation (SpecSCCP)* from the currently existing SCCP pass in LLVM !

… and PGO is easy with the Hot Path SSA (HPSSA) Form!

```
// Function to process "llvm.tau" function intrinsic.
void SpecSCCPInstVisitor::visitTauNode(Instruction &Tau) {
  // Code similar to that in visitPHINode(...).
  if (Tau.getType()->isStructTy())
      return (void)markOverdefined(&Tau);
  if (TauState.isOverdefined())
    return (void)markOverdefined(&Tau);
  // additional code.
  unsigned NumActiveIncoming = 0;
  SpecValueLatticeElement &TauState = getValueState(&Tau),
    beta = getValueState(Tau.getOperand(1)),
    x0 = getValueState(Tau.getOperand(0));

  for (unsigned i =
    SpecValueLattice
    beta.mergeIn(IV);
    NumActiveIncoming++;
    if (beta.isOverdefined())
      break;
  }

  if (beta.isConstantRange()
      && beta.getConstantRange().isSingleElement())
    beta.markSpeculativeConstantRange(beta.getConstantRange());
  if (beta.isConstant())
    beta.markSpeculativeConstant(beta.getConstant());

  x0.mergeInSpec(beta, TauState);
  ... // futher processing similar to visitPHINode();
}
```

It took us only an afternoon to transform SCCP to SpecSCCP

```
// Omit handling of "llvm.tau" intrinsic
// as a regular Instruction.
void SpecSCCPInstVisitor::solve() {
  ...
  ...
  for (auto& I : *&(*(BB))) {
    CallInst* CI = dyn_cast<CallInst>(&I);
    if (CI != NULL) {
      Function* CF = CI->getCalledFunction();
                                ID() ==
        Function::lookupIntrinsicID("llvm.tau")){
        visitTauNode(I);
      } else {
        visit(I);
      }
    } else {
      visit(I);
    }
  }
  ... // rest of the code.
}
```

Presentation Outline

1. HPSSA : Why another SSA Form?
   - Introduction to Path Profile Guided Optimizations
   - Profile Guided SpecSCCP Analysis using HPSSA Form
2. What is HPSSA form?
   - Hot Path SSA Form
   - Profile Guided SpecSCCP Pass
3. How is HPSSA Implemented?
   - Constructing HPSSA Form
   - Implementing HPSSA Form in LLVM
4. Conclusion

SCCP vs SpecSCCP

SCCP

```
int main() {
  int x = 2, m, n, y, z = 9, c = 1;
  std::cin >> m ;
  switch( m ) {
    case 2 : x = 2 * c + 5; n = 10; break;
    case 4 : x = 2 * c + 5; n = x - 2; break;
    case 6 : x = 2 * c + 1; n = x + 2; break;
    default : break;
  }
  y = 2 * x + 10;
  if ( y <= z + x ) {
    // ..
  } else {
    z = n + 3 * x ;
    switch ( z ) {
      default : break;
      case 200 : goto end;
      case 300 : exit(0); }
  }
  m = n + x ;
  end:
    z = x ;
  return 0;
}
```

SpecSCCP

```
int main() {
  int x = 2, m, n, y, z = 9, c = 1;
  std::cin >> m ;
  switch( m ) {
    case 2 : x = 2 * c + 5; n = 10; break;
    case 4 : x = 2 * c + 5; n = x - 2; break;
    case 6 : x = 2 * c + 1; n = x + 2; break;
    default : break;
  }
  y = 2 * x + 10;
  if ( y <= z + x ) {
    // ..
  } else {
    z = n + 3 * x ; // n : Speculative Constant 5
    switch ( z ) {
      default : break;
      case 200 : goto end;
      case 300 : exit(0); }
  }
  m = n + x ;  // x : Speculative Constant 7
  end:
    z = x ;
  return 0;
}
```

Legend: Overdefined Real Constants Speculative Constants

SCCP

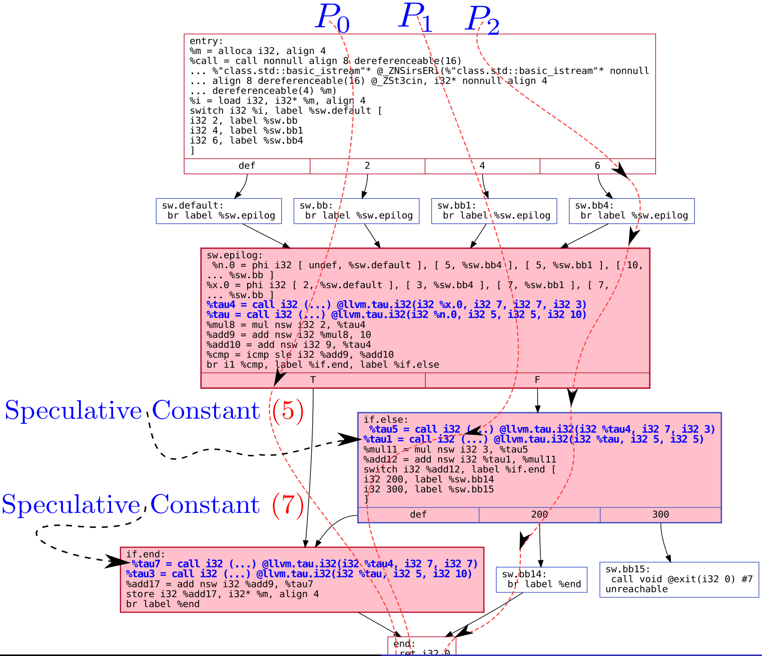
```
int main() {
  int x = 2, m, n, y, z = 9, c = 1;
  std::cin >> m ;
  switch( m ) {
    case 2 : x = 2 * c + 5; n = 10; break;
    case 4 : x = 2 * c + 5; n = x - 2; break;
    case 6 : x = 2 * c + 1; n = x + 2; break;
    default : break;
  }
  y = 2 *
  if ( y <=
    // ..
  } else {
    z = n + 3 * x ;
    switch ( z ) {
      default : break;
      case 200 : goto end;
      case 300 : exit(0); }
  }
  m = n + x ;
end:
  z = x ;
  return 0;
}
```

SpecSCCP

```
int main() {
  int x = 2, m, n, y, z = 9, c = 1;
  std::cin >> m ;
  switch( m ) {
    case 2 : x = 2 * c + 5; n = 10; break;
    case 4 : x = 2 * c + 5; n = x - 2; break;
    case 6 : x = 2 * c + 1; n = x + 2; break;
    default : break;
  }
    // ..
  } else {
    z = n + 3 * x ; // n : Speculative Constant 5
    switch ( z ) {
      default : break;
      case 200 : goto end;
      case 300 : exit(0); }
  }
  m = n + x ; // x : Speculative Constant 7
end:
  z = x ;
  return 0;
}
```

SpecSCCP discovers n & x as speculative constants.

Legend: Overdefined Real Constants Speculative Constants



P_0 P_1 P_2
entry:
%m = alloca i32, align 4
%call = call nonnull align 8 dereferenceable(16)
... %"class.std::basic_istream"* @_ZNSirsERi(%"class.std::basic_istream"* nonnull
... align 8 dereferenceable(16) @_ZSt3cin, i32* nonnull align 4
... dereferenceable(4) %m)
%i = load i32, i32* %m, align 4
switch i32 %i, label %sw.default [
i32 2, label %sw.bb
i32 4, label %sw.bb1
i32 6, label %sw.bb4
]
def
2
4
6
sw.default:
br label %sw.epilog
sw.bb:
br label %sw.epilog
sw.bb1:
br label %sw.epilog
sw.bb4:
br label %sw.epilog
sw.epilog:
%n.0 = phi i32 [undef, %sw.default], [5, %sw.bb4], [5, %sw.bb1], [10,
... %sw.bb]
%x.0 = phi i32 [2, %sw.default], [3, %sw.bb4], [7, %sw.bb1], [7,
... %sw.bb]
%tau4 = call i32 (...) @llvm.tau.i32(i32 %x.0, i32 7, i32 7, i32 3)
%tau = call i32 (...) @llvm.tau.i32(i32 %n.0, i32 5, i32 5, i32 10)
%mul8 = mul nsw i32 2, %tau4
%add9 = add nsw i32 %mul8, 10
%add10 = add nsw i32 9, %tau4
%cmp = icmp sle i32 %add9, %add10
br i1 %cmp, label %if.end, label %if.else
T
F
Speculative Constant (5)
if.else:
%tau5 = call i32 (...) @llvm.tau.i32(i32 %tau4, i32 7, i32 3)
%tau1 = call i32 (...) @llvm.tau.i32(i32 %tau, i32 5, i32 5)
%mul11 = mul nsw i32 3, %tau5
%add12 = add nsw i32 %tau1, %mul11
switch i32 %add12, label %if.end [
i32 200, label %sw.bb14
i32 300, label %sw.bb15
]
def
200
300
Speculative Constant (7)
if.end:
%tau7 = call i32 (...) @llvm.tau.i32(i32 %tau4, i32 7, i32 7)
%tau3 = call i32 (...) @llvm.tau.i32(i32 %tau, i32 5, i32 10)
%add17 = add nsw i32 %add9, %tau7
store i32 %add17, i32* %m, align 4
br label %end
sw.bb14:
br label %end
sw.bb15:
call void @exit(i32 0) #7
unreachable
end:
ret i32 0

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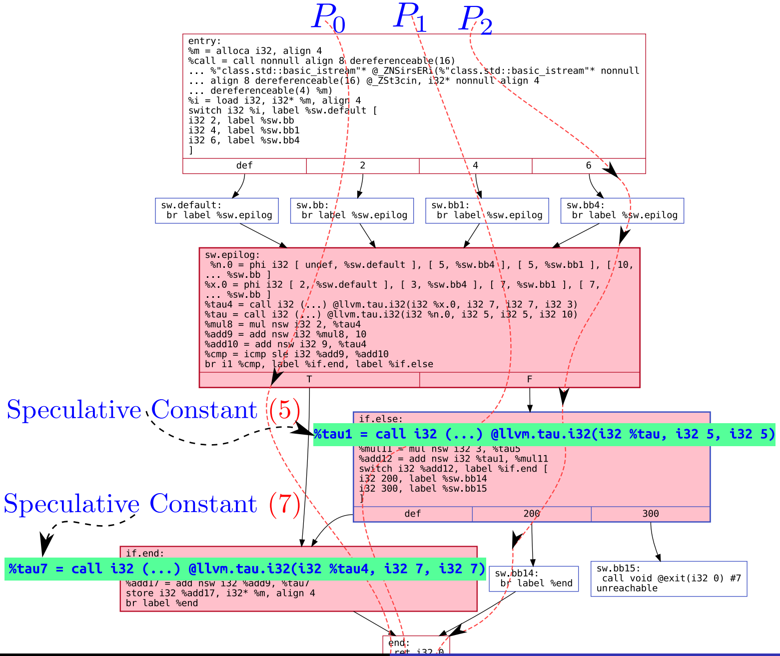
P_0 P_1 P_2
entry:
%m = alloca i32, align 4
%call = call nonnull align 8 dereferenceable(16)
... %"class.std::basic_istream"* @_ZNSirsERi(%"class.std::basic_istream"* nonnull
... align 8 dereferenceable(16) @_ZSt3cin, i32* nonnull align 4
... dereferenceable(4) %m)
%i = load i32, i32* %m, align 4
switch i32 %i, label %sw.default [
i32 2, label %sw.bb
i32 4, label %sw.bb1
i32 6, label %sw.bb4
]
def
2
4
6
sw.default:
br label %sw.epilog
sw.bb:
br label %sw.epilog
sw.bb1:
br label %sw.epilog
sw.bb4:
br label %sw.epilog
sw.epilog:
%n.0 = phi i32 [undef, %sw.default], [5, %sw.bb4], [5, %sw.bb1], [10,
... %sw.bb]
%x.0 = phi i32 [2, %sw.default], [3, %sw.bb4], [7, %sw.bb1], [7,
... %sw.bb]
%tau4 = call i32 (...) @llvm.tau.i32(i32 %x.0, i32 7, i32 7, i32 3)
%tau = call i32 (...) @llvm.tau.i32(i32 %n.0, i32 5, i32 5, i32 10)
%mul8 = mul nsw i32 2, %tau4
%add9 = add nsw i32 %mul8, 10
%add10 = add nsw i32 9, %tau4
%cmp = icmp sle i32 %add9, %add10
br i1 %cmp, label %if.end, label %if.else
T
F
Speculative Constant (5)
if.else:
%tau1 = call i32 (...) @llvm.tau.i32(i32 %tau, i32 5, i32 5)
%add12 = add nsw i32 %tau1, %mul11
switch i32 %add12, label %if.end [
i32 200, label %sw.bb14
i32 300, label %sw.bb15
]
def
200
300
Speculative Constant (7)
if.end:
%tau7 = call i32 (...) @llvm.tau.i32(i32 %tau4, i32 7, i32 7)
store i32 %add17, i32* %m, align 4
br label %end
sw.bb14:
br label %end
sw.bb15:
call void @exit(i32 0) #7
unreachable
end:

Standard SCCP VS. Speculative SCCP Pass.

```
# Running Regular SCCP Pass on Program.
$ opt -sccp -time-passes -debug-only=sccp \
  IR/LL/test.ll -S -o \
  IR/LL/test_sccp_onbaseline.ll \
  -f 2> output/custom_sccp_onbaseline.log

...
Output:
  ...
  Constant: i32 2 =   %mul = mul nsw i32 2, 1
  Constant: i32 7 =   %add = add nsw i32 2, 5
  Constant: i32 2 =   %mul2 = mul nsw i32 2, 1
  Constant: i32 7 =   %add3 = add nsw i32 2, 5
  Constant: i32 5 =   %sub = sub nsw i32 7, 2
  Constant: i32 2 =   %mul5 = mul nsw i32 2, 1
  Constant: i32 3 =   %add6 = add nsw i32 2, 1
  Constant: i32 5 =   %add7 = add nsw i32 3, 2
```

```
# Running HPSSA Transformation followed by Speculative SCCP Pass.
$ opt -load build/SCCPSolverTau.cpp.so
  -load build/HPSSA.cpp.so \
  -load-pass-plugin=build/SpecSCCP.cpp.so \
  -passes="specsccp" \
  -time-passes -debug-only=specsccp \
  IR/LL/test.ll -S -o IR/LL/test_spec_sccp.ll \
  -f 2> output/custom_speculative_sccp.log

...
Output :
  Constant: i32 2 =   %mul = mul nsw i32 2, 1
  Constant: i32 7 =   %add = add nsw i32 2, 5
  Constant: i32 2 =   %mul2 = mul nsw i32 2, 1
  Constant: i32 7 =   %add3 = add nsw i32 2, 5
  Constant: i32 5 =   %sub = sub nsw i32 7, 2
  Constant: i32 2 =   %mul5 = mul nsw i32 2, 1
  Constant: i32 3 =   %add6 = add nsw i32 2, 1
  Constant: i32 5 =   %add7 = add nsw i32 3, 2
Speculative Constant: i32 5 = %tau1 = call i32 (...)
    @llvm.tau.i32(i32 %tau, i32 5, i32 5)
Speculative Constant: i32 7 = %tau7 = call i32 (...)
    @llvm.tau.i32(i32 %tau4, i32 7, i32 7)
```

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From SpecSCCP Pass

Basic blocks from the transformed IR after the SpecSCCP pass with assignSpecValue() calls added.

```
...
if.else:                                          ; preds = %sw.epilog
  %tau = call i32 (...) @llvm.tau.i32(i32 %tau8, i32 7, i32 3)
  %tau10 = call i32 (...) @llvm.tau.i32(i32 %tau9, i32 5, i32 5)
  %tau10_spec = call i32 @assignSpecValue(i32 5)
  %mul11 = mul nsw i32 3, undef
  %add12 = add nsw i32 %tau10_spec, %mul11
  switch i32 %add12, label %sw.default13 [
    i32 200, label %sw.bb14
    i32 300, label %sw.bb15
  ]
...
...
if.end:                                           ; preds = %sw.epilog, %if.else
  %tau11 = call i32 (...) @llvm.tau.i32(i32 %tau8, i32 7, i32 7)
  %tau11_spec = call i32 @assignSpecValue(i32 7)
  %tau12 = call i32 (...) @llvm.tau.i32(i32 %tau9, i32 5, i32 10)
  %add17 = add nsw i32 undef, %tau11_spec
  store i32 %add17, i32* %m, align 4
  br label %end
```

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Using the HPSSA Form for writing new analyses

- Include the header file HPSSA.h to use llvm::HPSSAPass class.
- Load shared object using opt tool. opt -load HPSSA.cpp.so ...

```
#include <HPSSA.h> // import the header.
#include <SCCP.h>

class YourPGOPass : public PassInfoMixin<YourPGOPass> {
  public: PreservedAnalyses run(Function &F,
  FunctionAnalysisManager &AM);
};

PreservedAnalyses YourPGOPass::run(Function &F,
  FunctionAnalysisManager &AM) {
    ...
    HPSSAPass hpssaUtil; // Make a HPSSAPass Object.
    hpssaUtil.run(F, AM);  // Call the HPSSAPass::run() function.
    ...
    // Rest of the code ..
  }
```

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Presentation Outline

Presentation Outline

The Hot Path SSA Form (HPSSA)

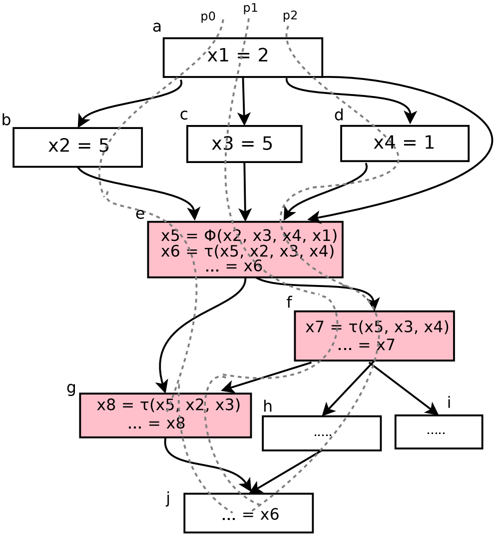
Semantics of a ϕ-function

$$y = \phi(x_1, x_2, \ldots, x_n)$$

Semantics of a τ-function

$$\tau(x_0, x_1, x_2, \ldots, x_n) = \begin{cases} x_0 & \text{safe interp.} \\ \phi(x_1, x_2, \ldots, x_n) & \text{speculative interp.} \end{cases}$$

The Hot Path SSA Form



No frequent path carrying:

- def $x_2 = 3$ to use at block **f**
- def $x_4 = 1$ to use at block **g**

The Hot Path SSA Form

Properties

If a program is in the Hot Path SSA form, then,

- each use of a variable is reachable by a single definition; [SSA-like form]

Properties

If a program is in the Hot Path SSA form, then,

- each use of a variable is reachable by a single definition; [SSA-like form]
- **safe interpretation**: [supports traditional analysis]
  - each use of a variable is reachable by the *meet-over-all-paths* reaching definition chains;

The Hot Path SSA Form

Properties

If a program is in the Hot Path SSA form, then,

- each use of a variable is reachable by a single definition; [SSA-like form]
- **safe interpretation**: [supports traditional analysis]
  - each use of a variable is reachable by the *meet-over-all-paths* reaching definition chains;
- **speculative interpretation**: [supports profile-guided analysis]
  - each use of a variable in a basic-block is reachable by the *meet-over-frequent-paths* reaching definitions. [a]

---

[a] or the meet-over-all-paths reaching definition chains, if the use is not reachable from any meet-over-hot-paths reaching definition chain

Presentation Outline

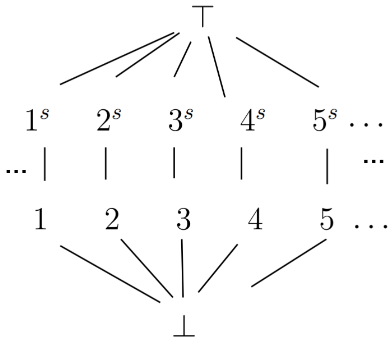
Speculative Sparse Conditional Constant Propagation (SpecSCCP)

- Introduce new speculative values $\{\ldots, 1^s, 2^s, \ldots\} \in C^S$
- Operation with *speculative* values result in *speculative* results (with same semantics as base operator)

$$\alpha^s \ \langle op \rangle \ \beta = (\alpha \ \langle op \rangle \ \beta)^s$$

- Transfer function for τ-functions
  ($\beta = x_1 \sqcup x_2 \sqcup \cdots \sqcup x_n$, i.e. join of speculative args.)

$$\tau(x_0, x_1, \ldots, x_n) \sqcup \begin{cases} \top & \text{if } \beta = \top \\ \beta & \text{if } \beta \neq \top \wedge x_0 \sqsubseteq \beta \\ \beta^s & \text{otherwise} \end{cases}$$



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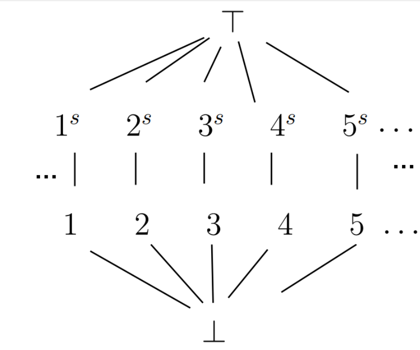

Almost trivial to generate profile-guided variants of standard analyses—an afternoon to "port" SCCP to SpecSCCP!

Presentation Outline

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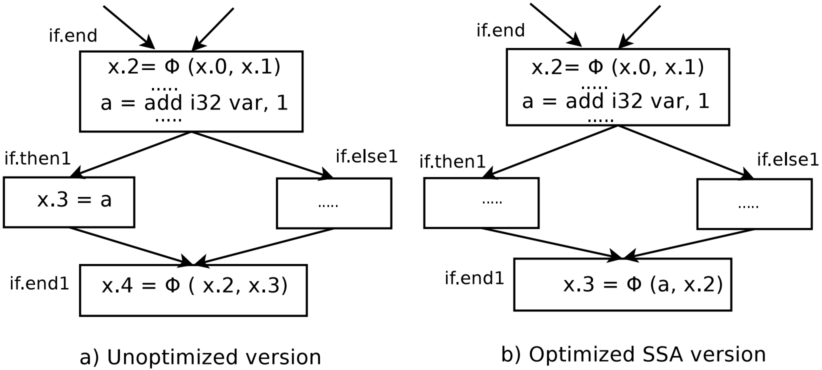
Brief Algorithm

- **Insert τ-functions**
  - Insert at Thermal Frontiers
- **Allocate arguments to τ-functions**
  - path-sensitive traversal through the program to identify definitions that reach τ-functions through hot paths
  - constrains its inspection to only the ϕ-functions and the τ-functions

Optimized SSA forms



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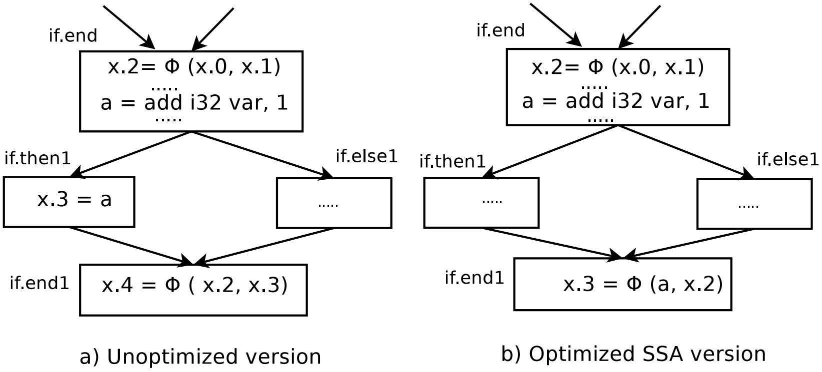

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in the above example, copy propagation breaks the *phi congruence property*...

Shreedhar et al. [SAS'99]

"The occurrences of all resources which belong to the same phi congruence class in a program can be replaced by a representative resource. After the replacement, the phi instruction can be eliminated without violating the semantics of the original program."

- Sreedhar et al. circumvent the problem by translating the optimized SSA form to the conventional SSA form (that satisfies the phi congruence property) before translating out of SSA.
- **We directly build the HPSSA form over the optimized SSA form!**

Presentation Outline

What we modified in LLVM Source?

- New `llvm::intrinsic` signature, `"llvm.tau"` to support addition and removal of τ-functions to the LLVM SSA IR representation.

```
+ //===----------- intrinsic for tau ----------------=====//
+ def int_tau : DefaultAttrsIntrinsic<[llvm_any_ty],
+                                     [llvm_vararg_ty],
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- Modified Verifier::verifyDominatesUse() function since we don't want our intrinsic to interfere with dominators computation.

```
+ //===----------- Changes for tau.intrinsic ------------------=====//
void Verifier::verifyDominatesUse(Instruction &I, unsigned i) {
    Instruction *Op = cast<Instruction>(I.getOperand(i));
    +    if (CallInst *CI = dyn_cast<CallInst>(&I)) {
    +    Function *CallFunction = CI->getCalledFunction();
    +    if (CallFunction != NULL && CallFunction->getIntrinsicID()==
    +        Function::lookupIntrinsicID("llvm.tau")) {
    +            return;
    +        }
    +    }
    ...
```

HPSSAPass : Overview

- `class HPSSAPass : public PassInfoMixin<HPSSAPass>`
  - Implemented `llvm::HPSSAPass` pass using the new LLVM Pass Manager.
  - Function `HPSSAPass::run(Function &F, ...)` runs over a `llvm::Function` and inserts `"llvm.tau"` intrinsic calls with speculative and safe argument at strategic positions in the LLVM IR and handles argument allocation for `"llvm.tau"` intrinsic calls.

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  - `HPSSAPass::AllocateArgs(BasicBlock* BB, DomTreeNode& DTN)` handles argument allocation for τ-functions inserted.

HPSSAPass : Destruction Pass

- Out of HPSSA Form.
  - A seperate pass using the new LLVM Pass Manager.
    ```
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      `class TDSTRPass : public PassInfoMixin<TDSTRPass>`
    - Using `TDSTRPass::run(Function &F, ...)`, we replace all use of existing tau operands with first argument of `"llvm.tau"` intrinsic (corresponds to the safe argument) and remove the `"llvm.tau"` intrinsic calll from the LLVM IR.
    - The LLVM IR becomes identical to what it was before running the HPSSA Pass.

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$\boxed{x_3 = \tau(x_0, x_1, x_2),\ \tau\text{-function}}$ $\boxed{x_3 = x_0, \quad \text{Replace all use of } x_3 \text{ with } x_0.}$

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Conclusion

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- We plan to open source our work soon and push it to the LLVM “main” branch.

LLVM Implementation : Profile Guided SpecSCCP Pass

- Modified the existing SCCP Pass to add `visitTauNode()` function which handles the special `"llvm.tau"` intrinsic instructions used for τ-functions.[1]

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- Modified the `SCCPInstVisitor::solve()` function to process `"llvm.tau"` intrinsic instructions using `visitTauNode()` instead of the standard `visit()` function.

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- `HPSSAPass::run(Function &F, FunctionAnalysisManager &AM)`
  - Invokes `HPSSAPass::getProfileInfo()` function to get a compact representation of all the profiled hot paths in the program and then calls `HPSSAPass::getCaloricConnector()` to get all the caloric connectors from the hot path information. This is a precursor to finding strategic positions to place `"llvm.tau"` intrinsic calls in the LLVM IR.

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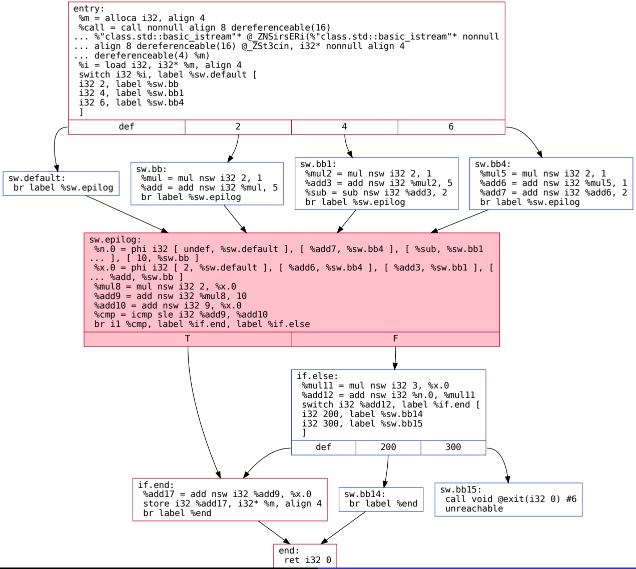
  - Uses the renaming stack and `HPSSAPass::Search()` function to search and replace all use of PHI result operand with that returned by the `"llvm.tau"` intrinsic call.

Program in SSA Form



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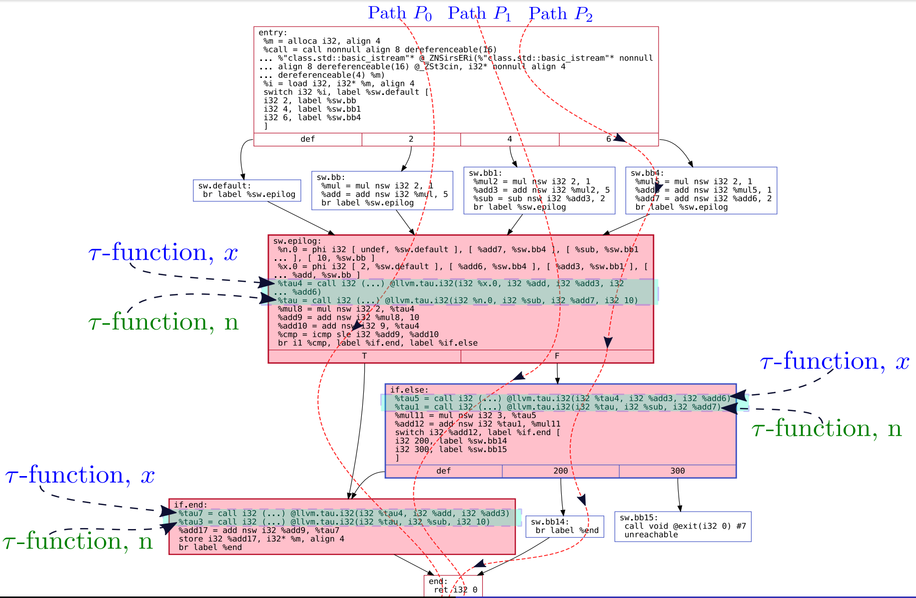
Program in Hot Path SSA Form



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