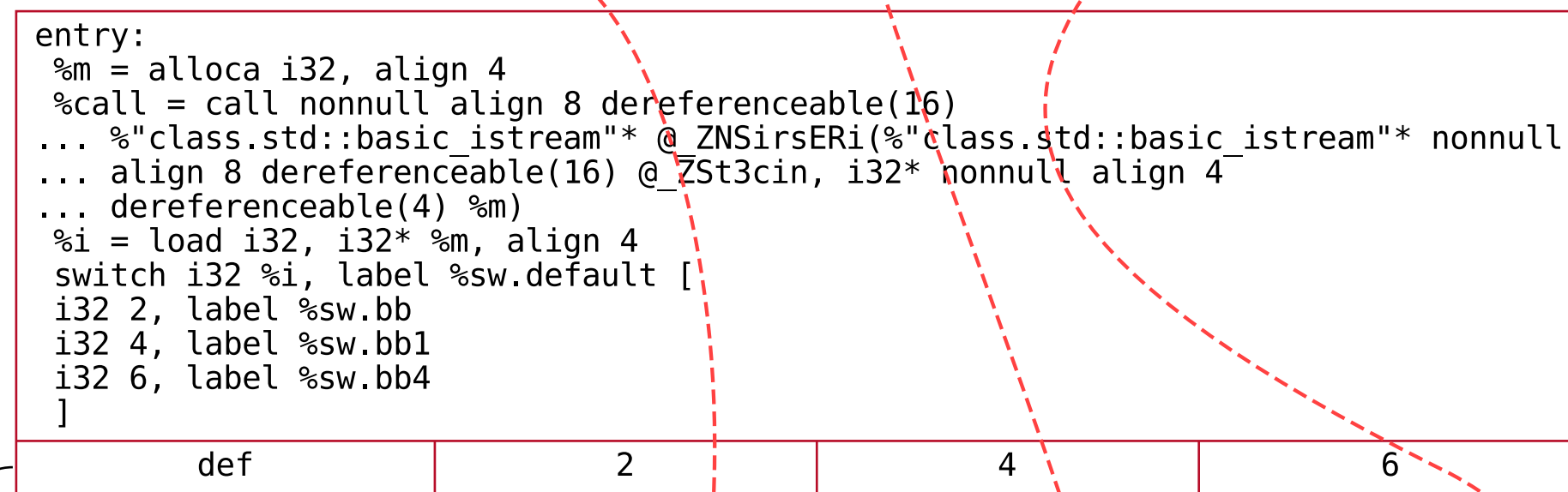


Path P_0 Path P_1 Path P_2

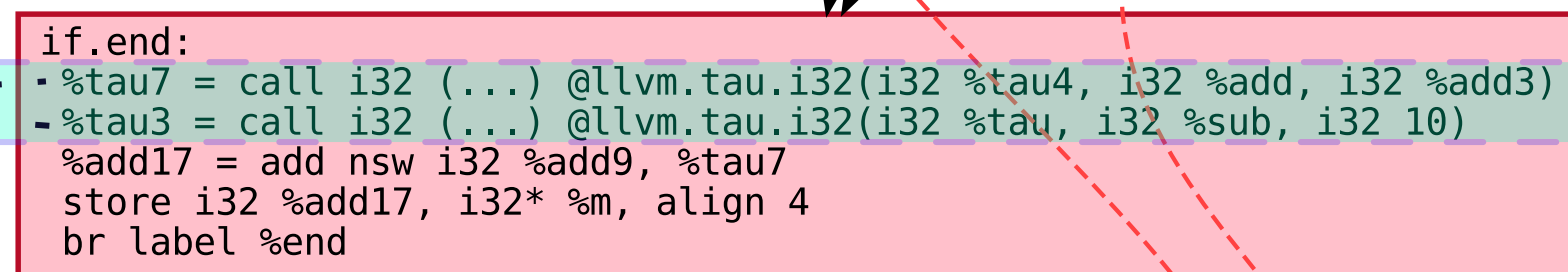
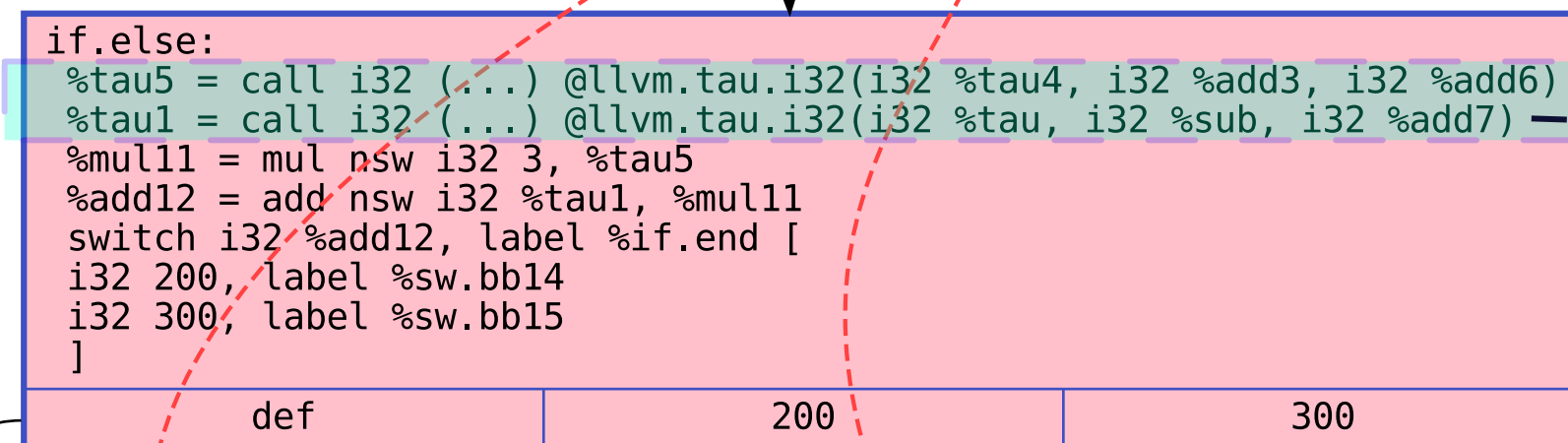
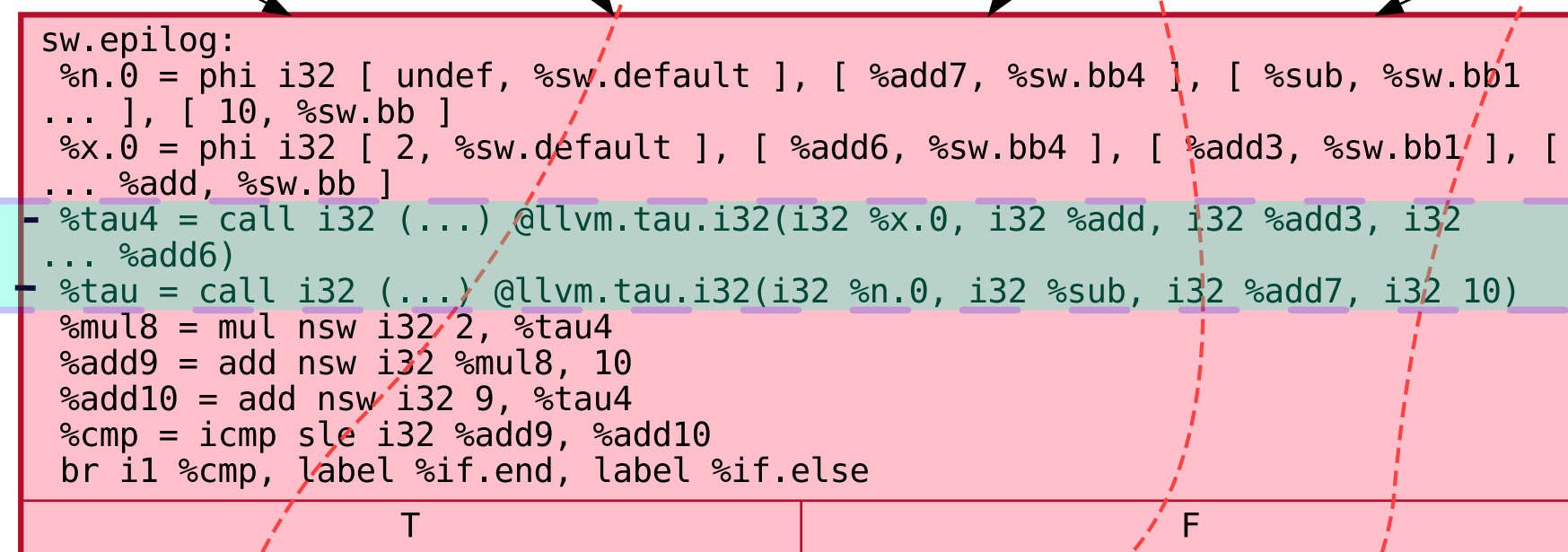


```
sw.default:
  br label %sw.epilog
```

```
sw.bb:
  %mul = mul nsw i32 2, 1
  %add = add nsw i32 %mul, 5
  br label %sw.epilog
```

```
sw.bb1:
  %mul2 = mul nsw i32 2, 1
  %add3 = add nsw i32 %mul2, 5
  %sub = sub nsw i32 %add3, 2
  br label %sw.epilog
```

```
sw.bb4:
  %mul5 = mul nsw i32 2, 1
  %add6 = add nsw i32 %mul5, 1
  %add7 = add nsw i32 %add6, 2
  br label %sw.epilog
```



```
sw.bb14:
  br label %end
```

```
sw.bb15:
  call void @exit(i32 0) #7
  unreachable
```

```
end:
  ret i32 0
```

Program in HPSSA Form

τ -function, x

τ -function, n

τ -function, x

τ -function, n

τ -function, x

τ -function, n