### The Hot Path SSA Form in LLVM

Algorithms & Applications

IIT Kanpur
Dept. Of Computer Science & Engineering

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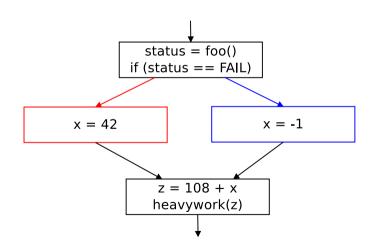
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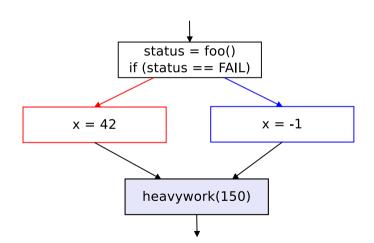
### Presentation Outline: Section 1

- HPSSA: Why another SSA Form?
  - Profile Guided Optimization
  - Doing Profile Guided Optimizations is Hard!
  - Profile Guided SSCCP Analysis using HPSSA Form
- 2 What is HPSSA form?
  - Hot Path SSA Form
  - Profile Guided SSCCP Pass
- 3 How is HPSSA Implemented?
  - Constructing HPSSA Form
  - Implementing HPSSA Form in LLVM
- 4 Conclusion

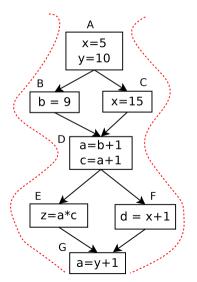
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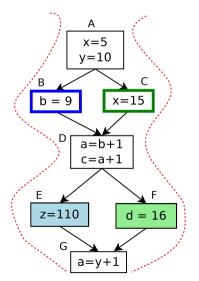
## Profile-guided analysis on paths



### Summary

- Profile-guided analysis across paths is stronger—can capture correlations between control-flow of basic-blocks
- Collecting path-profiles seems challenging—requires "recording" of a sequence of basic-blocks

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## Profiling acyclic paths

Introduction

### Ball-Larus Acyclic Profiling [Ball & Larus, MICRO'96]

- Core idea: assign an identifier to each path, that can be calculated efficiently at runtime
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### Capturing still longer paths (k-iteration paths)

- Allows capturing correlations across loop iterations [Roy & Srikant, CGO'09]; a generalization of the Ball-Larus algorithm
- Subsequent work by other groups [D'Elia & Demetrescu, OOPSLA'13]; uses a prefix forest to record BL paths

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  - Can impact code size (significant impact w/o speculation support in hardware)

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- Error resilient modules
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  - No impact on code size
- Optimizations that don't impact correctness
  - eg. register allocation
  - No impact on code size



## Why is path-profile-guided analysis hard?

- There has been enough interest in path-profile-guided analysis and optimizations....
- ...however, designing path-profile-guided variants of traditional optimizations remained hard
- ...hard enough to justify publications per optimization
  - Gupta, Benson, Fang. Path profile guided partial dead code elimination using predication. PACT '97.
  - Gupta, Benson, Fang. Path profile guided partial redundancy elimination using speculation. ICCL '98.
  - ...



## Profile Guided Optimization is easy with HPSSA Form

```
// Function to process "llvm.tau" function intrinsic.
   visitTauNode() {
     SpecValueLatticeElement TauState = getValueState(&Tau).
       beta = getValueState(Tau.getOperand(1)),
       x0 = getValueState(Tau.getOperand(0));
     if (TauState isOverdefined())
       return (void)markOverdefined(&Tau):
9
     for (unsigned i = 1, e = (Tau.getNumOperands() - 1); i != e; ++i){
10
       SpecValueLatticeElement IV = getValueState(Tau.getOperand(i)):
11
       beta.mergeIn(IV):
12
       NumActiveIncoming++;
13
       if (beta.isOverdefined())
14
       break:
15
16
17
     beta.mergeIn(x0):
18
     if (beta isConstant())
19
       TauState.markSpeculativeConstant(beta.getConstant());
20
21
     if (beta.isConstantRange())
       TauState.markSpeculativeConstantRange(beta.getConstantRange()):
23
24
     if (beta.isOverdefined() x0.isOverdefined())
25
       TauState.markOverdefined():
26
27
```

```
1 // Omit handling of "llvm.tau" intrinsic
 2 // as a regular Instruction.
 3 for (auto& I : *&(*(BB))) {
     CallInst* CI = dyn_cast<CallInst>(&I);
     if (CI != NULL) {
       Function* CF = CI->getCalledFunction():
       if (CF != NULL &&
       CF->getIntrinsicID() ==
         Function::lookupIntrinsicID("llvm.tau")){
         visitTauNode(I):
11
       } else {
         visit(I):
14
       else {
15
       visit(I):
16
17 }
```

### Using HPSSAPass [It is easy!]

- Include the header file HPSSA.h to use llvm::HPSSAPass class.
- Load shared object using opt tool. opt -load HPSSA.cpp.so ...

```
#include <HPSSA.h> // import the header.
     #include <SCCP.h>
     class SpecSCCPPass : public PassInfoMixin<SpecSCCPPass> {
5
       public: PreservedAnalyses run(Function &F.
       FunctionAnalysisManager &AM):
     }:
9
10
     PreservedAnalyses SpecSCCPPass::run(Function &F.
11
     FunctionAnalysisManager &AM) {
12
       if (F.getName() != "main")
13
         return PreservedAnalyses::all():
14
15
       HPSSAPass hpssaUtil: // Make a HPSSAPass Object.
16
       hpssaUtil.run(F, AM); // Call the HPSSAPass::run() function.
17
18
       std::vector<Instruction *> TauInsts
19
       = hpssaUtil.getAllTauInstrunctions(F): // Calling HPSSA utility function.
20
21
       std::cout << "\t\tTotal Tau Instructions : " << TauInsts.size() << "\n":
22
       . . .
23
24
25
     /// [output] Total Tau Instructions : 7
```

### Speculative Pass using HPSSA: SSCCP

Legends ■ Overdefined ■ Analysis proved Constants ■ Real Constants ■ Speculative Constants

```
1 int main() {
     int x = 2, m, y, z = 9, c = 1;
     std::cin >> m;
     switch( m ) {
    case 2 : x = 2 * c + 5: break:
    case 4 : x = 2 * c + 5; break;
      case 6 : x = 2 * c + 1; break;
      default : break:
10
     y = 2 * x + 10;
     if (y \le z + x) {
12
13
    } else {
14
      switch (z) {
15
16
      default : break:
    case 200 : std::cout << x ; goto end;
18
      case 300 : exit(0):
19
20
     end:
       z = c + x;
     return 0:
25 }
```

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      case 6 : x = 2 * c + 1; break;
      default : break:
10
12
13
     } else {
      switch (z) {
        default : break:
   case 200 : std::cout << x : goto end:
18
        case 300 : exit(0):
19
     m = y + x; // speculative constant : x
     end:
       z = c + x;
     return 0:
25 }
                      イロト イ御ト イヨト イヨト
```

### Speculative Pass using HPSSA: SSCCP

#### Standard SCCP VS. Speculative SCCP Pass.

```
# Running Regular SCCP Pass on Program.
      $ opt -sccp -time-passes -debug-only-sccp \
        TR/LL/test 11 -S -o \
        IR/LL/test_sccp_onbaseline.ll \
        -f 2> output/custom sccp onbaseline.log
10
        Constant: i32 2 =
                            %mul = mul nsw i32 2, 1
11
        Constant: i32 7 =
                            %add = add nsw i32 2. 5
12
        Constant: i32 2 =
                            mu12 = mu1 \text{ nsw } i32 2. 1
13
        Constant: i32 7 =
                            %add3 = add nsw i32 2. 5
14
        Constant: i32 2 =
                            %mul5 = mul nsw i32 2. 1
15
        Constant: i32 3 =
                            %add6 = add nsw i32 2. 1
```

```
# Running HPSSA Transformation followed by Speculative SCCP Pass.
     $ opt -load build/SCCPSolverTau.cpp.so
        -load build/HPSSA.cpp.so \
        -load-pass-plugin=build/SpecSCCP.cpp.so \
        -passes="specsccp" \
        -time-passes -debug-only=specsccp \
        IR/LL/test.11 -S -o IR/LL/test_spec_sccp.11 \
        -f 2> output/custom_speculative_sccp.log
10
11
     Output :
13
        ValueLattice (TauState) tau3 : speculative constantrange<3. 4>
14
15
        Constant: i32 2 =
                            %mul = mul nsw i32 2. 1
16
        Constant: i32 7 =
                           %add = add nsw i32 2. 5
        Constant: i32 2 =
                           %mul2 = mul nsw i32 2. 1
18
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                           %add3 = add nsw i32 2. 5
19
        Constant: i32 2 =
                           %mul5 = mul nsw i32 2. 1
20
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                           %add6 = add nsw i32 2. 1
```

### Presentation Outline: Section 2

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# The Hot Path SSA Form (HPSSA)

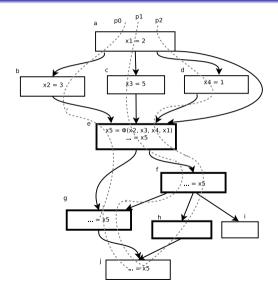
#### Semantics of a $\phi$ -function

$$y=\phi(x_1,x_2,\ldots,x_n)$$

#### Semantics of a au-function

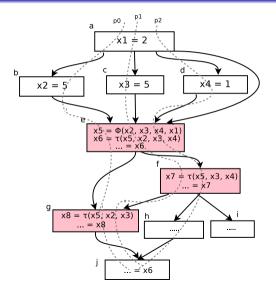
$$au(x_0, x_1, x_2, \dots, x_n) = \left\{ egin{array}{ll} x_0 & ext{safe interp.} \\ \phi(x_1, x_2, \dots, x_n) & ext{speculative interp.} \end{array} 
ight.$$

### The SSA form



#### No frequent path carrying:

- def  $x_2 = 3$  to use at block **f**
- def  $x_4 = 1$  to use at block **g**
- def  $x_1 = 2$  to either **f** or **g**



### No frequent path carrying:

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#### **Properties**

If a program is in the Hot Path SSA form, then,

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  - each use of a variable is reachable by the meet-over-all-paths reaching definition chains;

#### **Properties**

If a program is in the Hot Path SSA form, then,

- each use of a variable is reachable by a single definition; [SSA-like form]
- safe interpretation: [supports traditional analysis]
  - each use of a variable is reachable by the *meet-over-all-paths* reaching definition chains;
- speculative interpretation: [supports profile-guided analysis]
  - each use of a variable in a basic-block is reachable by the meet-over-frequent-paths
    reaching definitions. a

<sup>&</sup>lt;sup>a</sup>or the meet-over-all-paths reaching definition chains, if the use is not reachable from any meet-over-hot-paths reaching definition chain

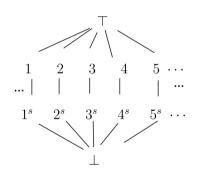
# Speculative Sparse Conditional Constant Propagation (SSCCP)

- Introduce new speculative values  $\{\ldots,1^s,2^s,\ldots\}\in C^S$
- Operation with speculative values result in speculative results (with same semantics as base operator)

$$\alpha^{s} \langle op \rangle \beta = (\alpha \langle op \rangle \beta)^{s}$$

• Transfer function for au-functions ( $eta=x_1\sqcup x_2\sqcup\dots$ )

$$\tau(x_0, x_1, \dots, x_n) \sqcup \begin{cases} x_0 \sqcup \top & \text{if } x_0 \sqcup \beta \neq \top \\ \beta & \text{if } x_0 \sqcup \beta = \top \land \beta \in C^s \\ \beta^s & \text{otherwise} \end{cases}$$



# Speculative Sparse Conditional Constant Propagation (SSCCP)

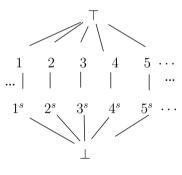
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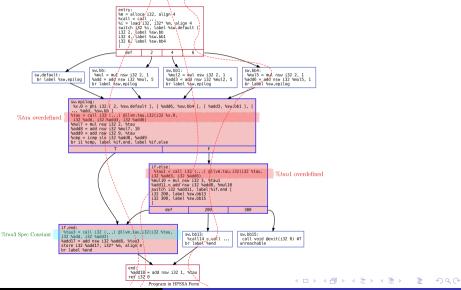
• Transfer function for au-functions ( $eta=x_1\sqcup x_2\sqcup\dots$ )

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 $eta^s$  otherwise Almost trivial to generate profile-guided variants of standard analyses—it took us an afternoon to "port" SCCP to SSCCP!



### Profile Guided SSCCP Pass in LLVM



Path Pr. Path Pr /Path Pa

### LLVM Implementation: Profile Guided SSCCP Pass

- Modified the existing SCCP Pass to add visitTauNode() function which handles the special "llvm.tau" intrinsic instructions used for τ-functions.<sup>1</sup>
- Added a new lattice element type "spec\_constant" in ValueLattice class supporting
  operations on speculative constants. Modified the SCCPInstVisitor::mergeIn() function to
  handle lattice "meet" operation for the new speculative constants introduced.
- Added new functions in the SCCPInstVisitor and SCCPSolver class to handle operations on speculative constants. Eg. Operands can be marked speculative using as function markSpeculativeConstant().
- Modified the SCCPInstVisitor::solve(). function to process "llvm.tau" intrinsic instructions
  using visitTauNode() instead of the standard visit() function.

<sup>&</sup>lt;sup>1</sup>Since we added the  $\tau$ -functions as an "llvm.tau" intrinsic, we blocked process it as a regular Instruction.

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## Hot/Cold Paths

#### Definition 1. Hot/Cold Paths

A program path  $p: n_1 \rightsquigarrow n_2$  is said to be hot (cold) if the sequence of edges from node  $n_1$  to  $n_2$  appears (does not appear) in any profiled path that occurs frequently in the program profile.

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#### Definition 3. Hot/Cold Reaching Definitions and Definition Chains

A definition  $\delta$  at a basic-block  $n_1$  is said to reach a respective use at a basic-block  $n_2$  hot if there exists a hot path from  $n_1$  to  $n_2$ , and  $\delta$  is not killed along that path. A definition  $\delta$  at a basic-block  $n_1$  is said to reach a respective use at a basic-block  $n_2$  cold if there does not exist a hot path from  $n_1$  to  $n_2$ , and  $\delta$  is not killed at least along one cold path from  $n_1$  to  $n_2$ .

### Inserting $\tau$ – functions

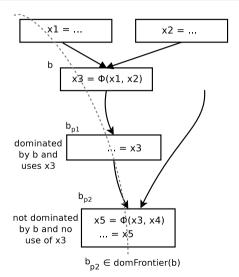
#### Necessary condition for $\tau$ -functions

Lemma 1. A node n requires a  $\tau$ -function for variable x due to a definition  $d^x$  (of a variable x) if

- n is the junction of a hot and a cold path, i.e., paths at different temperatures meet at this node;
- on is reachable by at least two different definitions of the variable x.

Proof. If condition I fails, a  $\tau$ -function is unnecessary as n can then be reachable by only hot or only cold definitions of x. If condition II fails, a  $\tau$ -function is again unnecessary as the node is then dominated by a definition of x.

## Inserting $\tau$ -functions



### Inserting $\tau$ – functions

#### Definition 4. Thermal Frontier (TF)

For definition d defined at a node u reaching v, v is in the Thermal Frontier of (u,d),  $v \in TF(u,d)$ , iff

- the node v is also exposed to a reaching definition d' defined at a node  $u \notin Dom(w)$  (w not dominated by u)
- $\theta(u \leadsto v) \neq \theta(w \leadsto v)$
- $\bullet$  v is the first node on the paths  $u \rightsquigarrow v$  and  $w \rightsquigarrow v$  that satisfies the above properties.

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#### **Theorem**

For a set of visible definitions of a variable x at a set of nodes  $\kappa$ ,  $\tau$ -statements are only required at the Iterated Thermal Frontier  $ITF^{x}$  for variable x.

## HPSSA construction [Roy et al., CC'10]

- Insert  $\phi$ -functions:
  - insert  $\phi$ -functions at the *iterated dominance frontiers*
- Insert  $\tau$ -functions
  - insert  $\tau$ -functions at the *iterated thermal frontiers*
- Allocate arguments to  $\phi$ -functions
  - ullet use a variable stack to allocate the  $\phi$ -function arguments
- Allocate arguments to  $\tau$ -functions
  - maintain path-sensitive reaching definitions for each program variable corresponding to each hot path on a path-sensitive stack;
  - for each instruction in the program, the algorithm update the respective stack to record the change in the path-sensitive reaching definitions due to the instruction;
  - when a  $\tau$ -function is encountered, the current set of reaching definitions on the stack is used to allocate the speculative arguments for the  $\tau$ -function.



# HPSSA construction over SSA [Jaiswal et. al., SCOPES'17]

#### Difficulties,

- Needs the SSA construction identified, broken into and retrofitted with the HPSSA construction phases.
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#### HPSSA over SSA

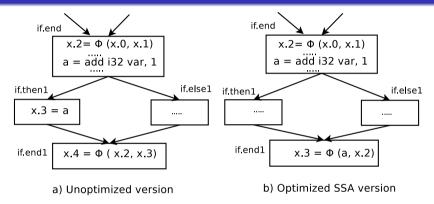
- Easily incorporated within existing compilers: Construction over the SSA form
- Efficient: Lesser instructions have to be traversed
- Simpler: many constructs are eliminated

Conclusions

### A naïve attempt: Mimic [Roy et al.]

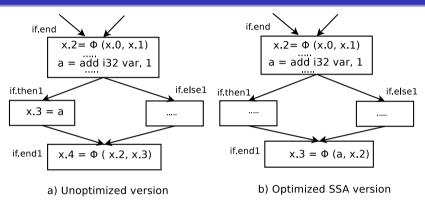
- attempt to "recover" the renamed versions of each base variable that is merged by the  $\phi$ -functions:
- then, allocate a single path-sensitive stack for all versions of the same base variable.

## Optimized SSA forms



a and x.2 are live simultaneously — hence, cannot be different versions of the same variable





a and x.2 are live simultaneously — hence, cannot be different versions of the same variable

in the above example, copy propagation breaks the phi congruence property...



### $\phi-congruence$ property

### Shreedhar et al. [SAS'99]

"The occurrences of all resources which belong to the same phi congruence class in a program can be replaced by a representative resource. After the replacement, the phi instruction can be eliminated without violating the semantics of the original program."

- Sreedhar et al. circumvent the problem by translating the optimized SSA form to the conventional SSA form (that satisfies the phi congruence property) before translating out of SSA.
- We directly build the HPSSA form over the optimized SSA form!

### Brief Algorithm

- Insert  $\tau$ -functions
  - Insert at Thermal Frontiers
- Allocate arguments to  $\tau$ -functions
  - path-sensitive traversal through the program to identify definitions that reach au-functions through hot paths
  - ullet constrains its inspection to only the  $\phi$ -functions and the au-functions

### Allocating $\tau$ -function arguments

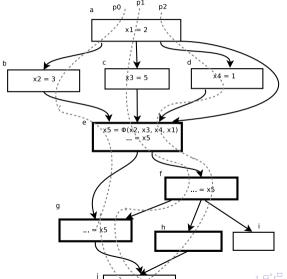
Uses a path-sensitive stack: phiStack

- phiStack is a stack of frames
- each frame  $\langle d_i, \xi_i \rangle$  where  $\xi_i = \{p_1, p_2, \dots\}$
- support operations:
  - push(frame f, block b)
  - pop(block b)

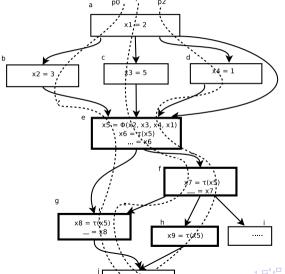
#### High-level algorithm

- ullet Load arguments from  $\phi$  and au-functions along with their hot path sets on **phiStack**
- ullet Assign the definition from the topmost frame of **phiStack** to any au-function encountered

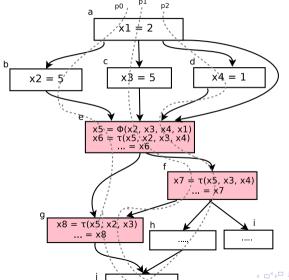
# SSA program



### au-functions inserted



## au-arguments allocated



#### What we modified in LLVM Source?

• New llvm::intrinsic signature, "llvm.tau" to support addition and removal of  $\tau$ -functions to the LLVM SSA IR representation.

```
+ //===-//
+ def int_tau : DefaultAttrsIntrinsic<[llvm_any_ty],
+ [llvm_vararg_ty],
+ []>;
```

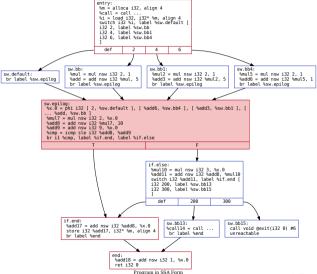
10 11  Modified Verifier::verifyDominatesUse() function since we don't want our intrinsic to interfere with dominators computation.

#### HPSSAPass: Overview

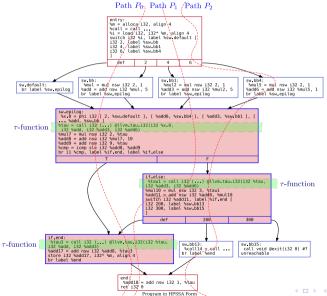
- class HPSSAPass : public PassInfoMixin<HPSSAPass>
  - Implemented llvm::HPSSAPass pass using the new LLVM Pass Manager.
  - Function HPSSAPass::run(Function &F, ...) runs over a llvm::Function and inserts
     "llvm.tau" intrinsic calls with speculative and safe arguments at strategic positions in the
     LLVM IR and handles argument allocation for "llvm.tau" intrinsic calls as described in the
     previous slides.
- Key HPSSA Data Structures :
  - Hot Path Set using llvm::BitVector for maintaining hot paths in the program.
  - Definition Accumalator, defAccumulator(op, currBB) function. The argument "op" is a phi argument that reaches basic-block "currBB" via hot path.
  - A stack of map values std::map<Value\*, Value\*> to store the most "recent" tau definition encountered so far corresponding for a tau variable used later in variable renaming.



### SSA Form



#### Hot Path SSA Form



#### HPSSAPass: Main Pass

- HPSSAPass::run(Function &F, FunctionAnalysisManager &AM)
  - Invoke HPSSAPass::getProfileInfo() function to get a compact representation of all the profiled hot paths in the program and then call HPSSAPass::getCaloricConnector() to get all the caloric connectors from the hot path information. This is a precursor to finding strategic positions to place "llvm.tau" intrinsic calls in the LLVM IR.
  - Runs over each basic block in the function "F" in topological order using iterator returned from llvm::Function::RPOT() call.
  - Uses the llvm::dominates() function from llvm::DominatorTreeAnalysis to check for dominance frontier while processing the child nodes of the current basic block. This step is a part of correctly placing "llvm.tau" intrinsic calls in the LLVM IR.
  - Uses the renaming stack and HPSSAPass::Search() function to search and replace all use
    of PHI result operand with that returned by the "llvm.tau" intrinsic call.

#### HPSSAPass: Destruction Pass

- Out of HPSSA Form.
  - A seperate pass using the new LLVM Pass Manager.
     class TDSTRPass : public PassInfoMixin<TDSTRPass>
  - Using TDSTRPass::run(Function &F, ...), we replace all use of existing tau
    operands with first argument of "llvm.tau" intrinsic (corresponds to the safe
    argument) and remove the "llvm.tau" intrinsic call from the LLVM IR.
  - The LLVM IR becomes identical to what it was before running the HPSSA Pass.

### Presentation Outline: Section 4

- HPSSA: Why another SSA Form?
  - Profile Guided Optimization
  - Doing Profile Guided Optimizations is Hard!
  - Profile Guided SSCCP Analysis using HPSSA Form
- 2 What is HPSSA form?
  - Hot Path SSA Form
  - Profile Guided SSCCP Pass
- How is HPSSA Implemented?
  - Constructing HPSSA Form
  - Implementing HPSSA Form in LLVM
- 4 Conclusion



### Conclusion