# LLVM & HPSSA Hot Path SSA Form in LLVM

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#### What we modified in LLVM Source?

• New llvm::intrinsic signature, "llvm.tau" to support addition and removal of  $\tau$ -functions to the LLVM SSA IR representation.

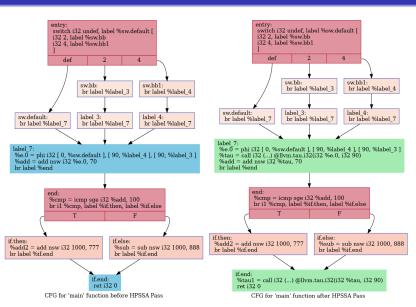
#### What we modified in LLVM Source?

 Modified Verifier::verifyDominatesUse() function since we don't want our intrinsic to interfere with dominators computation.

#### HPSSAPass: Overview

- class HPSSAPass : public PassInfoMixin<HPSSAPass>
  - Implemented llvm::HPSSAPass pass using the new LLVM Pass Manager.
  - Function HPSSAPass::run(Function \&F, ...) runs over a llvm::Function and inserts "llvm.tau" intrinsic calls with speculative and safe arguments at strategic positions in the LLVM IR and handles argument allocation for "llvm.tau" intrinsic calls as described in the previous slides.
- Key HPSSA Data Structures :
  - Hot Path Set using llvm::BitVector for maintaining hot paths in the program.
  - Definition Accumulator, defAccumulator(op, currBB) function.
     The argument "op" is a phi argument that reaches basic-block "currBB" via hot path.
  - A stack of map values std::map<Value\*, Value\*> to store the
    most "recent" tau definition encountered so far corresponding for a
    tau variable used later in variable renaming.

#### **HPSSA Transformation**



### HPSSAPass: Main Pass

- - Invoke HPSSAPass::getProfileInfo() function to get a compact representation of all the profiled hot paths in the program and then call HPSSAPass::getCaloricConnector() to get all the caloric connectors from the hot path information. This is a precursor to finding strategic positions to place "llvm.tau" intrinsic calls in the LLVM IR.
  - Runs over each basic block in the function "F" in topological order using iterator returned from llvm::Function::RPOT() call.
  - Uses the llvm::dominates() function from llvm::DominatorTreeAnalysis to check for dominance frontier while processing the child nodes of the current basic block. This step is a part of correctly placing "llvm.tau" intrinsic calls in the LLVM IR.
  - Uses the renaming stack and HPSSAPass::Search() function to search and replace all use of PHI result operand with that returned by the "llvm.tau" intrinsic call.

#### HPSSAPass: Destruction Pass

- Out of HPSSA Form.
  - A seperate pass using the new LLVM Pass Manager.
     class TDSTRPass : public PassInfoMixin<TDSTRPass>
  - Using TDSTRPass::run(Function \\delta F, ...), we replace all use of existing tau operands with first argument of "llvm.tau" intrinsic (corresponds to the safe argument) and remove the "llvm.tau" intrinsic call from the LLVM IR.
  - The LLVM IR becomes identical to what it was before running the HPSSA Pass.

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## HPSSAPass: Usage [It is easy!]

- Include llvm::HPSSAPass header file.
- Load shared object using opt tool. opt -load HPSSA.cpp.so ...

```
#include <HPSSA.h> // import the header.
class MyExamplePass : public PassInfoMixin<MyExamplePass> {
  public: PreservedAnalyses run(Function &F,
    FunctionAnalysisManager &AM):
1:
. . .
PreservedAnalyses MyExamplePass::run(Function &F,
  FunctionAnalysisManager &AM) {
  if (F.getName() != "main")
    return PreservedAnalyses::all():
  HPSSAPass hpssaUtil; // Make a HPSSAPass Object.
  hpssaUtil.run(F, AM): // Call the HPSSAPass::run() function.
  std::vector<Instruction *> TauInsts
    = hpssaUtil.getAllTauInstrunctions(F); // Calling HPSSA utility function.
  std::cout << "\t\tTotal Tau Instructions : " << TauInsts.size() << "\n";
}
/// [output] Total Tau Instructions : 7
```

#### New Additions to SCCP Pass

- We implement a speculative version of the SCCP to demonstrate the usefulness of the HPSSA Form.
- Modified the existing SCCP Pass to add in SCCPInstVisitor::visitTauNode() function similar to SCCPInstVisitor::visitPHINode(), which handles the special "lvm.tau" intrinsic instructions added for τ-functions.
- Added a new lattice element type "spec\_constant" in ValueLattice class supporting operations on speculative constants.
- Added new functions in the SCCPInstVisitor and SCCPSolver class to handle operations on speculative constants. Eg. Operands can be marked speculative using markSpeculativeConstant() function.

#### **Further Modifications**

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- Modified the SCCPInstVisitor::mergeIn() function to handle lattice "meet" operation for the new speculative constants introduced.
- Since we added the  $\tau$ -functions as an "llvm.tau" intrinsic which is essentially an llvm:CallInst type, we modified all appropriate visit and marking functions in SCCPInstVisitor, SCCPSolver and SCCPPass to handle this case separately by calling visitTauNode().
- Modified utility functions in SCCPInstVisitor and SCCPSolver class to print marking of speculative constants and related operations for debugging purpose.

```
... // logs
[BBWorkList] Visiting LLVM Instrinsic : llvm.tau (call)
Visiting Tau Instruction
Speculative Operand : , speculative constant
Speculative Operand : llvm.tau.i32, speculative constant
Merged speculative constant into %tau = call i32 (...)
%11vm.tau.i32(i32 %e.0, i32 90) : speculative constant
ValueLattice (TauState) : speculative constant
```

## SSCCP with an Example

We run the speculative SCCP on the example below.

```
int main() {
        int a = 1000, z, c, e = 0;
        switch(c) {
          case 2 : goto label_3; break;
          case 4 : goto label_4; break;
          default : goto label_7;
 8
        label_3:
 9
          e = 90:
10
          goto label 7:
11
        label_4:
12
          e = 100 - 10:
13
          goto label 7:
14
        label 7:
15
        // e in rhs is 90.
16
          e = e + 70:
17
          goto end;
18
        end:
19
        // e is greater than 100 always
20
          if (e >= 100) {
21
            a = a + 777:
22
          } else {
23
            a = a - 888:
24
          } return 0;
25
```

```
switch i32 undef, label %sw.default [
               i32 2, label %sw.bb
               i32 4, label %sw.bb1
                   def
                         ew hh-
                                                sw.bb1
                          br label %label 3
                                                br label %label 4
   sw.default:
                         label 3:
                                                label 4:
   br label %label 7
                          br label %label 7
                                                br label %label 7
 %e.0 = phi i32 [ 0, %sw.default ], [ 90, %label_4 ], [ 90, %label_3 ]
 %tau = call i32 (...) @llvm.tau.i32(i32 %e.0, i32 90)
 %add = add nsw i32 %tau. 70
 br label %end
              %cmp = icmp sge i32 %add, 100
              br i1 %cmp, label %if.then, label %if.else
%add2 = add nsw i32 1000, 777
                                      %sub = sub nsw i32 1000, 888
br label %if.end
                                     br label %if.end
        %tau1 = call i32 (...) @llvm.tau.i32(i32 %tau, i32 90)
        net i32.0
             CFG for 'main' function after HPSSA Pass
```

## SSCCP with an Example

• The PHINODE at label\_7 is removed due to constant propagation of value 90 for variable "e" along all paths. However since the operands of the  $\tau$ -function are marked speculative, this instruction and it's uses are not removed by the SSCCP Pass.

