# The Hot Path SSA Form in LLVM Algorithms & Applications

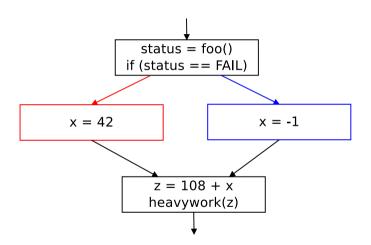
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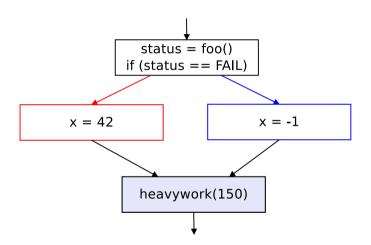
#### Outline

- Introduction
- 2 The Hot Path SSA (HPSSA) Form
- Constructing the HPSSA Form
- 4 Conclusions

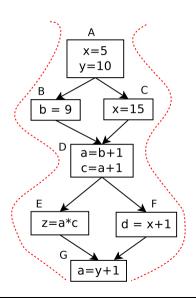
# A case for profile-guided optimizations (PGO)



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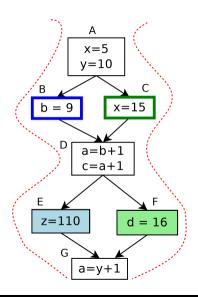
### Profile-guided analysis on paths



#### Summary

- Profile-guided analysis across paths is stronger—can capture correlations between control-flow of basic-blocks
- Collecting path-profiles seems challenging—requires "recording" of a sequence of basic-blocks

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### Profiling acyclic paths

#### Ball-Larus Acyclic Profiling [Ball & Larus, MICRO'96]

- Core idea: assign an identifier to each path, that can be calculated efficiently at runtime
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- Record frequencies against these identifiers (instead of a sequence of node identifiers)

#### Capturing still longer paths (k-iteration paths)

- Allows capturing correlations across loop iterations [Roy & Srikant, CGO'09]; a generalization of the Ball-Larus algorithm
- Subsequent work by other groups [D'Elia & Demetrescu, OOPSLA'13]; uses a prefix forest to record BL paths

### Profile-guided analyses

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#### Error resilient modules

- modules for statistical summarization on samples, generate data for ML models
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#### Optimizations that don't impact correctness

- eg. register allocation
- No impact on code size



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### Why is path-profile-guided analysis hard?

#### $disparate\ data-structures:\ program\ +\ profile$



1-2-4-5	30
21-2-5-5-1	25
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21-2-5-5-1	25
1-2-4-5	30
21-2-5-5-1	25

### Why is path-profile-guided analysis hard?

- There has been enough interest in path-profile-guided analysis and optimizations....
- ...however, designing path-profile-guided variants of traditional optimizations remained hard
- ...hard enough to justify publications per optimization
  - Gupta, Benson, Fang. Path profile guided partial dead code elimination using predication. PACT '97.
  - Gupta, Benson, Fang. Path profile guided partial redundancy elimination using speculation. ICCL '98.
  - ...

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....into a single, consistent data-structure

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... that provides the convenience and elegance of an SSA-like intermediate form

...allowing the design of profile-guided versions of "traditional" optimizations with **trivial algorithmic modification** of the base algorithms

...providing a **common representation** for both "traditional" as well as profile-guided analysis and optimizations

### The Hot Path SSA Form (HPSSA)

#### Semantics of a $\phi$ -function

$$y = \phi(x_1, x_2, \dots, x_n)$$

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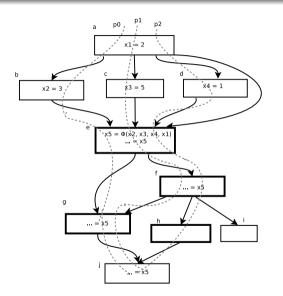
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$$y = \phi(x_1, x_2, \dots, x_n)$$

#### Semantics of a $\tau$ -function

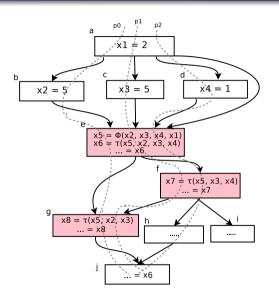
$$au(x_0,x_1,x_2,\ldots,x_n) = \left\{ egin{array}{ll} x_0 & ext{safe interp.} \\ \phi(x_1,x_2,\ldots,x_n) & ext{speculative interp.} \end{array} 
ight.$$

#### The SSA form



#### No frequent path carrying:

- def  $x_2 = 3$  to use at block **f**
- def  $x_4 = 1$  to use at block **g**
- def  $x_1 = 2$  to either **f** or **g**



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#### **Properties**

If a program is in the Hot Path SSA form, then,

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#### **Properties**

If a program is in the Hot Path SSA form, then,

- each use of a variable is reachable by a single definition; [SSA-like form]
- safe interpretation: [supports traditional analysis]
  - each use of a variable is reachable by the *meet-over-all-paths* reaching definition chains;
- speculative interpretation: [supports profile-guided analysis]
  - each use of a variable in a basic-block is reachable by the meet-over-frequent-paths
    reaching definitions.



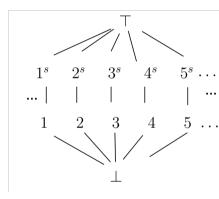
<sup>&</sup>lt;sup>a</sup>or the meet-over-all-paths reaching definition chains, if the use is not reachable from any meet-over-hot-paths reaching definition chain

- Introduce new speculative values  $\{\ldots,1^s,2^s,\ldots\}\in C^S$
- Operation with speculative values result in speculative results (with same semantics as base operator)

$$\alpha^{s} \langle op \rangle \beta = (\alpha \langle op \rangle \beta)^{s}$$

• Transfer function for  $\tau$ -functions  $(\beta = x_1 \sqcup x_2 \sqcup \cdots \sqcup x_n$ , i.e. join of speculative args.)

$$\tau(x_0, x_1, \dots, x_n) \sqcup \begin{cases} \top & \text{if } \beta = \top \\ \beta & \text{if } \beta \neq \top \land x_0 \sqsubseteq \beta \\ \beta^s & \text{otherwise} \end{cases}$$

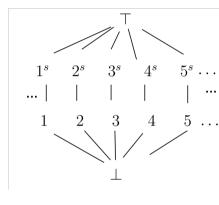


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Almost trivial to generate profile-guided variants of standard analyses—an afternoon to "port" SCCP to SpecSCCP!



Table 1. Speculative Constants discovered by the SSCP algorithm. ( '~' indicates almost; grp, prg, & src refer to inputs graphic, program & source respectively).

		Variable Uses		Expression Uses		Total		
Program	Inpt	Uses	HitRt	Uses	$\mathbf{Hit}\mathbf{Rt}$	Hits	Misses	HitRt
181.mcf	-	33110	100.00	49665	100.00	82775	0	100.00
175.vpr	-	6938074	100.00	8110837	100.00	15048911	0	100.00
	$\operatorname{grp}$	26592	100.00	5	100.00	26597	0	100.00
164.gzip	prg	17412	100.00	5	100.00	17417	0	100.00
	$\operatorname{src}$	4721	99.98	5	100.00	4725	1	99.98
197.parser	-	165970964	~100.00	340	97.94	165970861	443	~100.00
	$\operatorname{grp}$	132106650	~100.00	938	76.97	132107372	216	~100.00
256.bzip2	prg	100819492	~100.00	6576416	15.67	101849942	5545966	94.84
	$\operatorname{src}$	108134316	~100.00	5256006	17.94	109077366	4312956	96.20

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Optimized on "train" inputs, results produced on "ref" inputs... Shows the value of profile-guided analysis for code understanding and optimizations...

# Profile-Guided Register Allocation [Jain et al., HiPC'16]

```
# BB#4:
                                         # %if.then
                                                                  # BB#4:
                                                                                                           # %if.then
                                             in Loop: Header=B
                                                                                                                in Loop: Header=B
        add1
                %edi, 32(%esp)
                                         # 4-byte Folded Spill
                                                                          addl
                                                                                   %edi, %ecx
        addl
                                                                          addl
                                                                                   $20, %esi
                $20. %esi
        add1
                $117, %ebp
                                                                          add1
                                                                                   $117, %ebp
                .LBBO 6
                                                                                   .LBB0 6
                                                                           jmp
        .align
                16, 0x90
                                                                          .align
                                                                                   16, 0x90
.LBB0_5:
                                         # %if.else
                                                                                                           # %if.else
                                                                  .LBB0_5:
                                             in Loop: Header=B
                                                                                                                in Loop: Header=B
        addl
                $101, 40(%esp)
                                         # 4-byte Folded Spill
                                                                                                            # 4-byte Folded Spill
                                                                          addl
                                                                                   $101, 40(%esp)
                                                                                                           # 4-byte Reload
                                                                          movl
                                                                                   36(%esp), %edx
        subl
                40(%esp), %ecx
                                         # 4-byte Folded Reloa
                                                                          subl
                                                                                   40(%esp), %edx
                                                                                                           # 4-byte Folded Reloa
                                                                                                           # 4-byte Spill
                                                                          movl
                                                                                   %edx, 36(%esp)
        addl
                $33. %edi
                                                                          addl
                                                                                  $33, %edi
        addl
                $-11, %ebp
                                                                          addl
                                                                                   $-11, %ebp
```

```
.LBB0_5 is hot: 40(%esp) is cached in %ecx BB#4 is hot: 32(%esp) is cached in %ecx
```

#### ILP based register allocator

- Implements integer linear programming based profile-guided register allocation
- Base allocator (without profile information) is simplified version of [Goodwin and Wilken, 1996]

#### Base Allocator

$$\begin{aligned} & \min \sum_{v \in V, l \in L} S_{v,l} \cdot x_{v,l,\sigma}, \text{ (minimize spills)} \quad \textit{such that} \\ & \forall_{v \in V} \forall_{l \in L} \sum_{r \in R} x_{v,l,r} = 1 \quad \text{(a variable to at least one location)} \\ & \forall_{r \in R-\sigma} \forall_{l \in L} \sum_{v \in V} x_{v,l,r} \leq 1 \text{ (a register holds at most one variable)} \end{aligned}$$

### Cost for $\tau$ -functions

#### Modelling $\tau$ -functions

The cost for  $t = \tau(safe, t_1, t_2, ..., t_n)$ :

$$TauCost = \sum_{i=1}^{i=n} (loc(t, t_i) * freq(t_i))$$

where  $loc(t, t_i)$  is 1 for the locations where t and  $t_i$  are different, else 0.

## The objective function

The objective function for register allocation over HPSSA is modified to include the *TauCost* discussed above.

#### The objective fuction

$$\min \sum_{v \in V, l \in L} S_{v,l} \cdot x_{v,l,\sigma} + \sum_{i=1}^{l=m} TauCost_i$$
 (1)

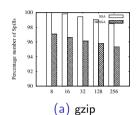
where,  $TauCost_i$  is the cost for the  $i^{th}$   $\tau$ -function (and m is the total number of  $\tau$ -functions in the program), and

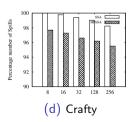
$$S_{v,l} = \begin{cases} 100, & \text{if v has further uses along hot path;} \\ 15, & \text{if v has further uses only along cold path;} \\ 0, & \text{if v has no further use.} \end{cases}$$

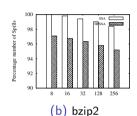
Almost a trivial modification to the objective function...

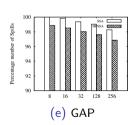


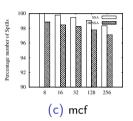
# Percentage of spills for different register sizes

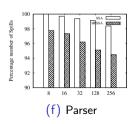














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## Hot/Cold Paths

#### Definition 1. Hot/Cold Paths

A program path  $p: n_1 \rightsquigarrow n_2$  is said to be hot (cold) if the sequence of edges from node  $n_1$  to  $n_2$  appears (does not appear) in any profiled path that occurs frequently in the program profile.

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#### Definition 3. Hot/Cold Reaching Definitions and Definition Chains

A definition  $\delta$  at a basic-block  $n_1$  is said to reach a respective use at a basic-block  $n_2$  hot if there exists a hot path from  $n_1$  to  $n_2$ , and  $\delta$  is not killed along that path. A definition  $\delta$  at a basic-block  $n_1$  is said to reach a respective use at a basic-block  $n_2$  cold if there does not exist a hot path from  $n_1$  to  $n_2$ , and  $\delta$  is not killed at least along one cold path from  $n_1$  to  $n_2$ .

## Inserting $\tau$ -functions

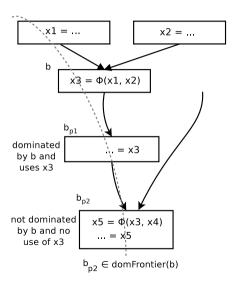
#### Necessary condition for $\tau$ -functions

Lemma 1. A node n requires a  $\tau$ -function for variable x due to a definition  $d^x$  (of a variable x) if

- n is the junction of a hot and a cold path, i.e., paths at different temperatures meet at this node;
- 2 n is reachable by at least two different definitions of the variable x.

Proof. If condition I fails, a  $\tau$ -function is unnecessary as n can then be reachable by only hot or only cold definitions of x. If condition II fails, a  $\tau$ -function is again unnecessary as the node is then dominated by a definition of x.

## Inserting au-functions



## Inserting $\tau$ – functions

### Definition 4. Thermal Frontier (TF)

For definition d defined at a node u reaching v, v is in the Thermal Frontier of (u,d),  $v \in TF(u,d)$ , iff

- the node v is also exposed to a reaching definition d' defined at a node  $u \notin Dom(w)$  (w not dominated by u)
- $\theta(u \leadsto v) \neq \theta(w \leadsto v)$
- **3** v is the first node on the paths  $u \rightsquigarrow v$  and  $w \rightsquigarrow v$  that satisfies the above properties.

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#### $\mathsf{Theorem}$

For a set of visible definitions of a variable x at a set of nodes  $\kappa$ ,  $\tau$ -statements are only required at the Iterated Thermal Frontier  $ITF^{\times}$  for variable x.



# HPSSA construction [Roy et al., CC'10]

- Insert  $\phi$ -functions:
  - insert  $\phi$ -functions at the *iterated dominance frontiers*
- Insert  $\tau$ -functions
  - insert  $\tau$ -functions at the *iterated thermal frontiers*
- Allocate arguments to  $\phi$ -functions
  - ullet use a variable stack to allocate the  $\phi$ -function arguments
- Allocate arguments to  $\tau$ -functions
  - maintain path-sensitive reaching definitions for each program variable corresponding to each hot path on a path-sensitive stack;
  - for each instruction in the program, the algorithm update the respective stack to record the change in the path-sensitive reaching definitions due to the instruction;
  - when a  $\tau$ -function is encountered, the current set of reaching definitions on the stack is used to allocate the speculative arguments for the  $\tau$ -function.



# HPSSA construction over SSA [Jaiswal et. al., SCOPES'17]

#### **Difficulties**

- Needs the SSA construction identified, broken into and retrofitted with the HPSSA construction phases.
- The algorithm is quite complex!

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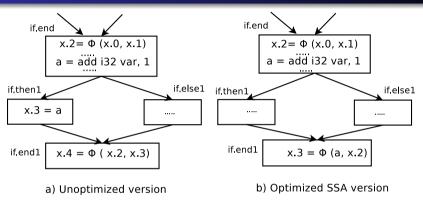
#### HPSSA over SSA

- Easily incorporated within existing compilers: Construction over the SSA form
- Efficient: Lesser instructions have to be traversed
- Simpler: many constructs are eliminated

# A naïve attempt: Mimic [Roy et al.]

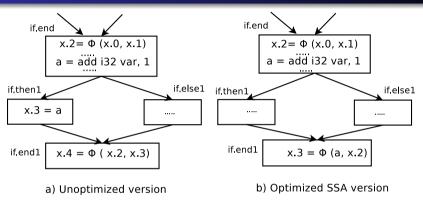
- attempt to "recover" the renamed versions of each base variable that is merged by the  $\phi$ -functions;
- then, allocate a single path-sensitive stack for all versions of the same base variable.

## Optimized SSA forms



a and x.2 are live simultaneously — hence, cannot be different versions of the same variable

## Optimized SSA forms



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in the above example, copy propagation breaks the phi congruence property...

## $\phi$ – congruence property

### Shreedhar et al. [SAS'99]

"The occurrences of all resources which belong to the same phi congruence class in a program can be replaced by a representative resource. After the replacement, the phi instruction can be eliminated without violating the semantics of the original program."

- Sreedhar et al. circumvent the problem by translating the optimized SSA form to the conventional SSA form (that satisfies the phi congruence property) before translating out of SSA.
- We directly build the HPSSA form over the optimized SSA form!

## Brief Algorithm

- Insert  $\tau$ -functions
  - Insert at Thermal Frontiers
- Allocate arguments to  $\tau$ -functions
  - $\bullet$  path-sensitive traversal through the program to identify definitions that reach  $\tau\text{-functions}$  through hot paths
  - ullet constrains its inspection to only the  $\phi$ -functions and the au-functions

## Allocating $\tau$ -function arguments

Uses a path-sensitive stack: phiStack

- phiStack is a stack of frames
- each frame  $\langle d_i, \xi_i \rangle$  where  $\xi_i = \{p_1, p_2, \dots\}$
- support operations:
  - push(frame f, block b)
  - pop(block b)

#### High-level algorithm

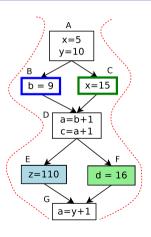
- Load arguments from  $\phi$  and  $\tau$ -functions along with their hot path sets on **phiStack**
- Assign the definition from the topmost frame of **phiStack** to any  $\tau$ -function encountered



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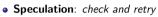


z=110



Introduction 000000

### Profile-guided analyses to optimizations



- eg. value speculation
- Can impact code size (significant impact w/o speculation support in hardware)

#### Compensation code

- eg. superblock scheduling
- Can impact code size

#### Error resilient modules

- modules for statistical summarization on samples, generate data for ML models
- No impact on code size

#### Optimizations that don't impact correctness

- eg. register allocation
- No impact on code size







The Hot Path SSA (HPSSA) Form

Constructing the HPSSA Form

Conclusions

#### Our Objective

Can we weave profile information into the program representation

....into a single, consistent data-structure

... that provides the convenience and elegance of an SSA-like intermediate form

...allowing the design of profile-guided versions of "traditional" optimizations with trivial algorithmic modification of the base algorithms

...providing a **common representation** for both "traditional" as well as profile-guided analysis and optimizations





x= y=1
b = 9
D a=t
c=z
z=110
G a=v

Profile-guided analy

duction The Hot Path SSA (HPSSA) Form

onstructing the HPSSA Form

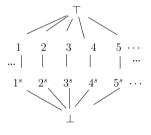
Conclusions 0000000000

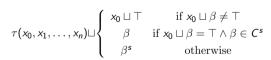
### Speculative Sparse Conditional Constant Propagation (SSCCP)

- ullet Introduce new speculative values  $\{\ldots,1^s,2^s,\ldots\}\in\mathcal{C}^{\mathcal{S}}$
- Operation with speculative values result in speculative results (with same semantics as base operator)

$$\alpha^{s} \langle op \rangle \beta = (\alpha \langle op \rangle \beta)^{s}$$

• Transfer function for  $\tau$ -functions ( $\beta = x_1 \sqcup x_2 \sqcup \dots$ )



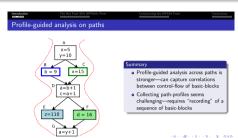


Almost trivial to generate profile-guided variants of standard analyses—it took us an afternoon to "port" SCCP to SSCCP!





Our Objective





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900 S (S) (S) (B) (B)



- · Speculation: check and retry
  - eg. value speculation
     Can impact code size (significant impact w/o speculation support in hardware)
- Compensation code
  - eg. superblock scheduling
  - Can impact code size
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  - modules for statistical summarization on samples, generate data for ML models
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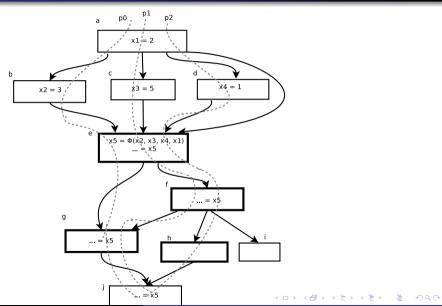
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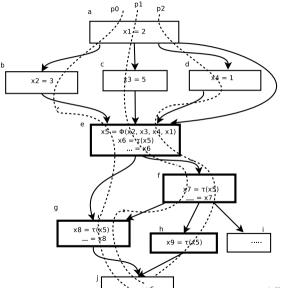
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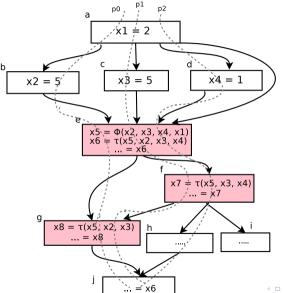
# SSA program



## au-functions inserted



# au-arguments allocated



### **Conclusions**

- We propose a new algorithm that converts SSA programs to HPSSA. Being built over the SSA form, our new algorithm is much more suited for compiler frameworks that offer an SSA-based intermediate representation. This algorithm is also simpler and more efficient than the existing algorithm that builds HPSSA programs from non-SSA programs.
- Our algorithm is capable of operating on optimized SSA forms (resulting from compiler optimizations on the SSA form) that do not satisfy the *Phi Congruence Property*.
- We design an ILP-based path-profile guided register allocation. We provide experimental evaluation of the performance of our approach on eight benchmarks of SPEC CINT2000 benchmark suite.

## Questions

Questions?

 The occurrences of all resources which belong to the same phi congruence class in a program can be replaced by a representative resource. After the replacement, the phi instruction can be eliminated without violating the semantics of the original program.



```
1: Calculate all caloric connectors in the CFG of the program.
 2: isInserted(I,b)=false forall instruction I and basic-block b
 3: Traverse CFG in topological order, performing the following:
 4: for all \phi-function instruction I \in \text{basic-block } b do
       for all hot path p passing through b do
         for b_p= next basic-blocks on p till b_p \notin DomFront(b) do
            if b_n \in \mathsf{CaloricConnectors} \land \neg isInserted(I, b_n) then
 7.
              insert a \tau-function x_{new} = \tau(x_{phi}) cor-
               responding to the \phi-function I: x_{obi} =
 8:
              \phi(\dots) after all \phi-function statements
              in block bo
               isInserted(1, b_p) = true
 9:
            end if
10:
         end for
11:
12.
       end for
13: end for
```

```
While processing a basic block b.
 1: for all \phi-function I: x_0 = \phi(x_1, x_2, \dots, x_n) \in b do
       for all x; for which O_{\infty}(h) exists do
          phiStack_{x_i}.push(\Omega_{x_i}(b), b)
       end for
       if b \in IncubationNode then
          frame = []
          for all (x_i, \xi_i) \in phiStack_{x_i}(b). Top do
            frame = frame.add(x_i, \xi_i \cup IncubPaths(b))
 Q-
          end for
10-
          for all arguments x_i in \phi-function I do
            if x_i is a concrete defn. \wedge b_n \to b an incoming edge then
11:
               frame = frame.add(x_i, pathSet(b_n \rightarrow b) \cup IncubPaths(b))
12-
13:
            end if
14:
            if x_i is a \phi-argument corresponding to a hot backedge then
               frame = frame.add(x_i,IncubPaths(b))
15:
16:
             end if
17:
          end for
18
          phiStack_..push(frame. b)
       end if
19-
20: end for
21: for all \tau-function instructions l \in h do
       for all (x_i, \mathcal{E}_i) \in phiStack_{\infty}(b). Top do
          if \mathcal{E}_i \cap pathSet(b) \neq \emptyset then
23
            add (x_i, \xi_i \cap pathSet(b)) to speculative arguments of I
24
25
          end if
26.
       end for
27: end for
28: for all outgoing edge b \rightarrow b_s do
      if be is a join node then
30
          for all (x_i, \mathcal{E}_i) \in phiStack_v. Top do
            if \mathcal{E}_i \cap pathSet(b \rightarrow b_e) \neq \emptyset then
31:
               \Omega_{x_i}(b_s) = \xi_i \cap pathSet(b \rightarrow b_s)
32
             end if
33
          end for
25.
       and if
36: end for
37: Make a recursive call on the children of node b in the dominator tree, traversing
    them in topological of the nodes in the control-flow graph order.
38: Pop off all frames pushed by block b from all phiStacks
```





