Your name

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You are to design and build a circuit which will produce the output signal F based upon the four input signals w, x, y, and z. This function can be implemented with 7 NAND gates. You may use inverters, but only at the input. The truth table giving F in terms of w, x, y, and z is as follows:

w	x	у	z	F(w,x,y,z)
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	0
				1
1	1		1	0
1	1	1	0	0
1	1	1	1	1
	0 0 0 0 0 0 0 0 0 1 1 1 1 1	0 0 0 0 0 0 0 0 0 0 1 0 1 0 1 0 1 0 1	0 0 0 0 0 0 0 0 0 0 1 0 0 1 0 0 1 1 0 0 1 1 1 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1 0 1 1 1 1 1 0 1	0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 1 1 0 1 0 0 0 1

Make sure you check the MSOP form with the instructor before proceeding.

When you are done, make sure this sheet is retained in your lab folder.

