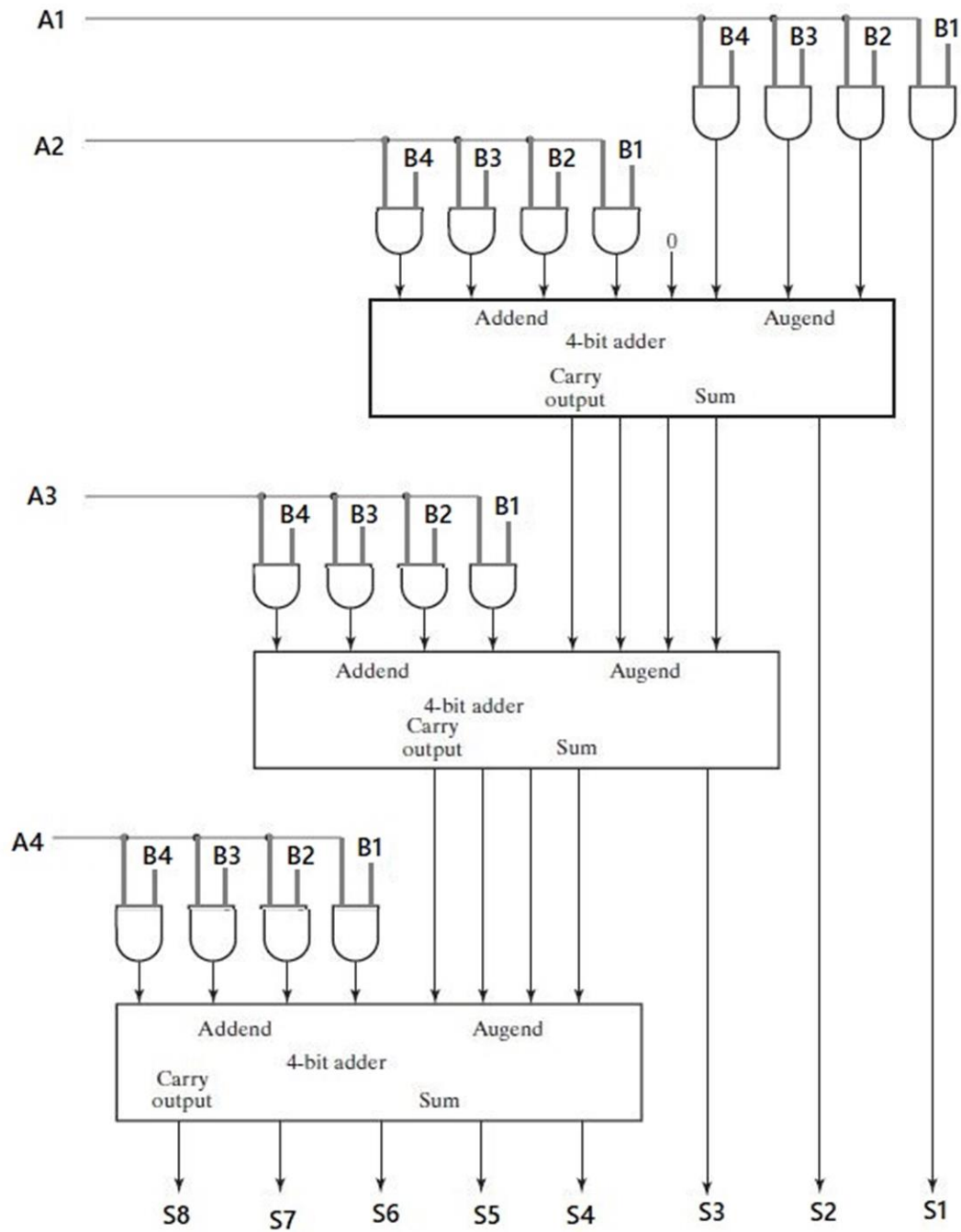


LOGIC CIRCUIT DIAGRAM



A 4-Bit by 4-Bit Binary Multiplier