Project 3: Sequential Chips

Student name(s): _____

Grading method: The implementation of some chips was described in the book, and some chips are simpler than others. The different weights assigned to the chips below reflect this variance. If the chip passes *all* the tests specified in the supplied test script, it receives two thirds of its allotted points. The remaining third reflects our evaluation of the way the chip is implemented.

Generally speaking, we prefer implementations that use *as few chip-parts as possible*, even if it implies using high-level and seemingly less efficient chip-parts. For example, it is better to use a Mux chip instead of more primitive chip-parts that deliver the same Mux functionality.

Chip	*Working?*	*Well built?*	*Comments*
Bit	/ 7	/ 3	
Register	/ 7	/ 3	
RAM8	/ 13	/ 6	
RAM64	/ 13	/ 6	
RAM512	/ 5	/ 3	
RAM4K	/ 5	/ 3	
RAM16K	/ 5	/ 3	
PC	/ 12	/ 6	
Total	/ 67	/ 33	

Total grade: _____