

A

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C

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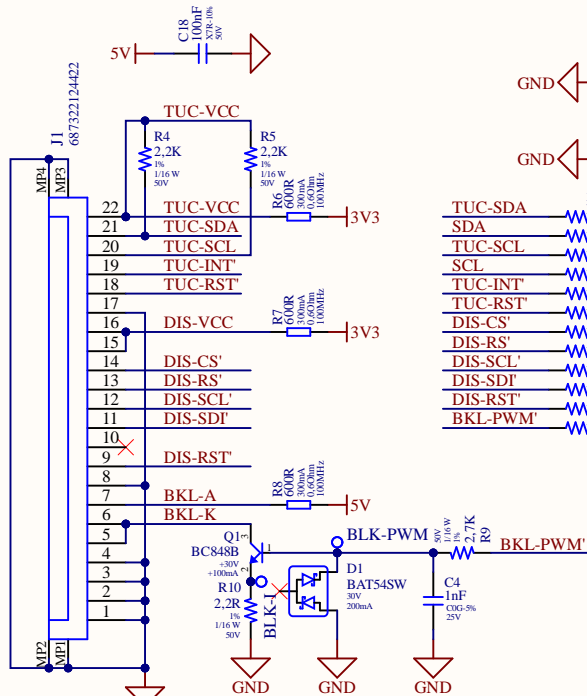
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A

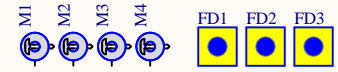
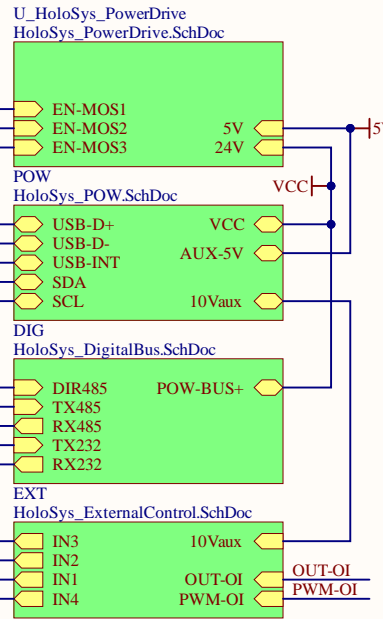
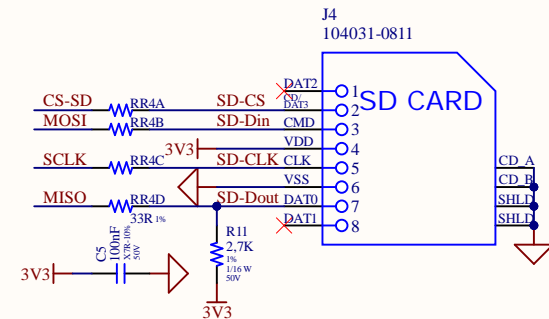
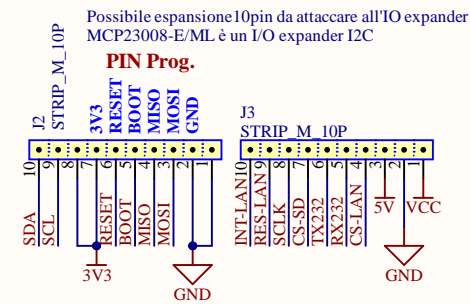
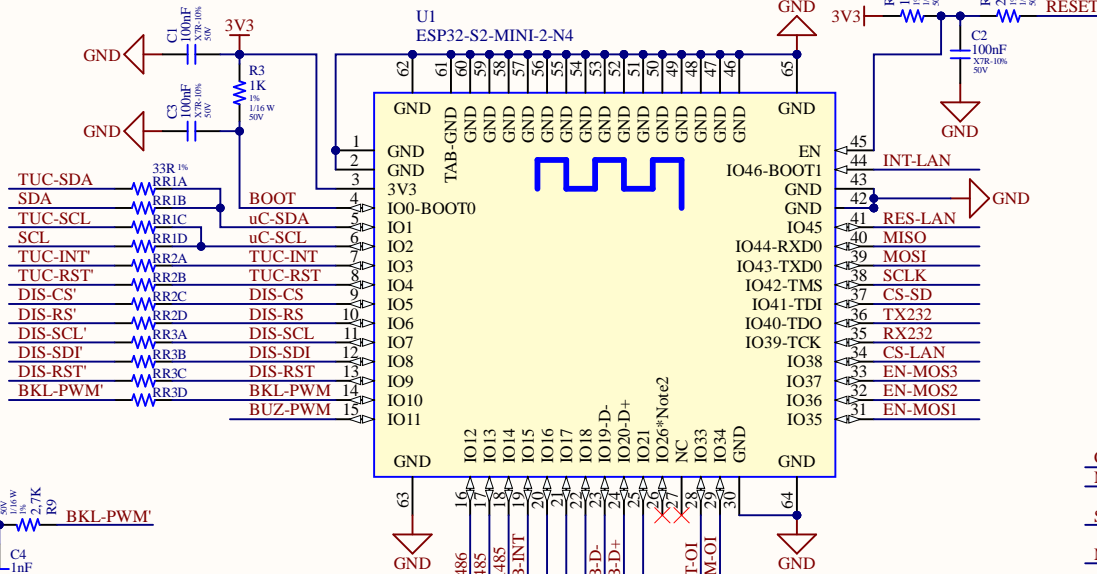
B


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Limitare a 120mA. Aggiungì Res al posto di ferrite per gestire meglio calore



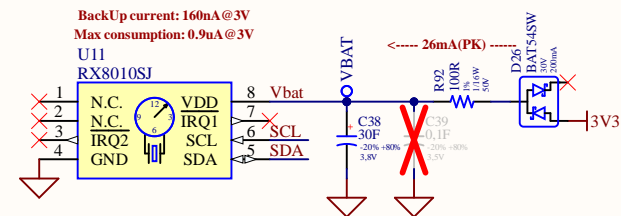
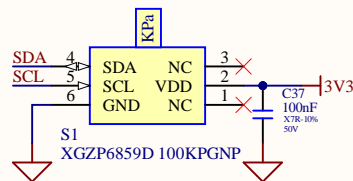
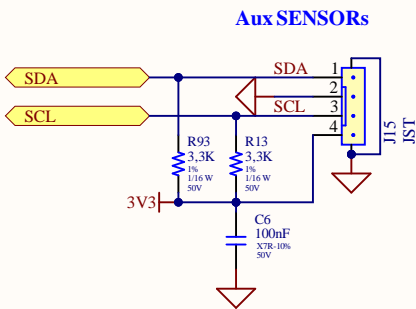
PRJ Name: HoloSys - Var: STD_ASSY			
Title: HoloSys Main			
Last Modification Date: 8/30/2024	Page 1 of 6		
PCB Code: PCB-TRS-HOSY-000-1	Rev. 1		
BRD Code: BRD-TRS-HOSY-000-1	Rev. 1	Designed By: G.Montanari Approved By: HSW Checked By: M.Zannarini	
File GIT Rev.: Not in version control			
Prj GIT Rev.: Not in version control			


1

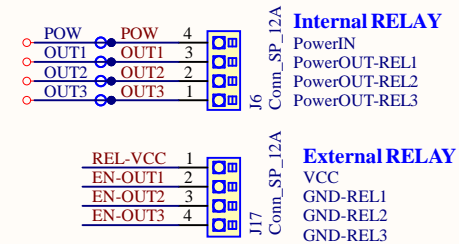
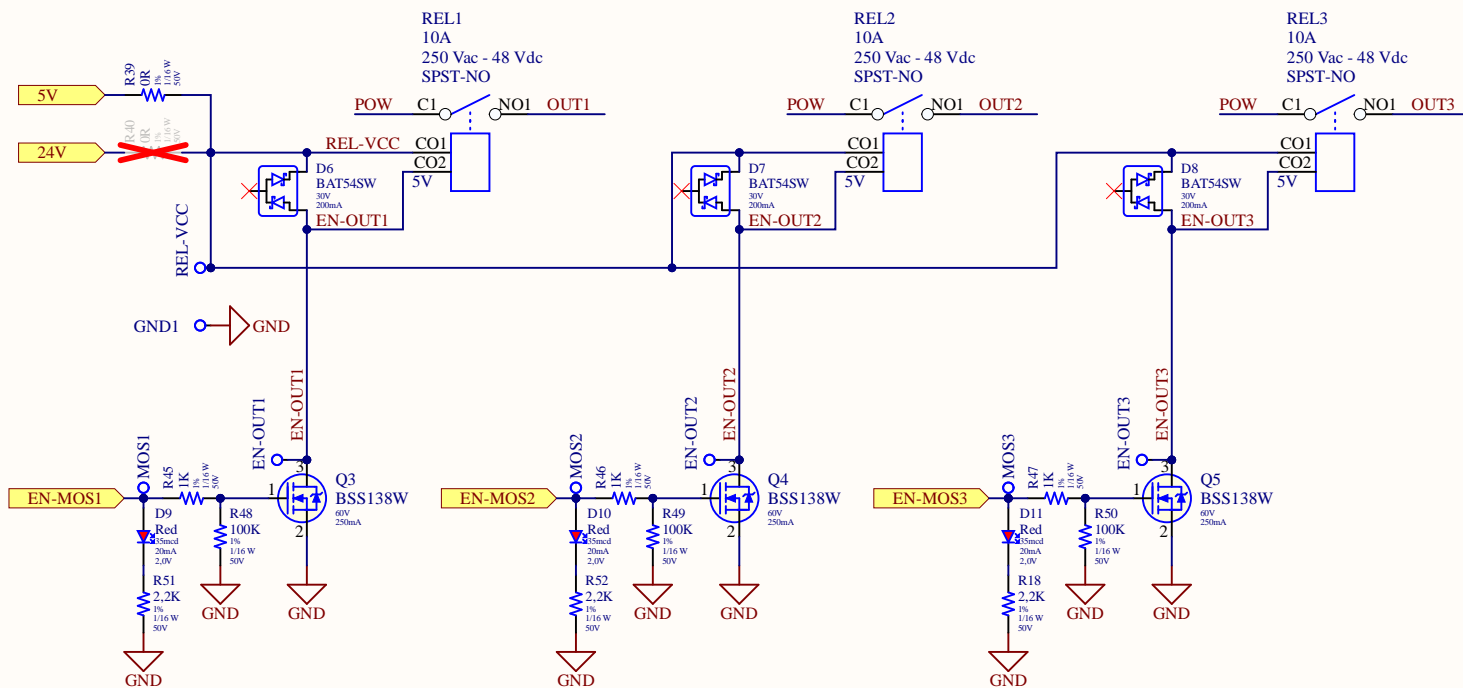
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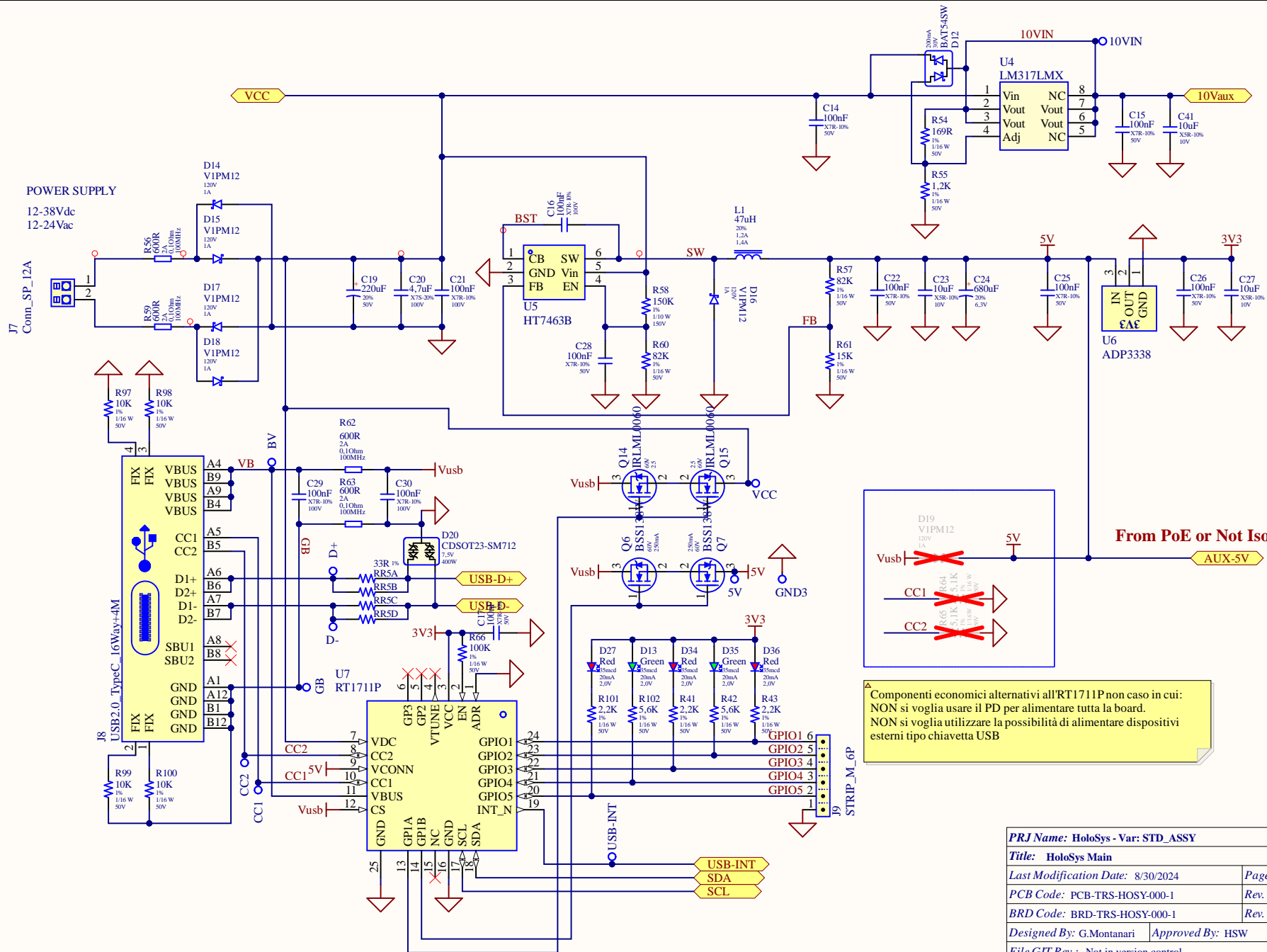
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PRJ Name: HoloSys - Var: STD_ASSY			
Title: HoloSys Main			
Last Modification Date: 8/30/2024	Page 2	of 6	
PCB Code: PCB-TRS-HOSY-000-1	Rev. 1		
BRD Code: BRD-TRS-HOSY-000-1	Rev. 1		
Designed By: G.Montanari	Approved By: HSW	Checked By: M.Zannarini	
File GIT Rev.: <u>Not in version control</u>			
Prj GIT Rev.: <u>Not in version control</u>			



PRJ Name: HoloSys - Var: STD_ASSY			
Title: HoloSys Main			
Last Modification Date: 8/30/2024	Page	3 of 6	
PCB Code: PCB-TRS-HOSY-000-1	Rev.	1	
BRD Code: BRD-TRS-HOSY-000-1	Rev.	1	
Designed By: G.Montanari	Approved By: HSW	Checked By: M.Zannarini	
File GIT Rev.: Not in version control			
Prj GIT Rev.: Not in version control			



PRJ Name: HoloSys - Var: STD_ASSY			
Title: HoloSys Main			
Last Modification Date: 8/30/2024	Page	4 of 6	
PCB Code: PCB-TRS-HOSY-000-1	Rev.	1	
BRD Code: BRD-TRS-HOSY-000-1	Rev.	1	
Designed By: G.Montanari	Approved By: HSW	Checked By: M.Zannarini	
File GIT Rev.: Not in version control			
Prj GIT Rev.: Not in version control			

1

2

3

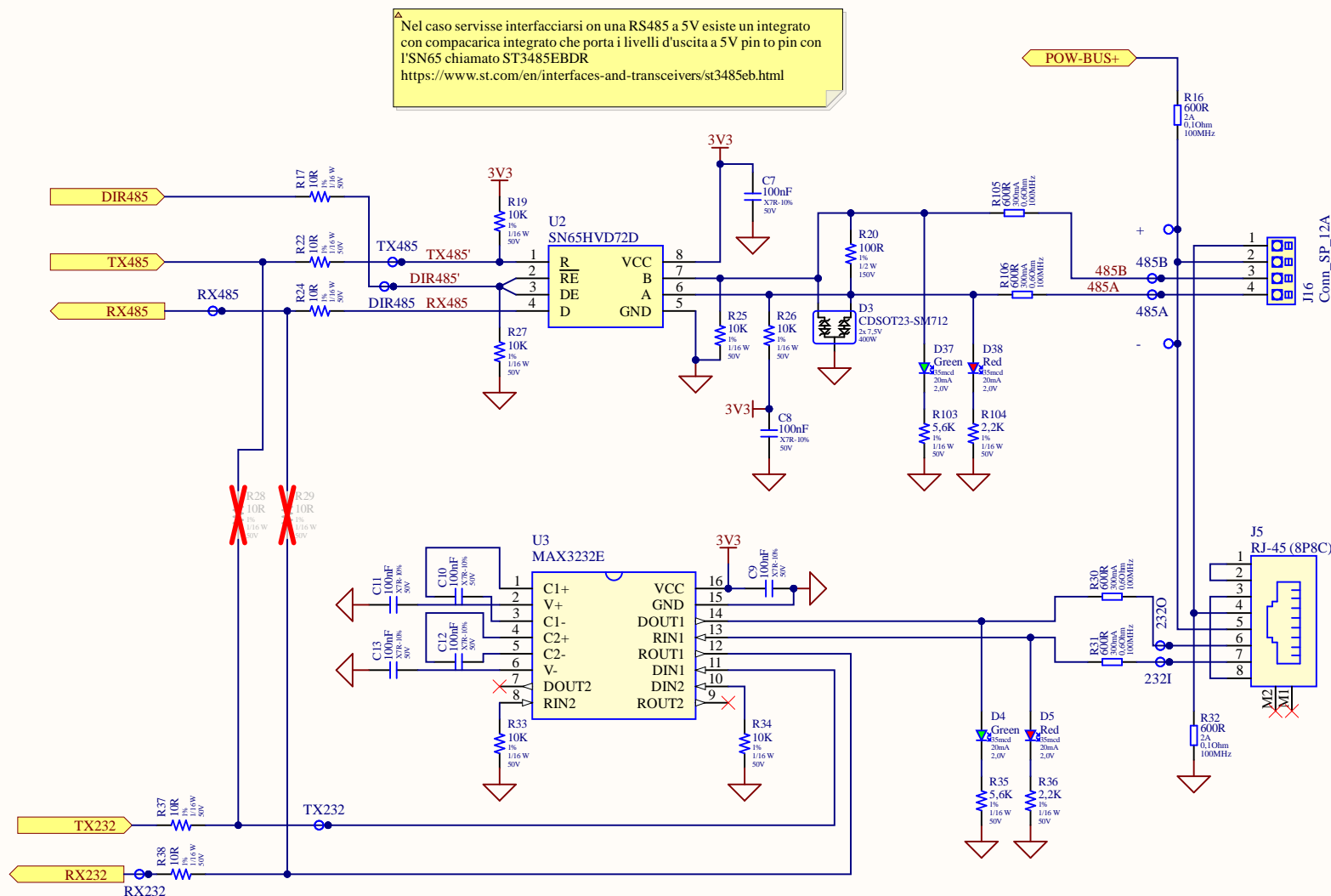
4

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L'utilizzo della RS232 non è compatibile con la presenza del modulo LAN che utilizza questi pin. Se è necessario usare la RS232 è non è necessaria la RS485 modificare le opportune resistenze

PRJ Name: HoloSys - Var: STD_ASSY

Title: HoloSys Main

Last Modification Date: 8/30/2024

Page 5 of 6

PCB Code: PCB-TRS-HOSY-000-1

Rev. 1

BRD Code: BRD-TRS-HOSY-000-1

Rev. 1

Designed By: G.Montanari

Approved By: HSW

Checked By: M.Zannarini

File GIT Rev.: Not in version control

Prj GIT Rev.: Not in version control

1

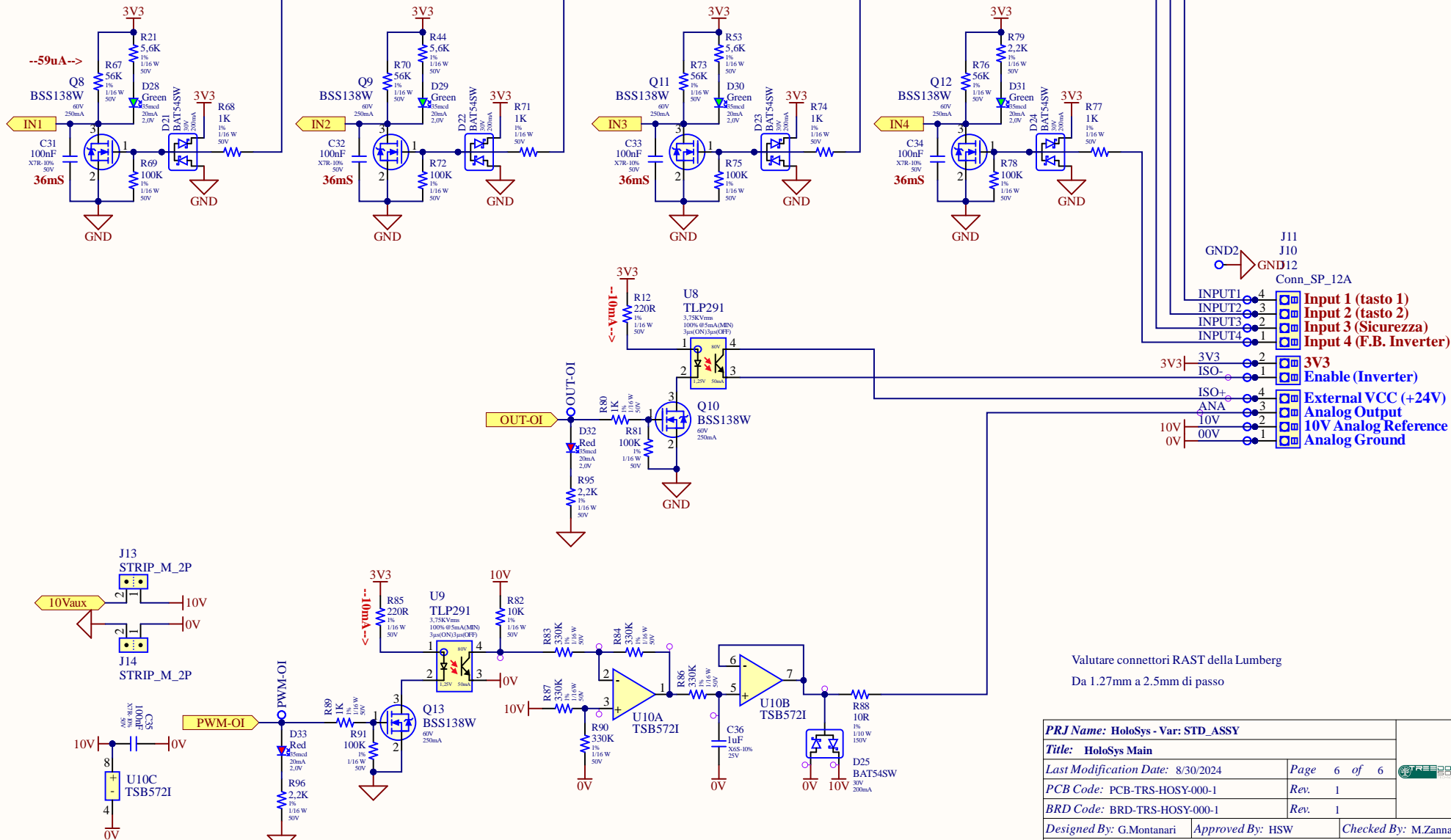
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FREE SOLUTION

D



PRJ Name: HoloSys - Var: STD_ASSY		
Title: HoloSys Main		
Last Modification Date: 8/30/2024	Page 6 of 6	
PCB Code: PCB-TRS-HOSY-000-1	Rev. 1	
BRD Code: BRD-TRS-HOSY-000-1	Rev. 1	
Designed By: G.Montanari	Approved By: HSW	Checked By: M.Zannarini
File GIT Rev.: Not in version control		
Prj GIT Rev.: Not in version control		