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First Term (Final Project 1)

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Introduction

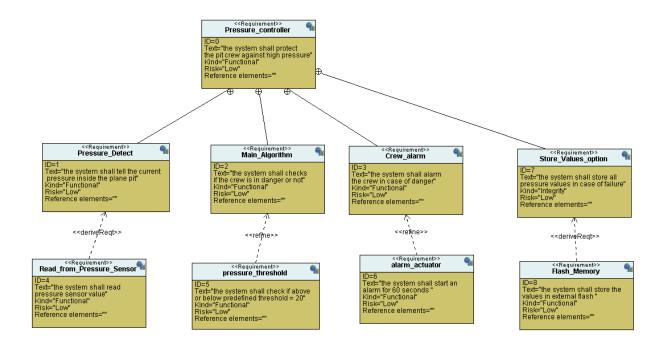
For finishing the first term the first project to be implemented is high pressure sensing algorithm to protect a cabin crew from pressure exceeding 20 bars, upon exceeding that pressure an alarm will be activated for 60 seconds to inform the crew of the situation.

I took the design sequence steps to reach a solution and implement my method for implementing the project which was implemented using waterfall method of designing and starts with making a requirements diagram of it then system analysis before implementing the design by state machine modules.

Microcontroller used in the project is STM32F103C6 from ARM with cortex-m3 with frequency 72 MHz

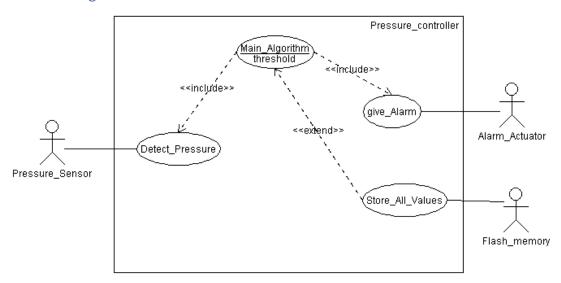
Although the requirements and analysis included an optional feature of storing values of pressure in a flash memory it wasn't implemented in the design.

Requirements

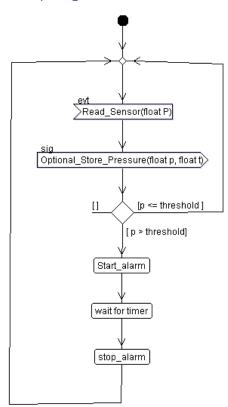


System analysis

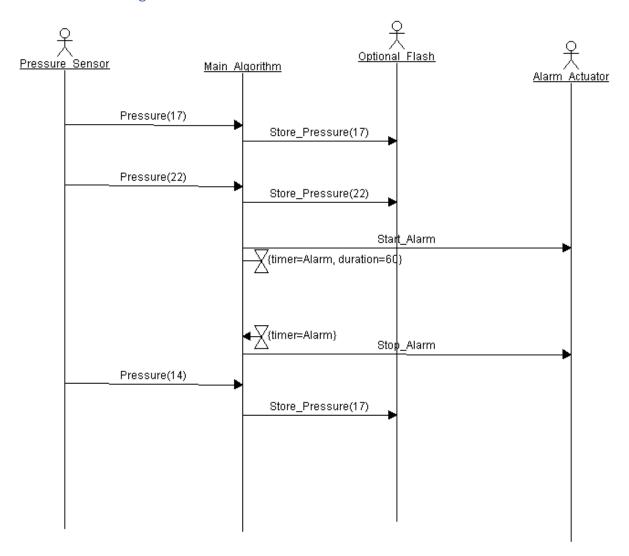
1. Case diagram



2. Activity diagram

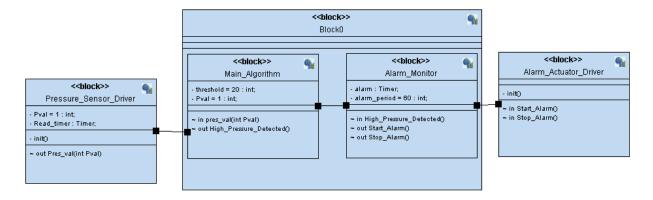


3. Scenario diagram



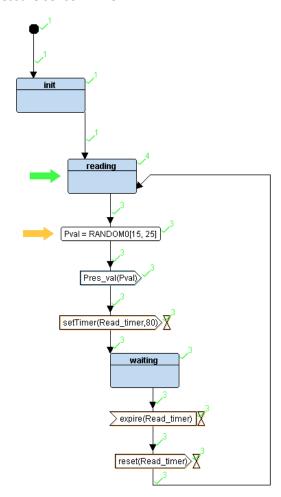
System design

1. Block diagram

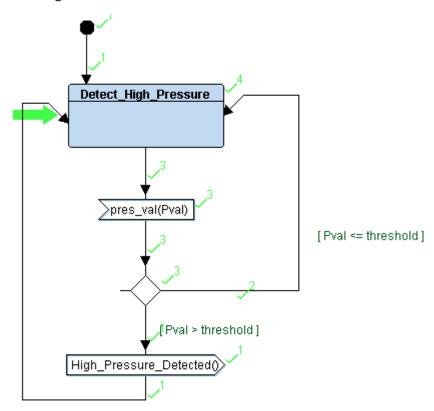


2. State diagrams

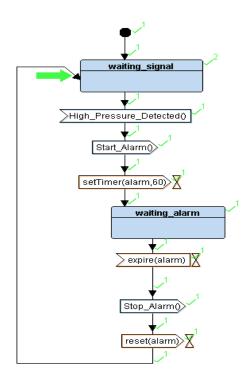
• Pressure sensor Driver



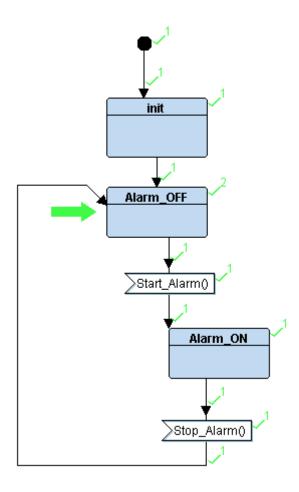
Main Algorithm



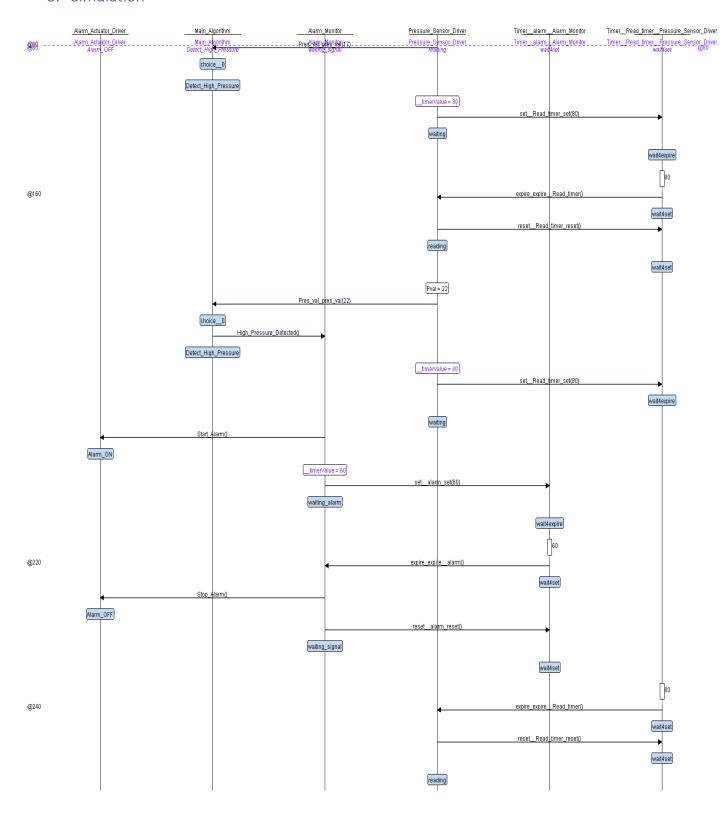
• Alarm monitor



Alarm actuator driver



3. Simulation



Code

1. Pressure sensor state machine modules.

```
1 ⊟/*
 2
    * PS Driver.h
 3
     * Created on: Feb 28, 2022
 4
 5
           Author: Hassan
   L */
 6
7
    8
9
    #define PS_DRIVE_H_
10
11
    #include "state.h"
12
   enum{
13
14
        PS reading,
15
        PS_waiting
16
    -}PS_state_id;
17
    /* prototypes */
18
19
     STATE_define_(PS_reading);
     STATE define (PS_waiting);
20
21
     /* ptr to function for states in PS */
22
23
     extern void (*PS_state)();
24
     #endif /* PS DRIVE H */
25
26
```

```
□/*
2
     * PS Driver.c
 3
 4
     * Created on: Feb 28, 2022
 5
            Author: Hassan
 6
     L */
 7
 8
     #include "PS_Driver.h"
9
10
     void (*PS state)();
11
     unsigned int PS Pval;
12
     /*init function is inside GPIO initialization */
13
14
15
16
    ☐STATE_define_(PS_reading) {
17
18
         PS state id = PS reading; //state name
19
20
         PS Pval = getPressureVal(); // state action
         Pres_val(PS_Pval);
21
22
         PS_state = STATE(PS_waiting);
    L
23
24
25
    ☐STATE_define_(PS_waiting){
26
27
         PS_state_id = PS_waiting; //state name
28
29
         Delay( 80000 ); // state action
30
31
         PS_state = STATE(PS_reading);
     L
32
```

2. Alarm state machine modules

```
□/*
     * Alarm.h
3
     * Created on: Feb 28, 2022
* Author: Hassan
5
     L */
6
8
    9
     #define ALARM_H
10
     #include "state.h"
11
12
13 == enum{
14
         High_Pressure,
15
         Norm_Pressure
16
     -}Alarm_state_id;
17
18
      /* prototypes */
19
      STATE_define_(High_Pressure);
20
      STATE_define_(Norm_Pressure);
21
22
      /* ptr to function for states in Alarm */
23
      extern void (*Alarm_state)();
24
25
      #endif /* ALARM_H_ */
26
```

```
-/*
     * Alarm.c
2
3
     * Created on: Feb 28, 2022
4
     * Author: Hassan
5
     L */
 6
 7
8
     #include "Alarm.h"
9
10
     #define THRESHOLD 20
11
12
     void (*Alarm state)();
13
     unsigned int Al Pval = 1;
14
     unsigned int Al_Signal = 0;
15
16
    _void Pres_val(int p) { //checks if pressure is high or normal
17
          Al Pval = p;
18
          Alarm_state= (Al_Pval > THRESHOLD)? (STATE(High_Pressure)): (STATE(Norm_Pressure));
19
20
21

☐STATE_define_(High_Pressure) {
22
          Alarm_state_id = High_Pressure; //state name
23
24
          Al Signal =0; // state action
25
         Set_Alarm_actuator(Al_Signal);
26
         Delay(60000);
27
         Alarm_state=STATE(Norm_Pressure);
    L
28
29
30
    STATE_define_(Norm_Pressure) {
31
          Alarm_state_id = Norm_Pressure; //state name
32
33
         Al_Signal =1; // state action
34
         Set_Alarm_actuator(Al_Signal);
35
         Delay(60000);
36
         Alarm_state=STATE(Norm_Pressure);
37
     L
38
```

3. Driver

```
2
    3
      #define DRIVER_H_
4
5
     #include <stdint.h>
6
      #include <stdio.h>
8
      #define SET_BIT(ADDRESS,BIT) ADDRESS |= (1<<BIT)</pre>
9
      #define RESET_BIT(ADDRESS,BIT) ADDRESS &= ~(1<<BIT)
      #define TOGGLE BIT(ADDRESS, BIT) ADDRESS ^= (1<<BIT)
10
11
      #define READ BIT(ADDRESS, BIT) ((ADDRESS) & (1<<(BIT)))
12
13
14
      #define GPIO_PORTA 0x40010800
15
      #define BASE RCC 0x40021000
16
17
      #define APB2ENR *(volatile uint32_t *)(BASE_RCC + 0x18)
18
19
     #define GPIOA_CRL *(volatile uint32_t *)(GPIO_PORTA + 0x00)
20
      #define GPIOA_CRH *(volatile uint32_t *)(GPIO_PORTA + 0X04)
21
      #define GPIOA IDR *(volatile uint32 t *)(GPIO PORTA + 0x08)
      #define GPIOA_ODR *(volatile uint32_t *)(GPIO_PORTA + 0x0C)
22
23
24
25
      void Delay(int nCount);
26
      int getPressureVal();
27
      void Set_Alarm_actuator(int i);
28
      void GPIO_INITIALIZATION ();
29
      #endif /* DRIVER H */
30
31
```

```
2
    #include "driver.h"
3
4
     void Delay(int nCount)
5
    □{
         for(; nCount != 0; nCount--);
    L
7
9
    int getPressureVal(){
10
         return (GPIOA_IDR & 0xFF);
11
12
    Dvoid Set_Alarm_actuator(int i) {
13
        if (i == 1) {
14
15
             SET_BIT(GPIOA_ODR,13); // lamb closes due to pull up resistor
16
17
        else if (i == 0) {
           RESET_BIT(GPIOA_ODR,13); // lamb opens due to pull up resistor
18
    E,
19
20
21
22
    ─void GPIO_INITIALIZATION () {
23
          SET_BIT (APB2ENR, 2);
24
          GPIOA CRL &= 0xFF0FFFFF;
25
         GPIOA_CRL |= 0x00000000;
26
         GPIOA CRH &= 0xFF0FFFFF;
27
         GPIOA_CRH |= 0x22222222;
28
```

4. Main file

```
1 =/*
     * main.c
2
3
4
     * Created on: Feb 28, 2022
5
           Author: Hassan
6
     #include "PS Driver.h"
8
    #include "Alarm.h"
10
11
12 ⊟void setup(){
13
         GPIO_INITIALIZATION();
14
15
         Alarm state = STATE (Norm Pressure);
16
         PS_state = STATE(PS_reading);
17
18
19
   ☐int main () {
20
21
         setup();
22
23
         while (1) {
           PS_state();
24
25
             Alarm_state();
26
27
28
```

5. Make file

```
1 #@copyright: Hassan Samy
 2 CC=arm-none-eabi-
 3 CFLAGS=-mcpu=cortex-m3 -mthumb -gdwarf-2
 4 INCS= -I ./driver
5 LIBS=
 6 SRC=$(wildcard *.c)
 7 OBJ=$(SRC:.c=.o)
8 As=$(wildcard *.s)
 9 AsOBJ=$(As:.s=.o)
10 Project_name=Final_Project1
11
12 all:$(Project_name).bin
       @echo "*"
13
        @echo "******Build is done.******
14
       @echo "*"
15
16
17
18 %.o:%.c
19
       $(CC)gcc.exe -c $(INCS) $(CFLAGS) $< -o $@
20
21 $(Project_name).elf: $(OBJ) $(AsOBJ)
22
       $(CC) ld.exe -T linker_script.ld -Map=Map_file.map $(LIBS) $(OBJ) $(AsOBJ) -o $@
23
24 $(Project_name).bin: $(Project_name).elf
25
        $(CC)objcopy.exe -0 binary $< $@
26
27 clean_all:
       rm *.o *.map *.elf *.bin
28
       @echo "******all clean.******
29
30
31 clean:
32
       rm *.elf *.bin
33
```

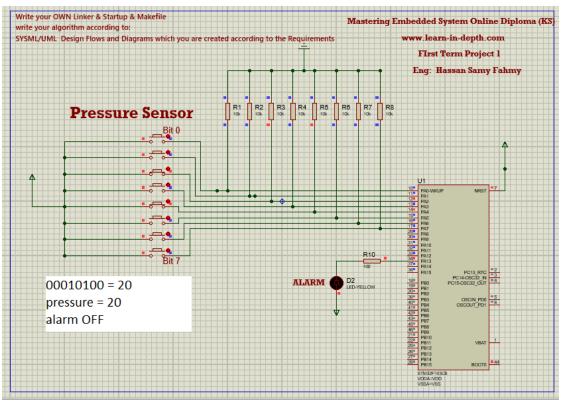
6. Startup file

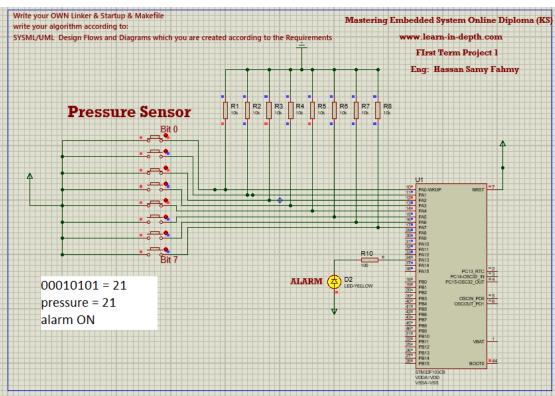
```
rile : startup.c
      * file
3
                      : Hassan Samy
4
         description : performs the start
         mechanism for the board until
5
6
             reaching main function.
     L*********************************/
9
     #include "Plat type.h"
10
11
     #define SP_top 0x020001000
12
     extern uint32 _S_DATA;
13
     extern uint32 _E_DATA;
extern uint32 _S_bss;
extern uint32 _E_bss;
14
15
16
17
     extern uint32 _E_text;
18
19
     extern int main(void);
     void Reset_Handler(void);
20
21
22
     void Default_Handler()
23 □ {
24
          Reset_Handler();
25
26
      void NMI_Handler(void)
27
                              attribute ((weak, alias("Default Handler")));
28
     void H_fault_Handler(void) __attribute__((weak,alias("Default_Handler")));
29
      void MM_fault_Handler(void) __attribute__((weak,alias("Default_Handler")));
30
     void Bus_fault_Handler(void) __attribute__((weak,alias("Default_Handler")));
     void Usage fault Handler(void) __attribute ((weak,alias("Default Handler")));
31
32
33
    Uint32 vectors[] attribute ((section(".vectors"))) ={
34
          SP_top,
35
          (uint32) & Reset Handler,
36
          (uint32) &NMI_Handler,
37
          (uint32) &H_fault_Handler,
38
          (uint32) &MM_fault_Handler,
39
          (uint32) &Bus fault Handler,
40
          (uint32) &Usage_fault_Handler
41
42
    void Reset_Handler(void) { /* copy the .data from ROM to RAM*/
43
          uint32 DATA_Size = (uint8*) & E_DATA - (uint8*) & S_DATA ;
44
45
          uint8 *P_src =(uint8*)&_E_text;
46
          uint8 *P_dst = (uint8*) &_S_DATA;
47
48
         uint32 i;
          for(i=0; i<DATA_Size;i++) {
49
50
              *((uint8*)P_dst++) = *((uint8*)P_src++);
51
          /\ast init the .bss with 0 in RAM \ast/
52
53
          uint32 bss_size = (uint8*)&_E_bss - (uint8*)&_S_bss ;
54
          P dst =(uint8*)& E bss;
55
          for(i=0; i<bss_size;i++){
56
              *((uint8*)P dst++) = (uint8)0;
57
58
59
          main();
60
61
```

7. Linker script

```
1 /*******************
      file
               : linker_script
 3 * author : Hassan Samy
4 * description : performs the linking
 5
          between all .o files and libraries
 6
          as well as making the .elf file.
 7 ********************************/
 8 MEMORY
9 {
10
       flash(RX): ORIGIN = 0x080000000 , LENGTH = 128k
11
        sram(RWX): ORIGIN = 0x20000000 , LENGTH = 20k
12
13 SECTIONS
14 {
15
        .text : {
16
          *(.vectors*)
17
           *(.text*)
18
          *(.rodata)
19
           _E_{\text{text}} = .;
20
21
      }>flash
22
23
        .data :{
24
           _S_DATA = .;
25
           *(.data)
26
           _E_DATA = .;
27
       }>sram AT>flash
28
29
       .bss :{
30
           S bss = .;
31
           *(.bss*)
32
           . = ALIGN(4);
           _E_bss = .;
33
34
           . = ALIGN(4);
35
           . = . + 0x1000;
36
          SP top = .;
37
       }>sram
38
```

Simulation by proteus





Software analysis

1. Map file

Allocating common Common symbol	symbols size	file
PS_Pval	0x4	PS_Driver.o
PS_state	0x4	PS_Driver.o
PS_state_id	0x1	main.o
Alarm_state	0x4	Alarm.o
Alarm_state_id	0x1	Alarm.o

Memory Configuration

Name	Origin	Length	Attributes
flash	0x08000000	0x00020000	xr
sram	0x20000000	0x00005000	XPW
default	0x00000000	0xffffffff	

Linker script and memory map

.text	0x08000000	0x394
(.vectors)		
.vectors	0x08000000	0x1c startup.o
	0x08000000	vectors
(.text)		
.text	0x0800001c	0xe0 Alarm.o
	0x0800001c	Pres_val
	0x08000064	ST_High_Pressure
	0x080000b0	ST_Norm_Pressure
.text	0x080000fc	0x10c driver.o
	0x080000fc	Delay
	0x08000120	getPressureVal
	0x08000138	Set Alarm actuator
	0x08000188	GPIO INITIALIZATION
.text	0x08000208	0x54 main.o
	0x08000208	setup
	0x08000238	main
.text	0x0800025c	0x7c PS_Driver.o
	0x0800025c	ST_PS_reading
	0x080002a4	ST_PS_waiting
.text	0x080002d8	0xbc startup.o
	0x080002d8	Bus_fault_Handler
	0x080002d8	MM_fault_Handler
	0x080002d8	Usage_fault_Handler
	0x080002d8	H_fault_Handler
	0x080002d8	Default_Handler
	0x080002d8	NMI Handler
	0x080002e4	Reset Handler
44 11 1		

*(.rodata)			
	0x08000394		_E_text = .
.glue_7	0x08000394	0x0	
.glue_7	0x00000000	0x0	linker stubs
.glue_7t	0x08000394	0x0	
.glue_7t	0x00000000	0x0	linker stubs
.vfp11_veneer		0x0	
.vfp11_veneer	0x00000000	0x0	linker stubs
.v4_bx	0x08000394	0x0	
.v4_bx	0x00000000	0x0	linker stubs
.iplt	0x08000394	0x0	
.iplt	0x00000000	0x0	Alarm.o
.rel.dyn	0x08000394	0x0	
.rel.iplt	0x00000000	9x9	Alarm.o
4-4-			1d -dd
.data	0x20000000	0X4	load address 0x08000394
*/	0x20000000		_S_DATA = .
*(.data)	000000000	04	11mm a
.data	0x20000000	0X4	Alarm.o
4-4-	0x20000000	00	Al_Pval
.data	0x20000004		driver.o
.data .data	0x20000004 0x20000004		main.o
.data	0x20000004 0x20000004		PS_Driver.o startup.o
·uata	0x20000004 0x20000004	676	_E_DATA = .
	0.00000004		_E_DATA = .
.igot.plt	0x20000004	ava	load address 0x08000398
.igot.plt	0x00000000		Alarm.o
.1got.p1t	0,00000000	ONO	AZUI III O
.bss	0x20000004	0x1014	load address 0x08000398
	0x20000004		S bss = .
(.bss)			
.bss	0x20000004	0x4	Alarm.o
	0x20000004		Al_Signal
.bss	0x20000008	0x0	driver.o
.bss	0x20000008	0x0	main.o
.bss	0x20000008	0x0	PS_Driver.o
.bss	0x20000008	0x0	startup.o
	0x20000008		. = ALIGN (0x4)
	0x20000008		_E_bss = .
	0x20000008		. = ALIGN (0x4)
	0x20001008		. = (. + 0x1000)
fill	0x20000008	0x1000	

```
*fill*
               0x20000008
                              0x1000
                                         SP_top = .
               0x20001008
COMMON
                                 0x5 Alarm.o
               0x20001008
               0x20001008
                                         Alarm_state
               0x2000100c
                                        Alarm_state_id
COMMON
               0x2000100d
                                 0x1 main.o
               0x2000100d
                                        PS_state_id
 *fill*
                                 0x2
               0x2000100e
COMMON
               0x20001010
                                 0x8 PS_Driver.o
               0x20001010
                                        PS_Pval
                                        PS_state
               0x20001014
LOAD Alarm.o
LOAD driver.o
LOAD main.o
LOAD PS_Driver.o
LOAD startup.o
OUTPUT(Final_Project1.elf elf32-littlearm)
.debug_info
               0x00000000
                               0x5a9
 .debug_info
               0x00000000
                              0x135 Alarm.o
 .debug_info
                              0x103 driver.o
               0x00000135
.debug_info
                             0x11d main.o
               0x00000238
.debug_info
               0x00000355
                               0xfc PS_Driver.o
 .debug_info
               0x00000451
                             0x158 startup.o
.debug_abbrev
               0x00000000
                             0x325
 .debug_abbrev 0x00000000
                               0xa5 Alarm.o
                              0x9d driver.o
 .debug_abbrev 0x000000a5
.debug_abbrev 0x00000142
                              0xa5 main.o
 .debug_abbrev 0x000001e7
                              0x7c PS_Driver.o
 .debug_abbrev 0x00000263
                              0xc2 startup.o
.debug_loc
               0x00000000
                             0x26c
 .debug_loc
               0x00000000
                               0x90 Alarm.o
 .debug loc
                               0xc8 driver.o
               0x00000090
.debug loc
                              0x58 main.o
               0x00000158
 .debug_loc
               0x000001b0
                              0x58 PS_Driver.o
               0x00000208
                               0x64 startup.o
 .debug_loc
.debug_aranges 0x00000000
                                0xa0
 .debug_aranges
               0x00000000
                                0x20 Alarm.o
 .debug_aranges
               0x00000020
                                0x20 driver.o
 .debug_aranges
               0x00000040
                                0x20 main.o
 .debug_aranges
                                0x20 PS_Driver.o
```

0x00000060

.debug_aranges				
	0x00000080	0x20	startup.o	
.debug_line	0x00000000	0x254		
.debug_line	0x00000000	0x71	Alarm.o	
.debug_line	0x00000071	0x99	driver.o	
.debug_line	0x0000010a	0x6c	main.o	
.debug line	0x00000176	0x5d	PS Driver.o	
.debug line	0x000001d3		startup.o	
-			•	
.debug str	0x00000000	0x231		
.debug str	0x00000000	0x103	Alarm.o	
		0x158	(size before	relaxing)
.debug str	0x00000103		driver.o	
			(size before	relaxing)
.debug_str	0x00000158		main.o	
100008_00.			(size before	relaxing)
.debug_str	0x0000017f		PS Driver.o	(CIUXING)
racbag_sc.	CACCOCCIT		(size before	relaxing)
.debug_str	0x000001af		startup.o	(CIUXING)
.ucbug_sci	0,00000101		(size before	relaving)
		OXIDE	(3120 001010	(Cluxing)
.comment	exeeeeeee	0x11		
.comment	0x000000000		Alarm.o	
· Commerc	0.00000000		(size before	relaving)
.comment	exeeeeeee		driver.o	(Cluxing
.comment	0x00000000		main.o	
.comment	0x00000000		PS_Driver.o	
.comment	0x00000000		startup.o	
. Commerc	0.000000000	6X12	Startup.o	
.ARM.attributes				
.AKM.actributes	000000000	0422		
and address to the	0x00000000	0x33		
.ARM.attribute		022	.1	
	0x00000000	0X33	Alarm.o	
.ARM.attribute				
	0x00000033	0X33	driver.o	
.ARM.attribute				
	0x00000066	0X33	main.o	
.ARM.attribute				
	0x00000099	0x33	PS_Driver.o	
.ARM.attribute				
	0x000000cc	0x33	startup.o	
.debug_frame	0x00000000	0x1b8		
.debug_frame			Alarm.o	
	0x00000064		driver.o	
	0x000000dc		main.o	
_	0x00000124		PS_Driver.o	
.debug_frame	0x0000016c	0x4c	startup.o	

2. Symbols table

```
Final Project1.elf:
20000008 B _E_bss
20000004 D _E_DATA
08000394 T _E_text
20000004 B _S_bss
20000000 D _S_DATA
20000000 D Al Pval
20000004 B Al Signal
20001008 B Alarm state
2000100c B Alarm_state_id
080002d8 W Bus_fault_Handler
080002d8 T Default_Handler
080000fc T Delay
08000120 T getPressureVal
08000188 T GPIO INITIALIZATION
080002d8 W H fault Handler
08000238 T main
080002d8 W MM fault Handler
080002d8 W NMI_Handler
0800001c T Pres_val
20001010 B PS_Pval
20001014 B PS state
2000100d B PS_state_id
080002e4 T Reset Handler
08000138 T Set Alarm actuator
08000208 T setup
20001008 B SP_top
08000064 T ST_High_Pressure
080000b0 T ST_Norm_Pressure
0800025c T ST_PS_reading
080002a4 T ST_PS_waiting
080002d8 W Usage_fault_Handler
08000000 T vectors
```

3. Sections table Final_Project1.elf: file format elf32-littlearm

Sect	ions:					
Idx	Name	Size	VMA	LMA	File off	Algn
0	.text	00000394	08000000	08000000	00080000	2**2
		CONTENTS,	ALLOC, LOA	AD, READONI	Y, CODE	
1	.data	00000004	20000000	08000394	00010000	2**2
		CONTENTS,	ALLOC, LOA	AD, DATA		
2	.bss	00001014	20000004	08000398	00010004	2**2
		ALLOC				
3	.debug_info	000005a9	00000000	00000000	00010004	2**0
		CONTENTS,	READONLY,	DEBUGGING		
4	.debug_abbrev	00000325	00000000	00000000	000105ad	2**0
		CONTENTS,	READONLY,	DEBUGGING		
5	.debug_loc	0000026c	00000000	00000000	000108d2	2**0
		CONTENTS,	READONLY,	DEBUGGING		
6	.debug_aranges	000000a0	00000000	00000000	00010b3e	2**0
		CONTENTS,	READONLY,	DEBUGGING		
7	.debug_line	00000256	00000000	00000000	00010bde	2**0
		CONTENTS,	READONLY,	DEBUGGING		
8	.debug_str	00000231	00000000	00000000	00010e34	2**0
		CONTENTS,	READONLY,	DEBUGGING		
9	.comment	00000011	00000000	00000000	00011065	2**0
		CONTENTS,	READONLY			
10	.ARM.attribute			00000000	00011076	5 2**0
		CONTENTS,	READONLY			
11	.debug_frame	000001b8	00000000	00000000	000110ac	2**2
		CONTENTS,	READONLY,	DEBUGGING		