

## Bit Basics

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A bit (Binary digIT) is single unit of binary storage. A bit is normally group with other bits to form a larger groupings of information.

4 bits form a **nibble**.

8 bits form a **byte** (e.g. 10100001 or 0xA1 in hex)

Bytes are grouped to form a **word**. These would normally be 1, 2, 4, or 8 bytes. The size of a word is depends on the particular processor.

**Main memory can be thought of as a large array of bytes.**

However, often data needs to be properly aligned for the processor to use it.

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## Kilobyte, Megabyte, Gigabyte

When communicating with others about amounts of memory one needs to be careful as there is a number of different standards. Which standard is used depends on the context.

| Multiples of bytes <span>V · T · E</span>                 |                  |                 |                       |                 |
|---|------------------|-----------------|-----------------------|-----------------|
| SI decimal prefixes                                       |                  | Binary usage    | IEC binary prefixes   |                 |
| Name<br>(Symbol)  | Value            |                 | Name<br>(Symbol)      | Value           |
| kilobyte (kB/KB)  | 10 <sup>3</sup>  | 2 <sup>10</sup> | kibibyte (KiB)        | 2 <sup>10</sup> |
| <b>megabyte</b> (MB)                                      | 10 <sup>6</sup>  | 2 <sup>20</sup> | <b>mebibyte</b> (MiB) | 2 <sup>20</sup> |
| gigabyte (GB)   | 10 <sup>9</sup>  | 2 <sup>30</sup> | gibibyte (GiB)        | 2 <sup>30</sup> |
| terabyte (TB)   | 10 <sup>12</sup> | 2 <sup>40</sup> | tebibyte (TiB)        | 2 <sup>40</sup> |
| petabyte (PB)   | 10 <sup>15</sup> | 2 <sup>50</sup> | pebibyte (PiB)        | 2 <sup>50</sup> |
| exabyte (EB)  | 10 <sup>18</sup> | 2 <sup>60</sup> | exbibyte (EiB)        | 2 <sup>60</sup> |
| zettabyte (ZB)  | 10 <sup>21</sup> | 2 <sup>70</sup> | zebibyte (ZiB)        | 2 <sup>70</sup> |
| yottabyte (YB)  | 10 <sup>24</sup> | 2 <sup>80</sup> | yobibyte (YiB)        | 2 <sup>80</sup> |
| See also: Multiples of bits · Orders of magnitude of data |                  |                 |                       |                 |

CCA ShareALike <http://en.wikipedia.org/wiki/Megabyte>

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## Storage on Modern Computers

My current desktop machine has:

- 6144 KiB cache
- 3.7 GiB main memory
- 11.0 GiB swap space reserved on the hard disk
- 283GiB of space on the hard disk
- CD-ROM stores around 703 MiB
- DVDs store from 4.3 to 8.0 GiM

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**Mbps** means "mega bits per second" (1Mbps = 1000000 bits per second)

**MIPS** means "million instructions per second". Often a poor measure of processor performance, as this value is depends greatly on which instructions are executed.

**FLOPS** means "floating-point operations per second".



Characters can be stored using a number. This number can look up a standard table to determine the particular character.

There are a few different standards.

**ASCII** (American Standar Code for Information Interchange) a 7 bit code which has a table of 128 characters. (see "man ascii").

**EBCDIC** (Extended Binary Coded Decimal Interchange Code) a 8 bit character encoding (used mainly on IBM mainframes).

**UTF-8** is a variable width standard that can represent Unicode characters and is backward compatible with ASCII.

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## Strings

Strings are an artifact of the programming language.

Strings are normally stored as **an array of characters with a null (or 0) terminating them.**

What does the following mean?

0x43 0x4F 0x4D 0x50 0x32 0x33 0x30 0x30 0x00

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## Endianness

**Endianness** generally refers to the **order the bytes** within a single word are stored within main memory.

A big-endian machine will store most significate byte first (lowest address order). Processor include Motorola 6800, SPARC.

A little-endian machine will store the least significate byte first (lowest address order). Processors include x86, DEC Alpha, and Atmel AVR

**Some hardware can be set so it can do either** big-endian or little-endian on particular memory segments. These include ARM, PowerPC, MIPS, and IA-64.

Generally a programmer will not care about this order as long as it is consistent. However, it becomes important when computers are connected via a network or share binary data.

Suppose we have a 32-bit machine and we stored the integer value for 10 at address 0xA0.

With a little-endian and big-endian machine we store the following:

| little-endian | big-endian |
|---------------|------------|
| A0 : 0A       | A0 : 00    |
| A1 : 00       | A1 : 00    |
| A2 : 00       | A2 : 00    |
| A3 : 00       | A3 : 0A    |