### Introduction to Operating Systems (SWE3004)

Project #2 — CPU Scheduling



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#### Xv6 Process

Process states (procstate in proc.h)

- UNUSED: Not used
- EMBRYO: Newly allocated (not ready for running yet)
- SLEEPING: Waiting for I/O, child process, or time
- RUNNABLE: Ready to run
- RUNNING: Running on CPU
- ZOMBIE: Exited

### Xv6 Process Scheduler

main() in main.c

```
17 int
18 main(void)
19 {
39  userinit();  // first user process
40  // Finish setting up this processor in mpmain.
41  mpmain();
42 }
```

mpmain() in main.c

### Xv6 Process Scheduler

- scheduler() in proc.c
  - Round-robin fashion

```
66 scheduler(void)
    struct proc *p;
    for(;;){
      // Enable interrupts on this processor.
      sti();
      // Loop over process table looking for process to run.
      acquire(&ptable.lock):
      for(p = ptable.proc: p < &ptable.proc(NPROC); p++){</pre>
         if(p->state != RUNNABLE
           continue:
        // Switch to chosen process. It is the process's job
        // to release ptable.lock and then reacquire it
        // before jumping back to us.
        proc = p;
         switchuvm(p);
        p->state = RUNNING;
         swtch(&cpu->scheduler, proc->context);
         switchkvm();
         proc = 0;
      release(&ptable.lock);
```

### Xv6 Process Scheduler

swtch() in swtch.S

```
8 .globl swtch
9 swtch:
     movl 4(%esp), %eax
10
     movl 8(%esp), %edx
11
12
     # Save old callee-save registers
13
14
     pushl %ebp
     pushl %ebx
15
     pushl %esi
16
     pushl %edi
17
18
19
     # Switch stacks
20
     movl %esp, (%eax)
21
     movl %edx, %esp
22
23
     # Load new callee-save registers
24
     popl %edi
     popl %esi
25
26
     popl %ebx
27
     popl %ebp
28
     ret
```

### Xv6 Entering Scheduler

sched() in proc.c

```
// Enter scheduler. Must hold only ptable.lock
299 // and have changed proc->state.
300 void
301 sched(void)
302 {
303
     int intena;
304
      if(!holding(&ptable.lock))
305
        panic("sched ptable.lock");
306
      if(cpu->ncli != 1)
307
        panic("sched locks");
308
      if(proc->state == RUNNING)
309
310
        panic("sched running");
311
      if(readeflags()&FL_IF)
        panic("sched interruptible");
312
      intena = cpu->intena;
313
      swtch(&proc->context, cpu->scheduler);
314
      cpu->intena = intena;
315
316
```

# Xv6 Entering Scheduler

- When?
  - 1. Exiting process (exit() in proc.c)

```
// Jump into the scheduler, never to return.
proc->state = ZOMBIE;
sched();
```

2. Sleeping process (sleep() in proc.c)

```
371  // Go to sleep.
372  proc->chan = chan;
373  proc->state = SLEEPING;
374  sched();
```

## Xv6 Entering Scheduler

- When?
  - 3. Yielding CPU due to timer interrupt
    - trap() in trap.c

```
// Force process to give up CPU on clock tick.
// If interrupts were on while locks held, would need to check nlock.
if(proc && proc->state == RUNNING && tf->trapno == T_IRQ0+IRQ_TIMER)
yield();
```

yield() in proc.c

```
318 // Give up the CPU for one scheduling round.
319 void
320 yield(void)
321 {
322   acquire(&ptable.lock); //DOC: yieldlock
323   proc->state = RUNNABLE;
324   sched();
325   release(&ptable.lock);
326 }
```

### Project Assignment #2

- Implement MLFQ scheduler on xv6
  - The lower nice value, the higher priority
  - The highest priority process is selected for next running
  - Tiebreak: round-robin fashion
  - More clarifications on the requirements will be announced later.
- Entering scheduler when
  - 1. Exiting process
  - 2. Sleeping process
  - 3. Yielding CPU
  - 4. Changing priority

## Template Code

git clone <a href="https://github.com/jinsoox/xv6-skku.git">https://github.com/jinsoox/xv6-skku.git</a>-b pa2

- Modifications
  - halt system call
    - Halt xv6 program
  - make tarball
    - Compress your source codes into one .tar.gz file for submission
    - You should enter your ID & project no. on Makefile
  - CPUS=1
  - Ignore to yield CPU on clock tick
  - yield system call
    - Yield CPU