Hardware & Software Verification

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Lecture 5: More Isabelle

Lecture Outline

Proving the correctness of a logic synthesiser.



Recursive data structures

```
datatype "circuit" =
   NOT "circuit"
| AND "circuit" "circuit"
| OR "circuit" "circuit"
| TRUE
| FALSE
| INPUT "int"
```

```
circuit ::= NOT circuit

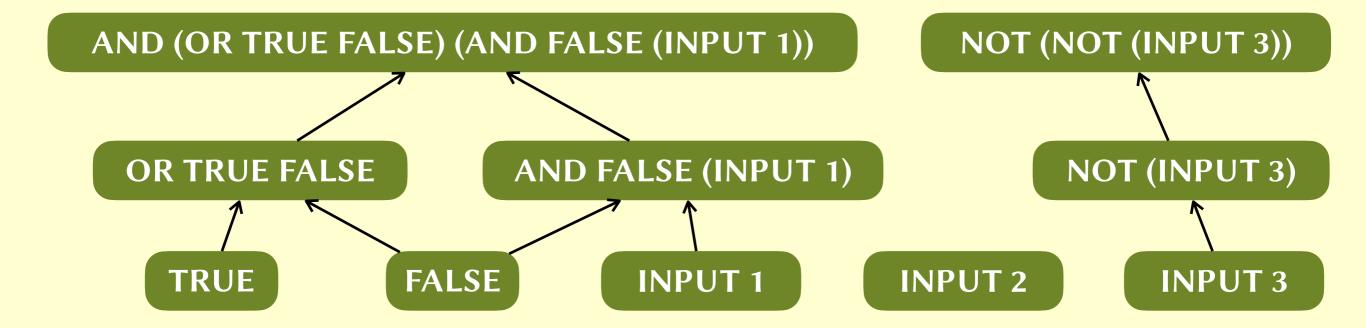
AND circuit circuit

OR circuit circuit

TRUE

FALSE

INPUT int
```

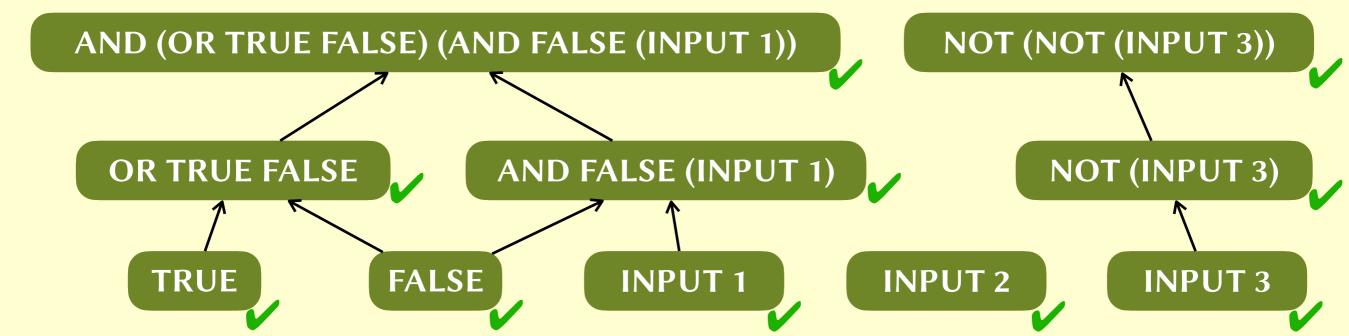




Structural induction

- Suppose we want to show that property P holds for all circuits.
- It suffices to show that each constructor preserves P.

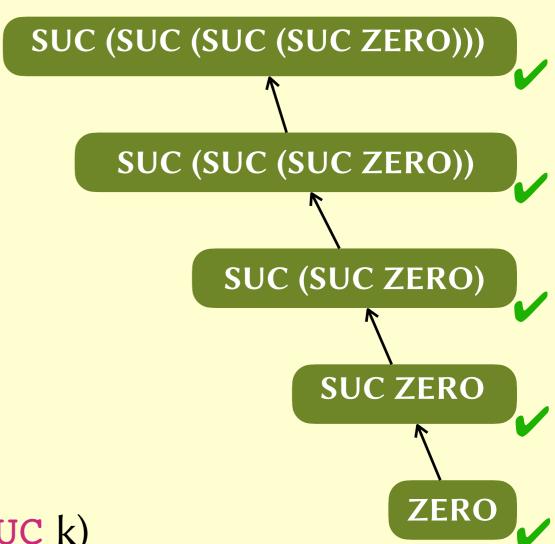
- 1. $\forall c. P(c) \Rightarrow P(\text{NOT } c)$
- 2. $\forall c_1, c_2. (P(c_1) \land P(c_2)) \Longrightarrow P(AND c_1 c_2)$
- 3. $\forall c_1,c_2. (P(c_1) \land P(c_2)) \Longrightarrow P(OR c_1 c_2)$
- 4. P(TRUE)
- 5. P(FALSE)
- **6.** ∀i. P(**INPUT** i)



Mathematical induction

```
nat ::= ZERO | SUC nat
```

- $1. \quad P(ZERO)$
- 2. $\forall k. P(k) \Rightarrow P(SUC k)$



Proof by structural induction

- **Theorem.** simulate (mirror c) ρ = simulate c ρ .
- Proof. We proceed by induction on the structure of c.

```
simulate (mirror k) ρ = simulate k ρ
as our induction hypothesis. We must prove that
simulate (mirror (NOT k)) ρ = simulate (NOT k) ρ
which we do as follows:
simulate (mirror (NOT k)) ρ
= simulate (MOT (mirror k)) ρ
= ¬ simulate (mirror k) ρ
= ¬ simulate (mirror k) ρ
= ¬ simulate k ρ
= simulate (NOT k) ρ
[ by definition of simulate ]
= ¬ simulate (NOT k) ρ
[ by definition of simulate ]
```



Rule induction

```
fun f where
  "f (Suc (Suc n)) = f / n + f (Suc n)"
| "f (Suc 0) = 1"
| "f / 0 = 1"
```

- **Theorem.** $f(n) \ge n$.
- **Proof.** Define $P(n) = (f(n) \ge n \land f(n) \ge 1)$. Rule induction here requires us to prove:
 - 1. $\forall n. (P(n) \land P(Suc n)) \Rightarrow P(Suc (Suc n))$
 - 2. P(Suc 0)
 - P(0)

Summary

- Semantics of logical implication
- Recursive data structures
- Recursive functions
- Structural induction
- Rule induction