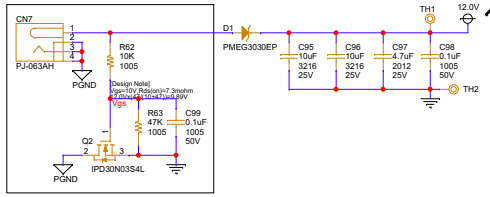
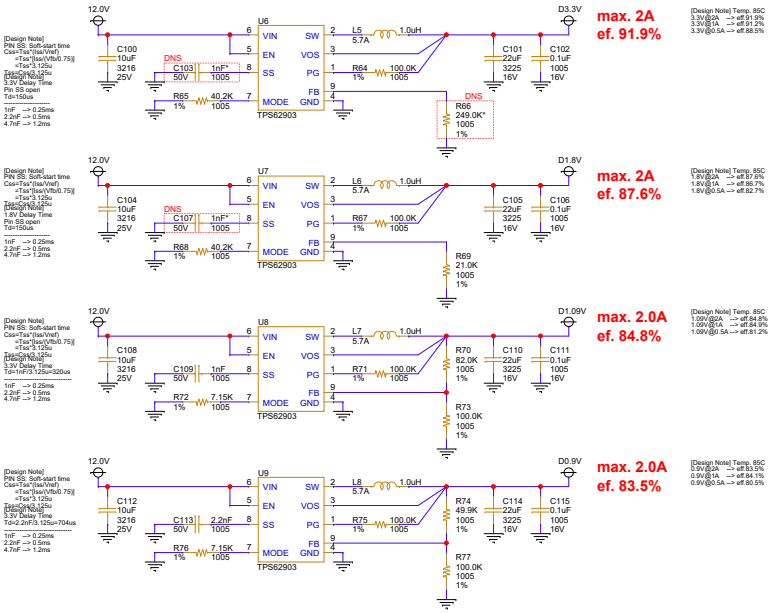
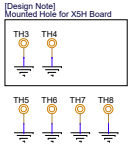
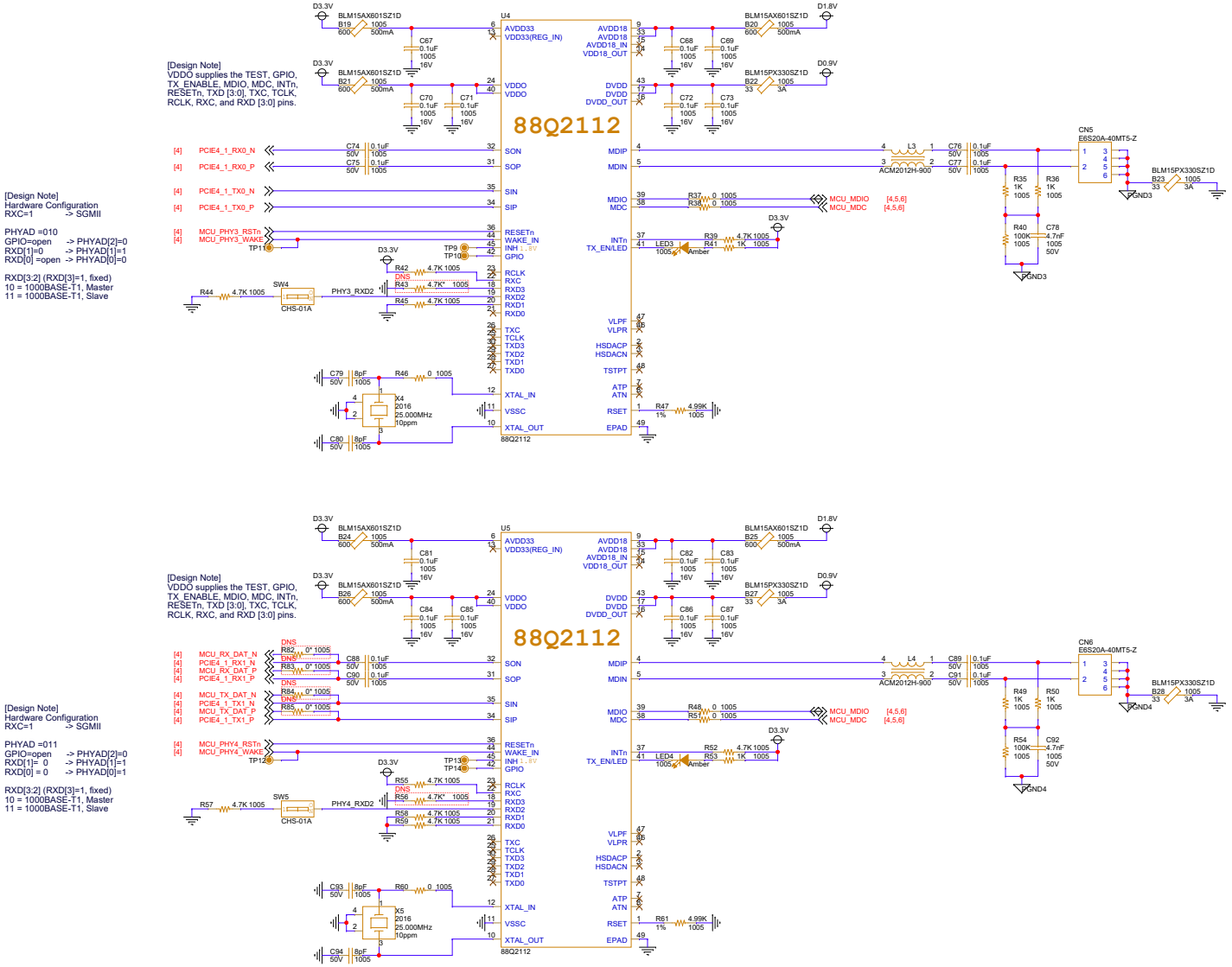
[Design Note]
DC Input may not mounted

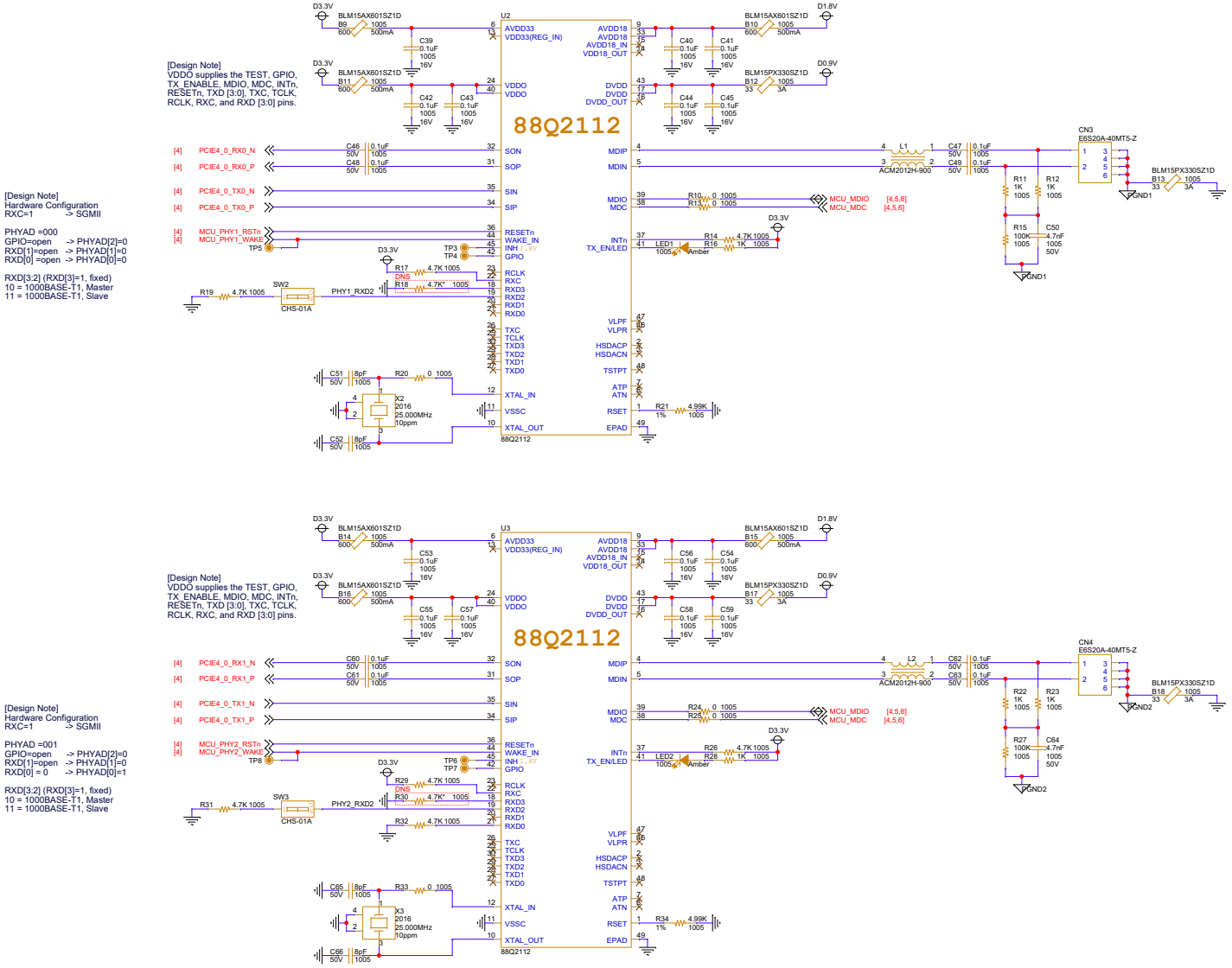




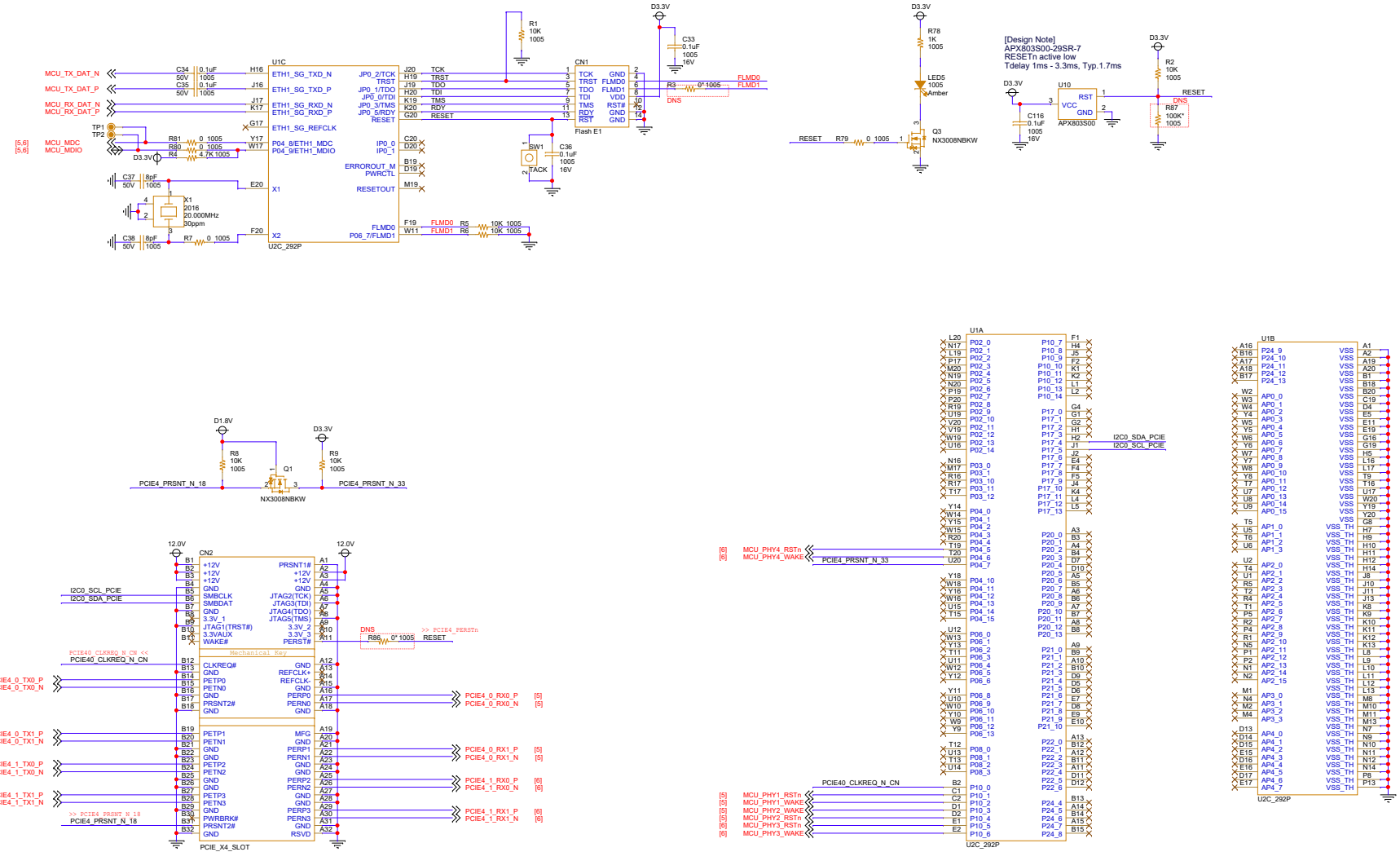
[Design Note]
Mounted Hole for X5H Board





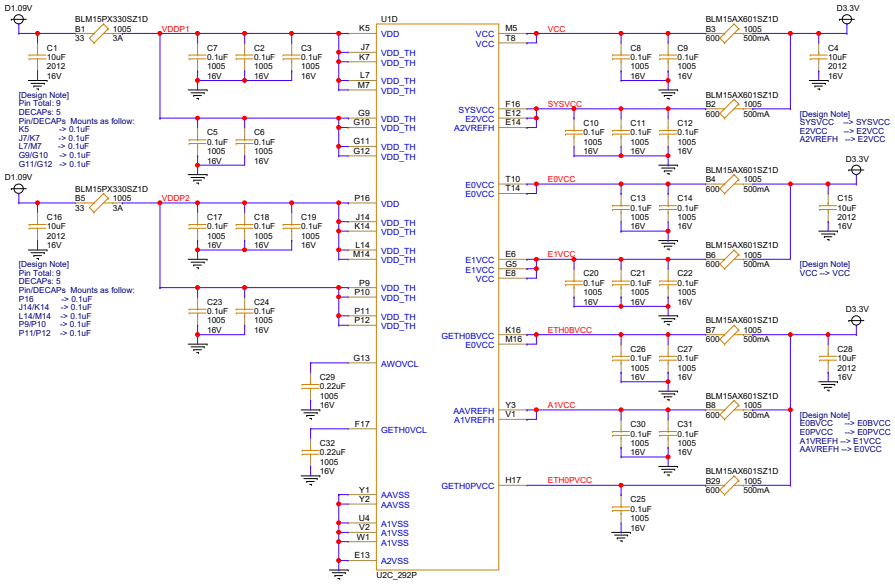


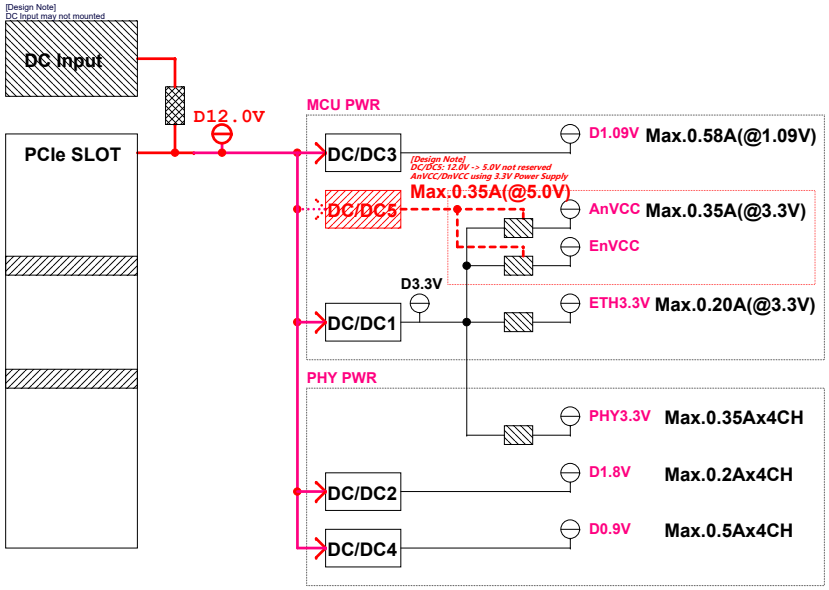
Preliminary

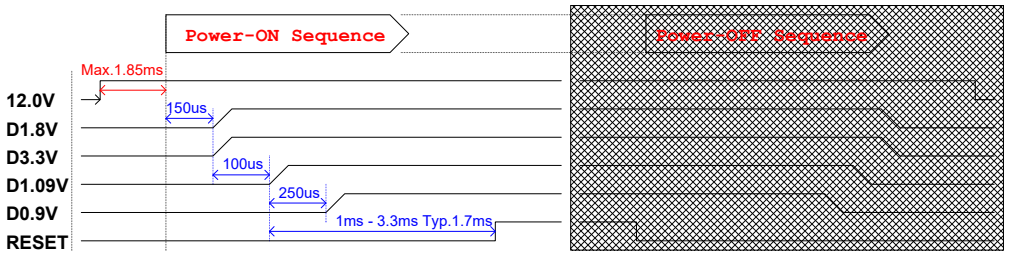


CONFIDENTIAL

Preliminary







[Design Note]
RESET Timing restrict:
U2C8 RESET after VDD(D1.09V) min.0.2ms

X5H PCIE4.0 SLOT ETH EX-Board

R7F702600AFABB-C

Rev.0.01

Preliminary