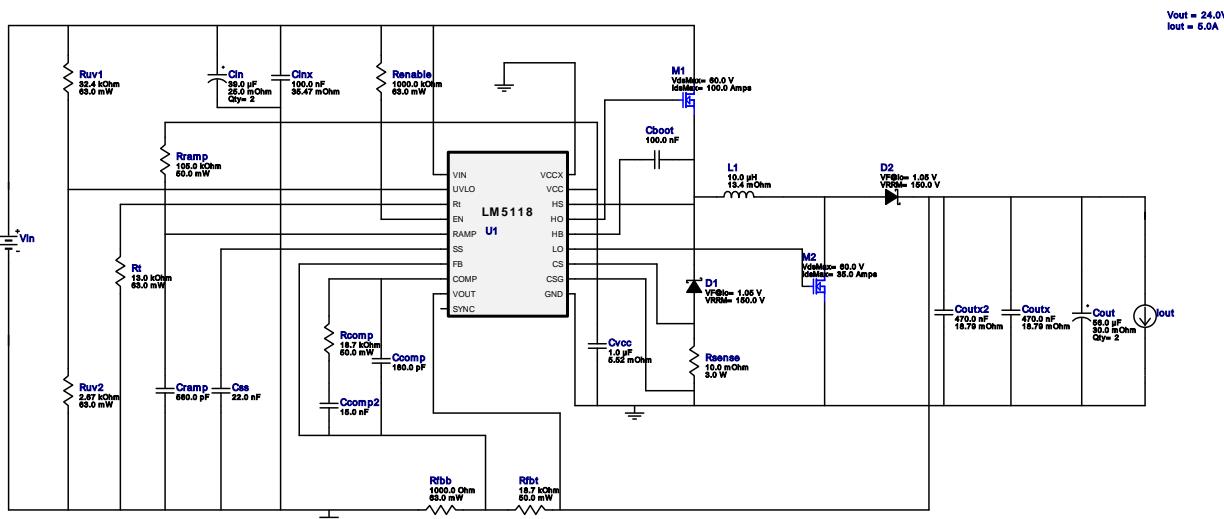


WEBENCH® Design Report

Design : 5 LM5118MH/NOPB
 LM5118MH/NOPB 20V-30V to 24.00V @ 5A

VinMin = 20.0V
 VinMax = 30.0V
 Vout = 24.0V
 Iout = 5.0A
 Device = LM5118MH/NOPB
 Topology = Buck_Boost
 Created = 2026-01-28 08:06:35.311
 BOM Cost = \$11.18
 BOM Count = 28
 Total Pd = 9.99W



Design Alerts

LM5118 Design

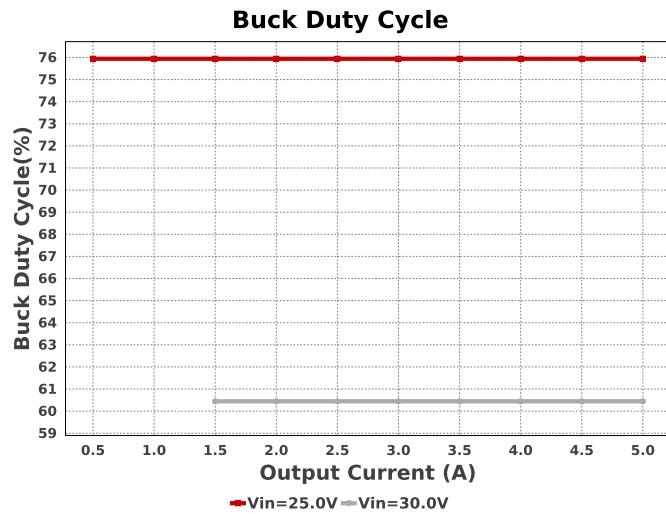
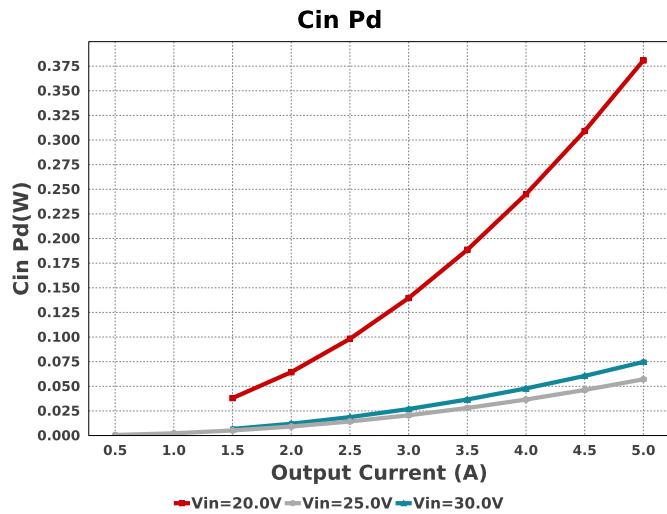
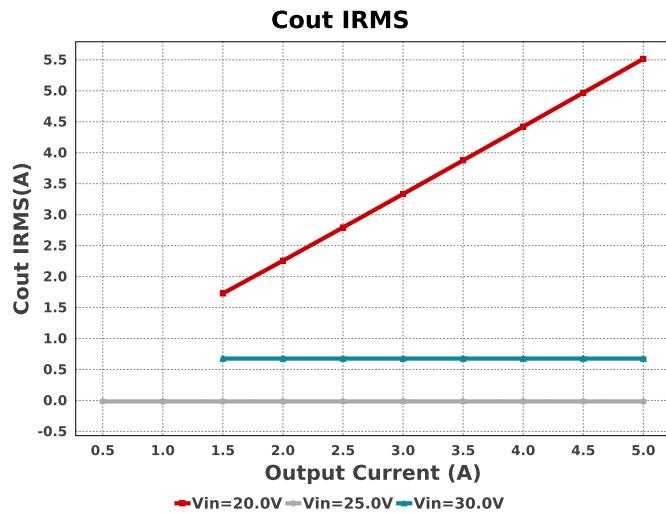
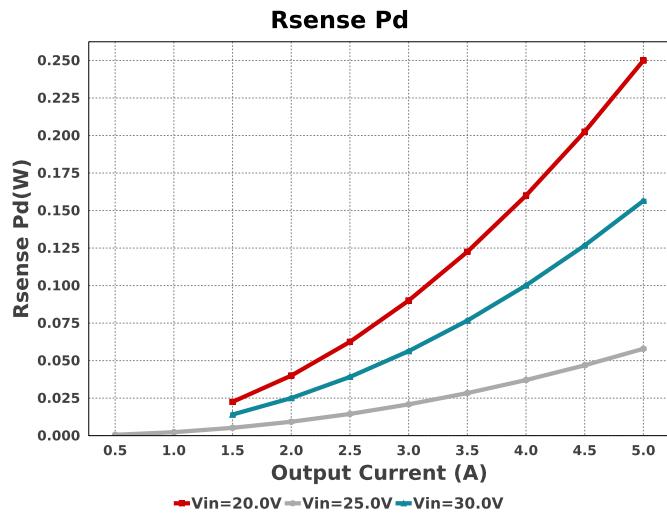
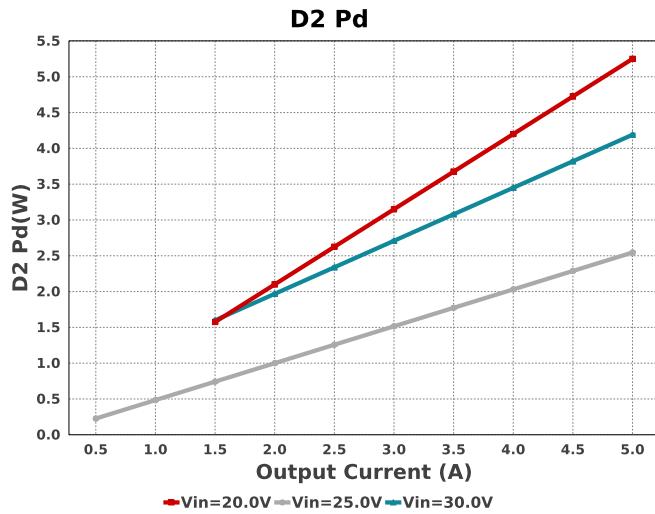
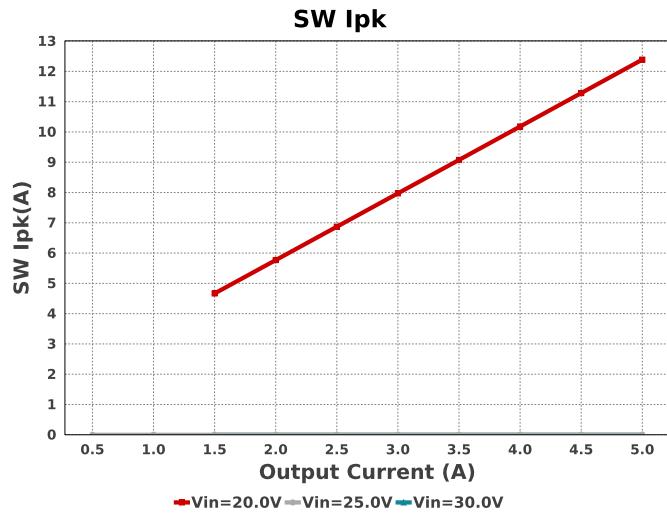
Tool Tip for Keep selected FETs during Redesign Configuration Option: By Default if you hit REDESIGN button, Webench re-designs all the external components including Fets. But if we have checked this configuration option, currently selected fets in schematic will get locked and redesign happens for only other external components. This helps to update the desing by keeping Fets unchanged.

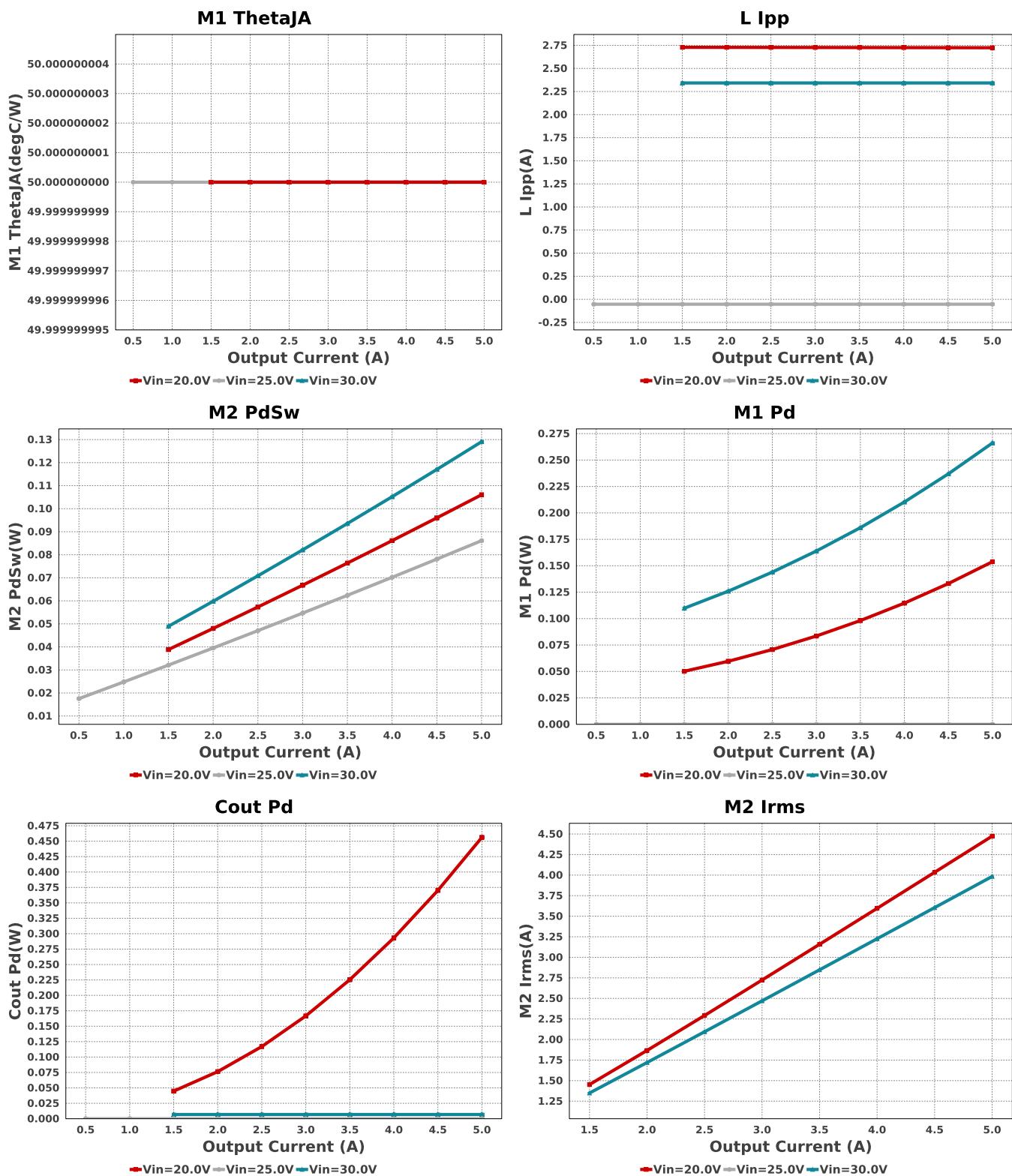
Electrical BOM

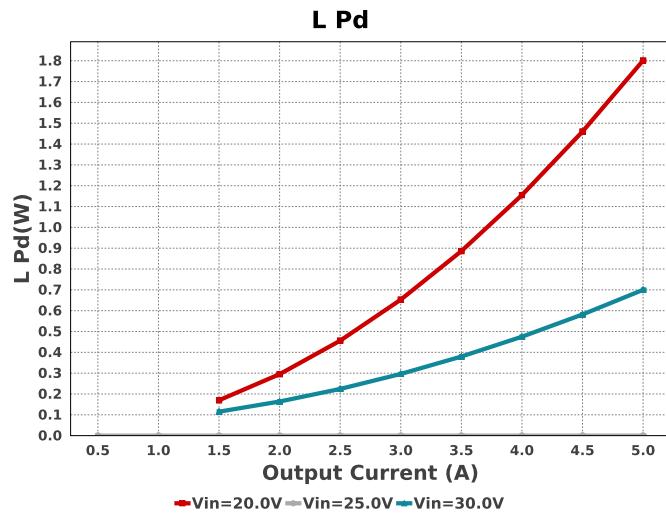
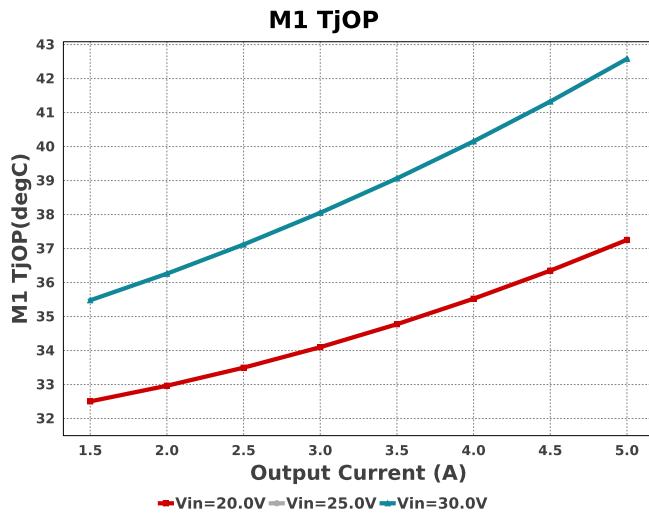
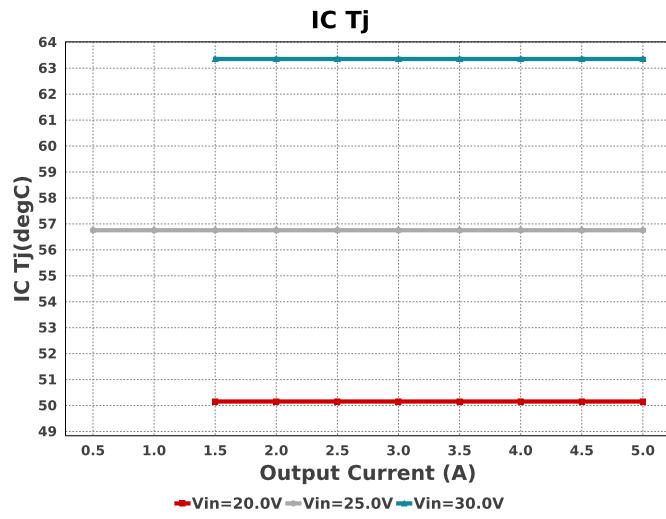
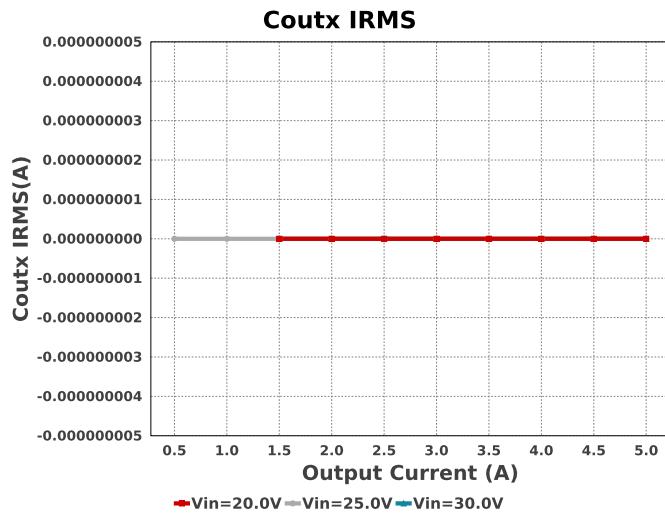
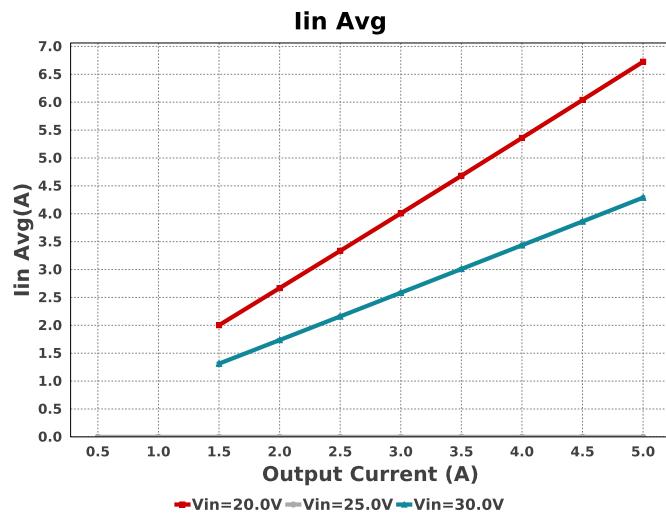
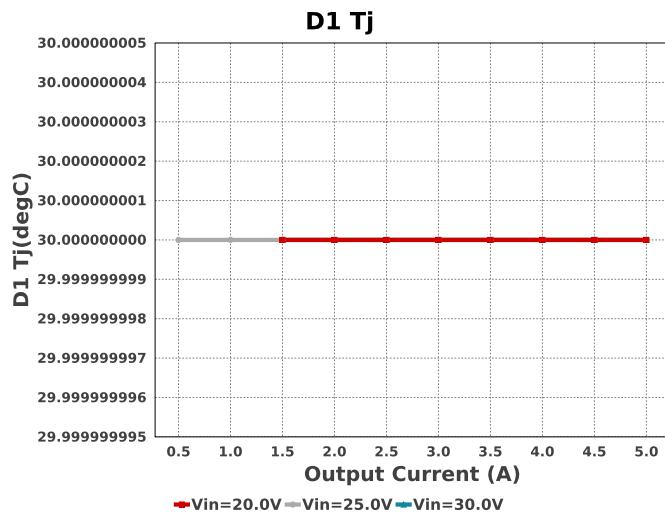
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	AVX	08053C104JAZ2A Series= X7R	Cap= 100.0 nF VDC= 25.0 V IRMS= 0.0 A	1	\$0.07	0805 7 mm ²
Ccomp	Kemet	C0805C181K5GACTU Series= C0G/NP0	Cap= 180.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
Ccomp2	TDK	C2012C0G1H153J085AA Series= C0G/NP0	Cap= 15.0 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.06	0805 7 mm ²
Cin	Panasonic	50SVPF39M Series= SVPF	Cap= 39.0 uF ESR= 25.0 mOhm VDC= 50.0 V IRMS= 3.8 A	2	\$1.15	CAPSMT_62_E12 106 mm ²
Cinx	Kemet	C0805C104M5RACTU Series= X7R	Cap= 100.0 nF ESR= 35.47 mOhm VDC= 50.0 V IRMS= 1.64 A	1	\$0.01	0805 7 mm ²
Cout	Panasonic	EEHZA1J560P Series= ZA	Cap= 56.0 uF ESR= 30.0 mOhm VDC= 63.0 V IRMS= 1.8 A	2	\$0.93	Panasonic_G 151 mm ²
Coutx	TDK	C1005X5R1V474K050BC Series= X5R	Cap= 470.0 nF ESR= 18.79 mOhm VDC= 35.0 V IRMS= 0.0 A	1	\$0.02	0402 3 mm ²

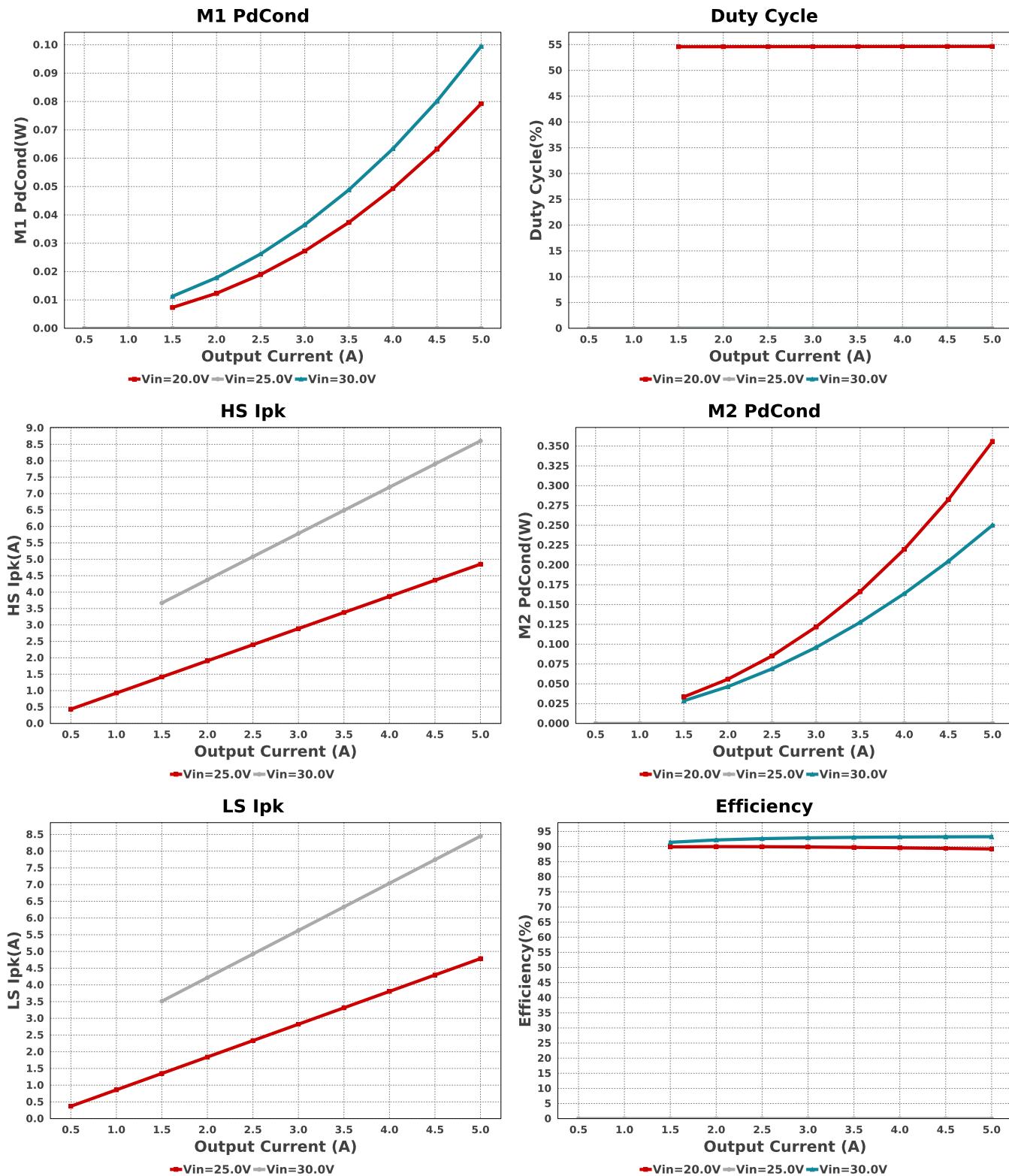
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Coutx2	TDK	C1005X5R1V474K050BC Series= X5R	Cap= 470.0 nF ESR= 18.79 mOhm VDC= 35.0 V IRMS= 0.0 A	1	\$0.02	- 0402 3 mm ²
Cramp	MuRata	GRM1555C1H561JA01J Series= C0G/NP0	Cap= 560.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	- 0402 3 mm ²
Css	TDK	C2012C0G1H223K125AA Series= C0G/NP0	Cap= 22.0 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.08	- 0805 7 mm ²
Cvcc	TDK	C1608X5R1H105K080AB Series= X5R	Cap= 1.0 uF ESR= 5.52 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.04	- 0603 5 mm ²
D1	SMC Diode Solutions	SB20150TR	VF@Io= 1.05 V VRMM= 150.0 V	1	\$0.32	 DO-201AD 166 mm ²
D2	SMC Diode Solutions	SB20150TR	VF@Io= 1.05 V VRMM= 150.0 V	1	\$0.32	 DO-201AD 166 mm ²
L1	Coilcraft	XAL1010-103MEB	L= 10.0 µH 13.4 mOhm	1	\$1.71	 XAL1010 160 mm ²
M1	Texas Instruments	CSD18540Q5B	VdsMax= 60.0 V IdsMax= 100.0 Amps	1	\$0.75	 DNK0008A 56 mm ²
M2	Texas Instruments	CSD18543Q3A	VdsMax= 60.0 V IdsMax= 35.0 Amps	1	\$0.21	 DNH0008A 18 mm ²
Rcomp	Yageo	RC0201FR-0718K7L Series= ?	Res= 18.7 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	- 0201 2 mm ²
Renable	Vishay-Dale	CRCW04021M00FKED Series= CRCW..e3	Res= 1000.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	- 0402 3 mm ²
Rfb2	Vishay-Dale	CRCW04021K00FKED Series= CRCW..e3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	- 0402 3 mm ²
Rfbt	Yageo	RC0201FR-0718K7L Series= ?	Res= 18.7 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	- 0201 2 mm ²
Rramp	Yageo	RC0201FR-07105KL Series= ?	Res= 105.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	- 0201 2 mm ²
Rsense	Bourns	CRA2512-FZ-R010ELF Series= CRA	Res= 10.0 mOhm Power= 3.0 W Tolerance= 1.0%	1	\$0.13	 2512 43 mm ²
Rt	Vishay-Dale	CRCW040213K0FKED Series= CRCW..e3	Res= 13.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	- 0402 3 mm ²
Ruv1	Vishay-Dale	CRCW040232K4FKED Series= CRCW..e3	Res= 32.4 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	- 0402 3 mm ²
Ruv2	Vishay-Dale	CRCW04022K67FKED Series= CRCW..e3	Res= 2.67 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	- 0402 3 mm ²

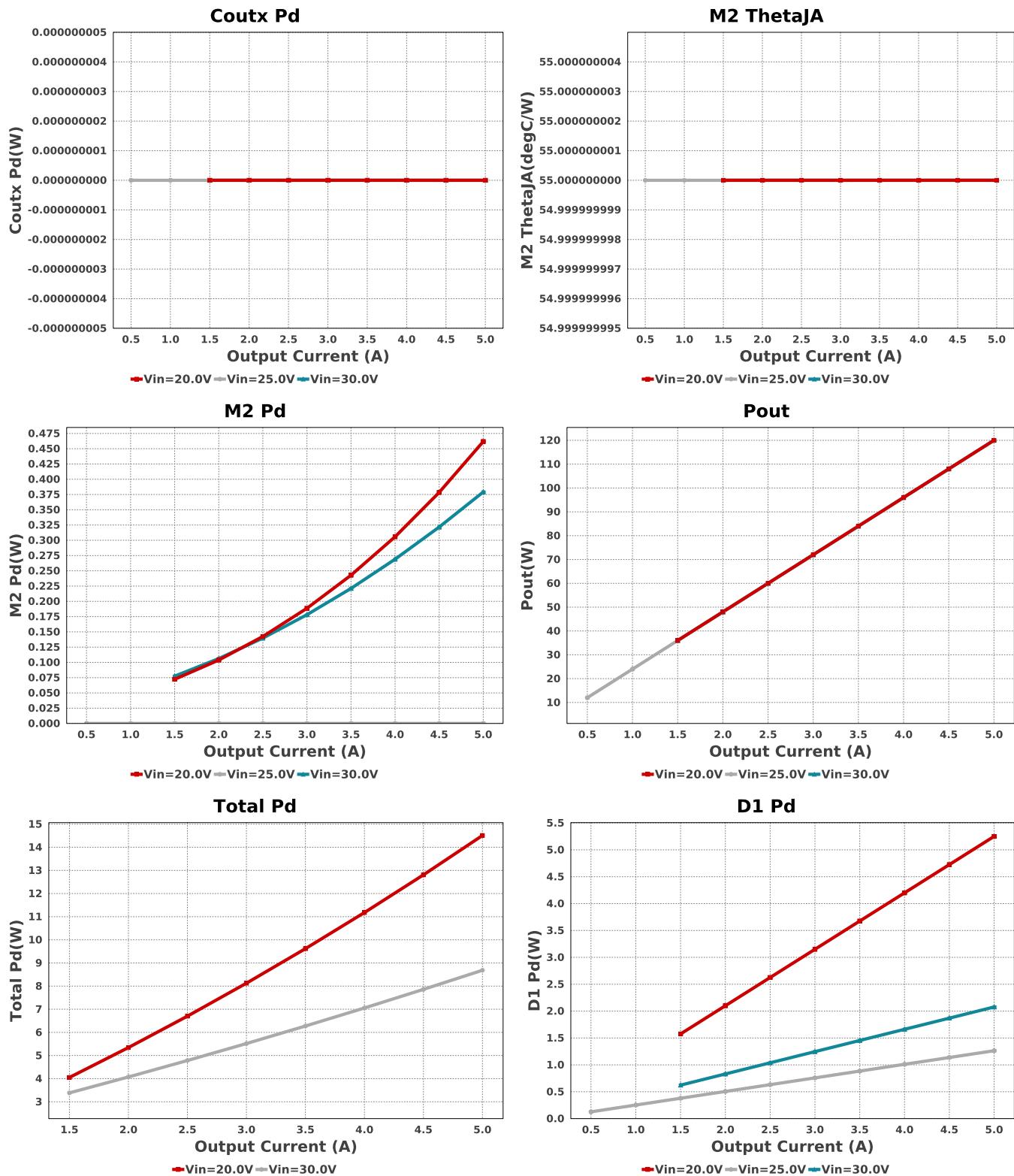
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
U1	Texas Instruments	LM5118MH/NOPB	Switcher	1	\$3.18	 MXA20A 71 mm ²

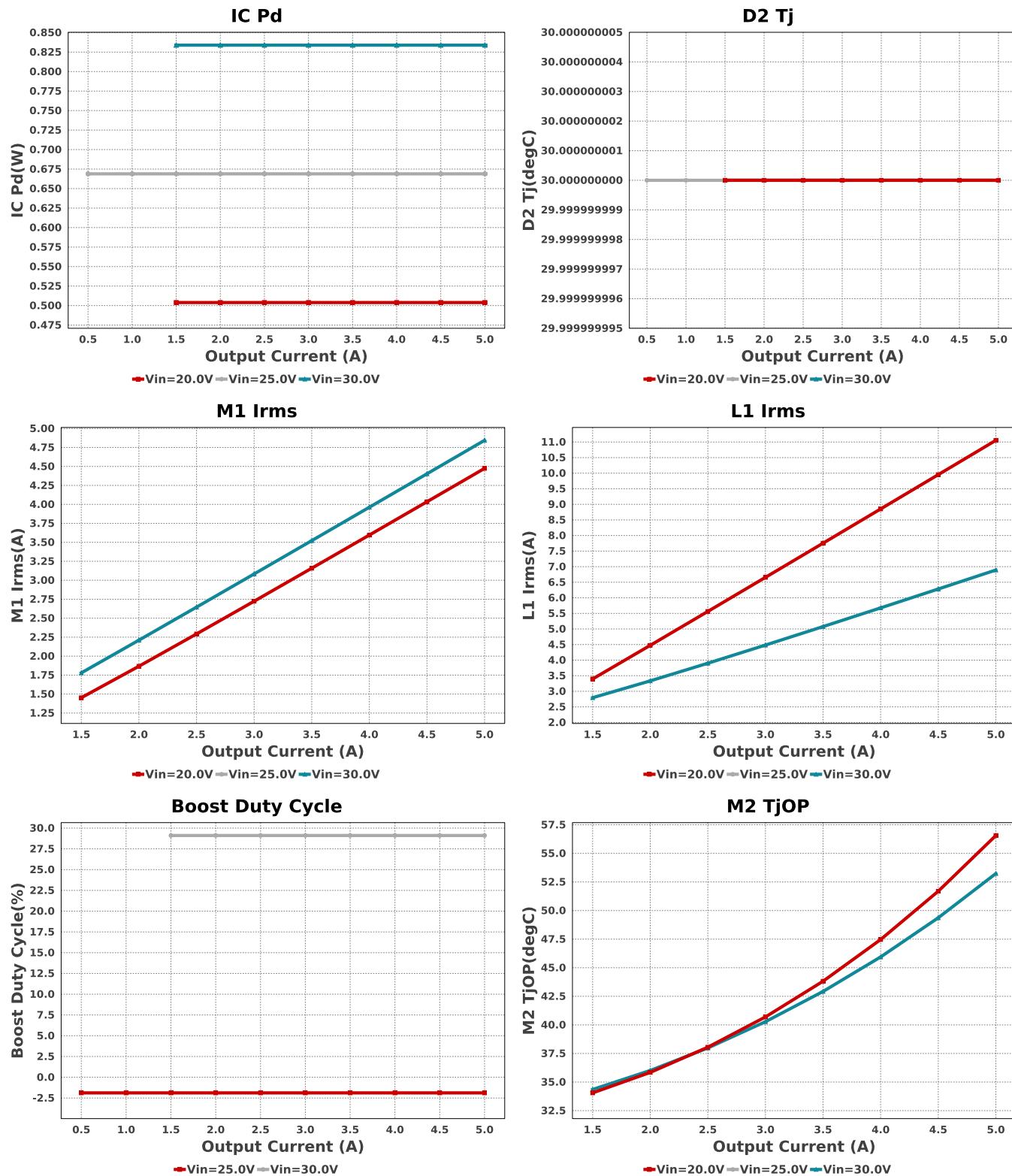


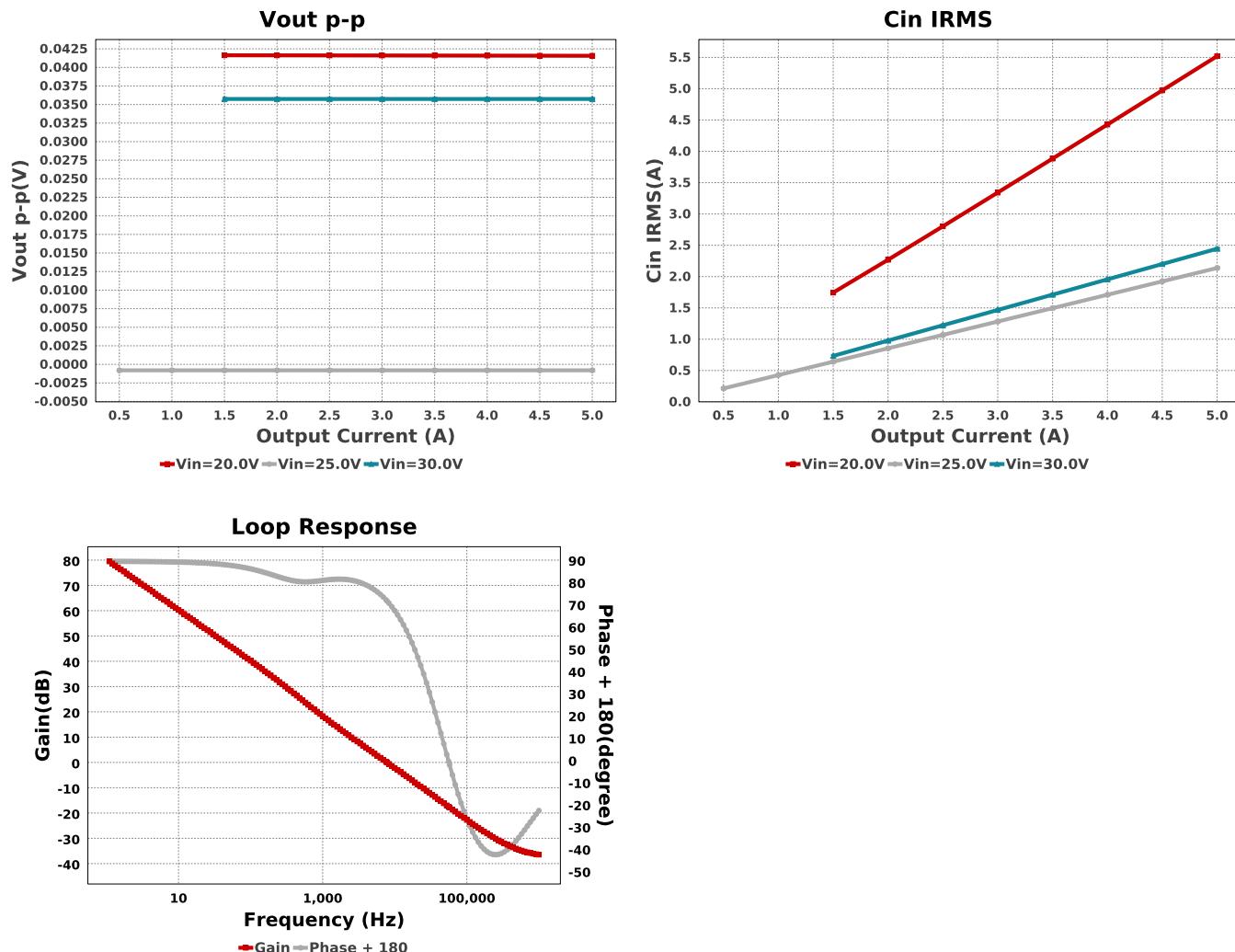












Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	5.521 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	380.96 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	5.515 A	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	456.29 mW	Capacitor	Output capacitor power dissipation
5.	Coutx IRMS	0.0 A	Capacitor	Output capacitor_x RMS ripple current
6.	Coutx Pd	0.0 W	Capacitor	Output capacitor_x power loss
7.	D1 Pd	5.25 W	Diode	Diode power dissipation
8.	D1 Tj	30.0 degC	Diode	D1 junction temperature
9.	D2 Pd	5.25 W	Diode	Diode2 power dissipation
10.	IC Pd	833.89 mW	IC	IC power dissipation
11.	IC Tj	63.356 degC	IC	IC junction temperature
12.	IC Tolerance	18.0 mV	IC	IC Feedback Tolerance
13.	ICThetaJA	40.0 degC/W	IC	IC junction-to-ambient thermal resistance
14.	Iin Avg	4.499 A	IC	Average input current
15.	L Ipp	2.724 A	Inductor	Peak-to-peak inductor ripple current
16.	L Pd	1.802 W	Inductor	Inductor power dissipation
17.	L1 Irms	11.056 A	Inductor	Inductor ripple current
18.	M1 Irms	4.474 A	Mosfet	MOSFET RMS ripple current
19.	M1 Pd	176.09 mW	Mosfet	MOSFET power dissipation
20.	M1 PdCond	101.51 mW	Mosfet	M1 MOSFET conduction losses
21.	M1 ThetaJA	50.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
22.	M1 TjOP	37.253 degC	Mosfet	MOSFET junction temperature
23.	M2 Irms	4.474 A	Mosfet	MOSFET RMS ripple current
24.	M2 Pd	583.67 mW	Mosfet	MOSFET power dissipation
25.	M2 PdCond	477.63 mW	Mosfet	M2 MOSFET conduction losses
26.	M2 PdSw	106.04 mW	Mosfet	M2 MOSFET switching losses
27.	M2 ThetaJA	55.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
28.	M2 TjOP	56.566 degC	Mosfet	MOSFET junction temperature
29.	Cin Pd	380.96 mW	Power	Input capacitor power dissipation
30.	Cout Pd	456.29 mW	Power	Output capacitor power dissipation
31.	Coutx Pd	0.0 W	Power	Output capacitor_x power loss
32.	D1 Pd	5.25 W	Power	Diode power dissipation

#	Name	Value	Category	Description
33.	D2 Pd	5.25 W	Power	Diode2 power dissipation
34.	IC Pd	833.89 mW	Power	IC power dissipation
35.	L Pd	1.802 W	Power	Inductor power dissipation
36.	M1 Pd	176.09 mW	Power	MOSFET power dissipation
37.	M1 PdCond	101.51 mW	Power	M1 MOSFET conduction losses
38.	M2 Pd	583.67 mW	Power	MOSFET power dissipation
39.	M2 PdCond	477.63 mW	Power	M2 MOSFET conduction losses
40.	M2 PdSw	106.04 mW	Power	M2 MOSFET switching losses
41.	Rsense Pd	250.0 mW	Power	LED Current Rsns Power Dissipation
42.	Total Pd	9.989 W	Power	Total Power Dissipation
43.	Rsense Pd	250.0 mW	Resistor	LED Current Rsns Power Dissipation
44.	BOM Count	28	System	Total Design BOM count
			Information	
45.	Cross Freq	7.793 kHz	System	Bode plot crossover frequency
46.	D2 Tj	30.0 degC	System	D2 junction temperature
47.	Duty Cycle	54.66 %	System	Duty cycle
48.	Efficiency	88.9 %	System	Steady state efficiency
49.	FootPrint	1.265 k mm ²	System	Total Foot Print Area of BOM components
50.	Frequency	399.501 kHz	System	Switching frequency
51.	Gain Marg	-17.558 dB	System	Bode Plot Gain Margin
52.	Iout	5.0 A	System	Iout operating point
53.	Low Freq Gain	79.499 dB	System	Gain at 1Hz
54.	Mode	CCM	System	Conduction Mode
55.	Operating Topology	Buck-Boost	System	The current operating topology of the device
56.	Phase Marg	72.21 deg	System	Bode Plot Phase Margin
57.	Pout	120.0 W	System	Total output power
58.	SW Ipk	12.39 A	System	Peak switch current
59.	Total BOM	\$11.18	System	Total BOM Cost
60.	Vin	20.0 V	System	Vin operating point
61.	Vout	24.0 V	System	Operational Output Voltage
62.	Vout Actual	24.231 V	System	Vout Actual calculated based on selected voltage divider resistors
63.	Vout Tolerance	3.409 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
64.	Vout p-p	41.559 mV	System	Peak-to-peak output ripple voltage
			Information	

Design Inputs

Name	Value	Description
Iout	5.0	Maximum Output Current
VinMax	30.0	Maximum input voltage
VinMin	20.0	Minimum input voltage
Vout	24.0	Output Voltage
base_pn	LM5118	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L_1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

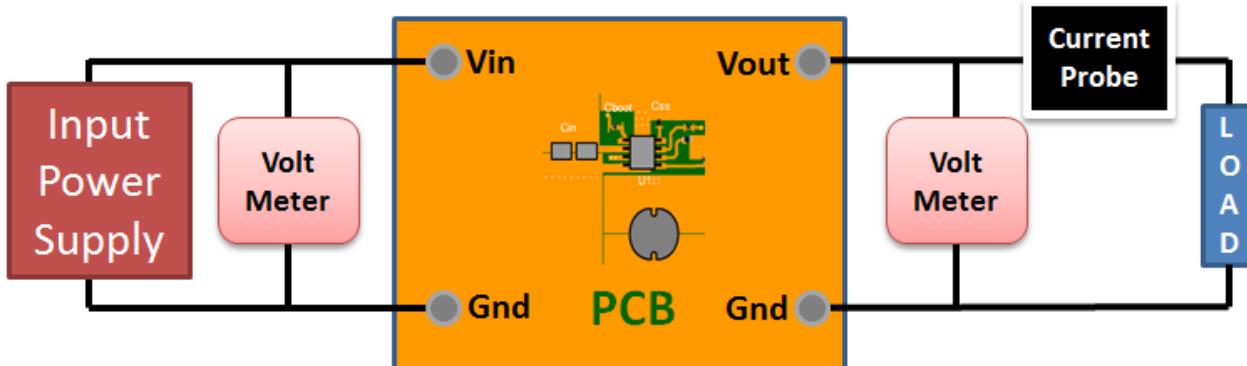
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 20.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum I_{out} of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. The LM5118 is a wide range buck-boost controller which is operable in an ultra wide input range of 3 to 75V. A buck-boost regulator can maintain regulation for input voltages either higher or lower than the output voltage. The challenge is that buck-boost power converters are not as efficient as buck regulators. The LM5118 has been designed as a dual mode controller whereby the power converter acts as a buck regulator while the input voltage is above the output. As the input voltage approaches the output voltage, a gradual transition to the buck-boost mode occurs. This gradual transition between modes eliminates disturbances at the output during transitions.
2. Master key : D9C36EA418C0A5CE9A27A54D0E582552[v1]
3. **LM5118 Product Folder** : <http://www.ti.com/product/lm5118> : contains the data sheet and other resources.

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